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FIELD MAINTENANCE PRINT SET

TABLE OF CONTENTS

UNIT VARIATIONS  
COVERED BY THIS  
PRINT SET

K-TC-DSRVB-A-0	DSRVB-A PRINT SET
K-DD-DSRVB-A-0	DEC SERVER 200/MC DRAWING DIRECTORY
E-UA-DSRVB-A-DBU	DEC SERVER 200/MC
K-PL-DSRVB-A-DBP	DEC SERVER 200/MC (PL)
E-AD-7023163-0-DBU	COVER, ASSY
K-PL-7023163-0-DBP	COVER, ASSY (PL)
E-AD-7023164-0-DBU	BASE, ASSY, DEC SERVER 200/MC
K-PL-7023164-0-DBP	BASE, ASSY, DEC SERVER 200/MC (PL)
D-IA-7023165-0-DBU	FAN ASSY
K-PL-7023165-0-DBP	FAN ASSY (PL)
K-DD-5418027-0-0	RS232 I/O BOARD
E-UA-5418027-0-0	RS232 I/O BOARD UNIT ASSY
K-PL-5418027-0-DBP	RS232 I/O BOARD PARTS LIST
K-BD-5418027-0-ESD1	RS232 I/O BOARD BLOCK DIAGRAM
K-CS-5418027-0-ESD2	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD3	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD4	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD5	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD6	RS232 I/O BOARD SCHEMATIC
K-CS-5418027-0-ESD7	RS232 I/O BOARD SCHEMATIC
D-EC-5018026-0-0	ETCH CUT DRAWING
K-DD-5418034-0-0	DEC SERVER 200
E-UA-5418034-0-0	DEC SERVER 200 UNIT ASSY
K-PL-5418034-0-DBP	DEC SERVER 200 PARTS LIST
K-BD-5418034-0-B1	DEC SERVER 200 BLOCK DIAGRAM
K-BD-5418034-0-B2	DEC SERVER 200 BLOCK DIAGRAM
K-BD-5418034-0-B3	DEC SERVER 200 BLOCK DIAGRAM
K-BD-5418034-0-CPU1	DEC SERVER 200 BLOCK DIAGRAM
K-CS-5418034-0-CPU2	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CPU3	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CPU4	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CPU5	DEC SERVER 200 SCHEMATIC
K-CS-5418034-0-CPU6	DEC SERVER 200 SCHEMATIC

DSRVB-42  
DSRVB-AA  
DSRVB-AB

FIELD MAINTENANCE PRINT SET

DIGITAL EQUIPMENT CORPORATION

PRINT SET ORDER NO

MP-01827-01 B01

REVISION HISTORY

CONT. REVISION HISTORY

MADE BY : L. REED  
DATE : 13-OCT-87

REV	ECO NUMBER	DATE	REV	ECO NUMBER	DATE
C	DSRVBA-MK002	13-OCT-87	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-
-	-	-	-	-	-

CHECKED BY : F. CARBONE  
DATE : 13-OCT-87

PROJECT ENGR : M. LAWERENCE  
DATE : 13-OCT-87

RSPBLE ENGR : M. LAWERENCE  
DATE : 13-OCT-87

FIELD SERVICE : A. RIVERA  
DATE : 13-OCT-87

d i g i t a l

TITLE: FIELD MAINTENANCE PRINT SET

DSRVB-A  
PRINT SET

SIZE | CODE | DOCUMENT NUMBER | REV.  
K | TC | DSRVB-A-0 | C

SHEET 1 OF 3 | EDIT : 2

TC FILENAME: DSRVBAC1.TCF

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FIELD MAINTENANCE PRINT SET

UNIT VARIATIONS  
COVERED BY THIS  
PRINT SET

DSRVB-A2  
DSRVB-AA  
DSRVB-AB

MP-01827-01 B01

d i g i t a l

TITLE: FIELD MAINTENANCE PRINT SET

DSRVB-A  
PRINT SET

SIZE	CODE	DOCUMENT NUMBER	REV
K	TC	DSRVB-A-0	0

SHEET 2 OF 3 | EDIT : 3

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FIELD MAINTENANCE PRINT SET

TABLE OF CONTENTS

K-CS-5418034-O-NET5	DECSERVER 200 SCHEMATIC
K-CS-5418034-O-PWR	DECSERVER 200 SCHEMATIC
K-CS-5418034-O-CAP1	DECSERVER 200 SCHEMATIC
K-CS-5418034-O-CAP2	DECSERVER 200 SCHEMATIC
K-CS-5418034-O-D1	DECSERVER 200 SCHEMATIC
K-CS-5418034-O-D2	DECSERVER 200 SCHEMATIC
K-CS-5418034-O-D3	DECSERVER 200 SCHEMATIC
E-EC-5018033-O-0	ETCH CUT DRAWING

UNIT VARIATIONS  
COVERED BY THIS  
PRINT SET

DSRVB-A2  
DSRVB-AA  
DSRVB-AB

FIELD MAINTENANCE PRINT SET

DIGITAL EQUIPMENT CORPORATION

PRINT SET ORDER NO

MP-01827-01 B01

REVISION HISTORY

CONT. REVISION HISTORY

MADE BY : L. REED  
DATE : 13-OCT-87

REV	ECO NUMBER	DATE	REV	ECO NUMBER	DATE
-----	------------	------	-----	------------	------

A	INITIAL	-	-		-
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CHECKED BY : F. CARBONE  
DATE : 13-OCT-87

PROJECT ENGR : M. LAWERENCE  
DATE : 13-OCT-87

RSPBLE ENGR : M. LAWERENCE  
DATE : 13-OCT-87

FIELD SERVICE : A. RIVERA  
DATE : 13-OCT-87



TITLE: FIELD MAINTENANCE PRINT SET

DSRVB-A  
PRINT SET

SIZE	CODE	DOCUMENT NUMBER	REV.
K	TC	DSRVB-A-0	C

SHEET 3 OF 3 EDIT :

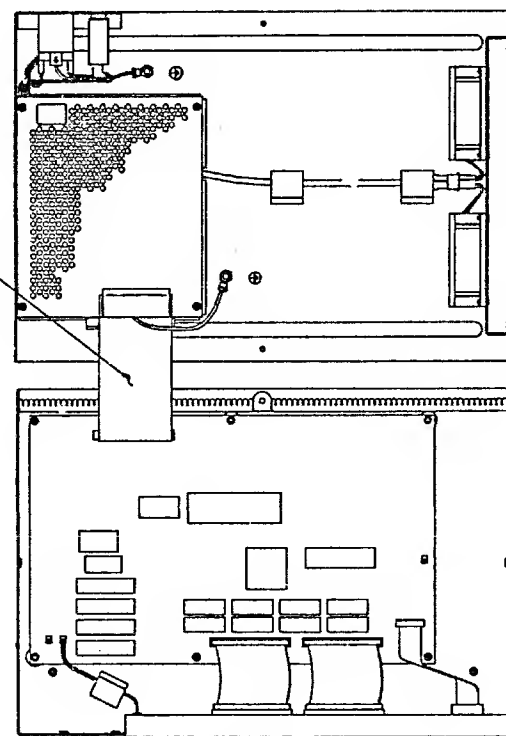


DESCRIPTION		DRAWING NO.		PART NO.		TUS NO.	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 1114)							
HOLE TOLERANCE	HOLE POS. TOL	SURFACE QUALITY	DIMENSION RANGE IN INCHES				
			OVER 13.0	OVER 12.0	OVER 10.0	OVER 8.0	OVER 6.0
±.01	±.01	✓	±.004	±.006	±.008	±.010	±.015
±.01	±.01						
±.01	±.01						
QUANTITY & VARIATION							
THIRD ANGLE PROJECTION			DATE 01 APR 86		TITLE		
			E. TENANDER				
DO NOT SCALE DRAWING			DATE 13 JUN 86		DECSERVER		
REMOVE DIM'S AND BREAK SHARP CORNERS			DATE 13 JUN 86		200/MC		
MATERIAL			DATE 13 JUN 86		DOCUMENT NUMBER		
					SHEET		

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J  
H  
G  
F  
E  
D  
C  
B  
A

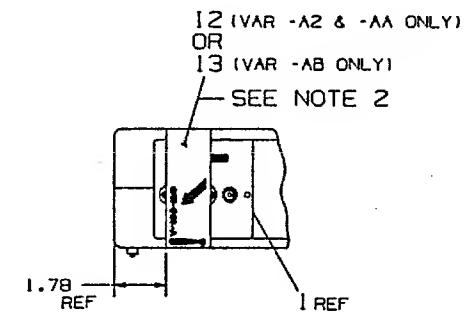
NOTE CONNECTION OF  
DC POWER CABLE FROM  
ITEM 1 OR 19  
(POWER SUPPLY)  
TO ITEM 2 (RS232)



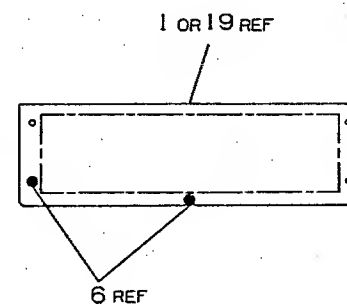
TOP VIEW WITH ITEMS  
3 & 4 REMOVED AND  
ITEM 1 FLIPPED OPEN

1 OR 19 REF

2 REF



VIEW A-A



VIEW B-B  
VIEW SHOWN WITH ITEMS  
3 & 4 REMOVED TO SHOW  
LOCATIONS OF ITEM 6

J  
H  
G  
F  
E  
D  
C  
B  
A

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION		
						A2 B2	AA B2	AB B2
1	1	E-AD-7023163-0-DBU	70-23163-01		COVER, ASSY.	REF	REF	REF
2	2	E-AD-7023164-0-DBU	70-23164-01		BASE ASSY, DEC SERVER 200/MC	1	1	1
3	3	E-IA-7432072-0-DBU	74-32072-01		COVER, CORPORATE BOX	1	1	1
4	4	E-IA-7432072-0-DBU	74-32072-02		BASE, CORPORATE BOX	1	1	1
5	5	A-PS-1224506-0-0	12-24506-02		FOOT, RUBBER .38THK	4	4	4
6	6		90-00049-22		SCREW, SEMS PAN PHIL 6-32	7	7	7
7	7		90-09749-00		SCREW, MACH FLAT PHIL 6-32	2	2	2
8	8		90-08411-01		SCREW, MACH RND PHIL 8-32	4	4	4
9	9		90-10718-01		SCREW, SHLD PHIL 6-32	4	4	4
10	10	A-PS-3616786-0-0	36-16786-01		LABEL, ECO STATUS FOUR PART	2	2	2
11	11	A-PS-3624044-0-0	36-24044-01		LABEL, ETHERNET ADDRESS	1	1	1
12	12	A-PS-3617905-0-0	36-17905-16		LABEL, WARNING EQUIP. RATING, 100-12	1	1	-
13	13	A-PS-3617905-0-0	36-17905-17		LABEL, WARNING EQUIP. RATING, 220-24	-	-	1
14	14	A-PS-3625985-0-0	36-25985-01		LABEL, BOTTOM COMMON BOX DSRVB-A	2	2	2
15	15	A-PS-3700805-0-0	37-00805-06		PKG COMM SERVER DSRVB/DSRVC CUS.	1	1	1
16	16	K-PL-DSRVB-0-DBP	DSRVB-KA		DSRVB-AA, -BA KIT FOR US, CANADA (FRE	-	1	-
17	17	A-PS-3626516-0-0	36-26516-02		LABEL, BAR CODE PLOYESTER 4 UP	1	1	1
18	18	A-PS-1215336-0-0	12-15336-08		CONN, D SUB 25POS SE	-	1	1
19	20	E-AD-7023163-0-DBU	70-23163-02		COVER ASSEMBLY	1	1	1
20	21		QLZ06-UZ		DECSVR 200 VMS LIC W/WARR	-	REF	REF
21	22		QLZ14-UZ		DECSVR 200 ULTRIX LIC W/WARR	-	REF	REF

1 GEN: NOTE: 1. REFER TO K-PL-DSRVB-0-DBP FOR COUNTRY KITS.

2 GEN: NOTE: 2. -A2 IS FOR NORTH AMERICAN FIELD SERVICE SPARES ONLY.

3 GEN: NOTE: 3. ITEM #18 NOT SHOWN ON UNIT, BUT INSERTED SEPERATELY IN SHIPPING CONTAINER.

4 GEN: IF ITEM #19 (70-23163-02) IS NOT AVAILABLE, ITEM #1 (70-23163-01) CAN BE SUBSTITUTED.

REVISION HISTORY			KPL MATRIX FORMAT		SECTION A OF A		DRN: E. TENANDER		D I G I T A L				
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 02-APR-86		CHK'D: BOURGAULT/WM		TITLE PARTS LIST				
---	INITIAL	A	[AI] A2, AA, AB		DATE: 13-JUN-86		DATE: 13-JUN-86		DESERVER 200/MC				
WM	DSRVB-A-LK001	B	[BI]				DES.ENG: W. MCCARTHY		DOCUMENT NUMBER				
ML	DSRVB-A-MK002	C	[CI]				DATE: 13-JUN-86		SIZE CODE NUMBER REV				
ML	DSRVB-A-MK003	D	[DI]				RESP.ENG.: A. PARIKH		K PL DSRVB-A-DBP D				
			[EI]				DATE: 13-JUN-86		RELEASE DATE: 15-FEB-88				
			[FI]				MFG.ENG: J. SEQUINOT		RELEASE STATUS: RELEASED				
			[HI]				DATE: 13-JUN-86						
			[JI]										
			BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:				
			DSRVB		E-UA-DSRVB-A-DBU		K-DD-DSRVB-A-0		DSRVBAD.PLS				
									EDIT #				
									1				

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LEGEND		
PART NO	REV	VARIATION
70-23163-01	B1	AS SHOWN, SHTS 1 & 2
70-23163-02	A1	AS SHOWN, SHTS 3 & 4

- NOTES:
1. INSTALL LABEL (ITEM 17) AFTER ITEMS 4, 5, & 6 ARE INSTALLED.
  2. LOCATE CABLE CLAMP (ITEM 18) APPROXIMATELY WHERE SHOWN.
  3. LOCATE LABELS (ITEMS 8 & 20, FOR -01 VARIATION, ITEM 8 FOR -02 VARIATION) APPROXIMATELY WHERE SHOWN.
  4. CAUTION:: DO NOT PINCH ANY WIRES UNDER POWER SUPPLY COVER (ITEM 22).

VARIATION -01

- LAYER 1: WORK LAYER (SHT 1)  
LAYER 2: FORMAT LAYER (SHT 1)  
LAYER 3: WORK LAYER (SHT 2)  
LAYER 4: FORMAT LAYER (SHT 2)  
LAYER 5: WORK LAYER (SHT 3)  
LAYER 6: FORMAT LAYER (SHT 3)  
LAYER 7: WORK LAYER (SHT 4)  
LAYER 8: FORMAT LAYER (SHT 4)

PLOT AT: 1:00

CAUTION: OFF SHEET PARTS LIST EXISTS  
SEE K-PL-7023163-0-DBP

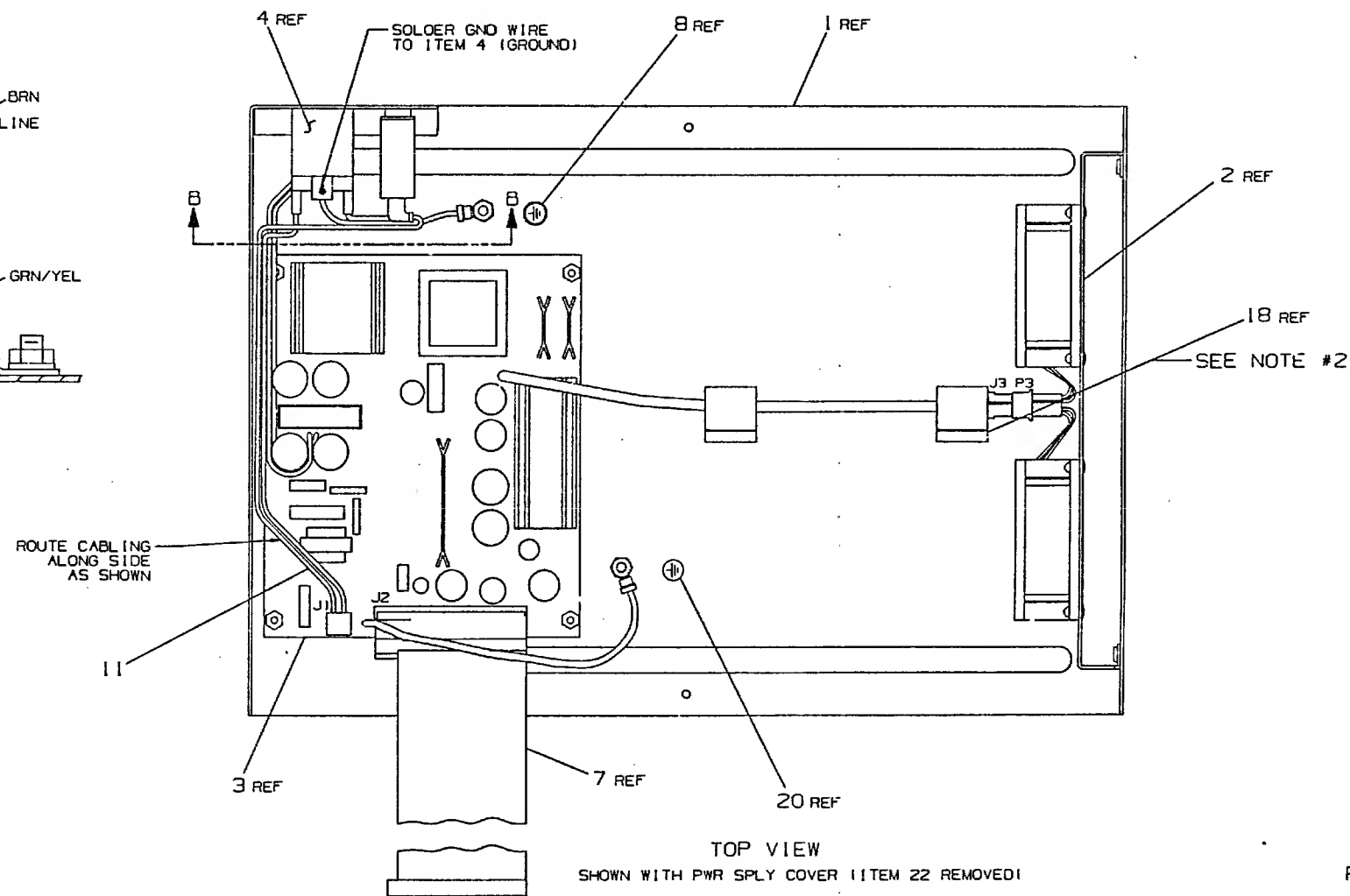
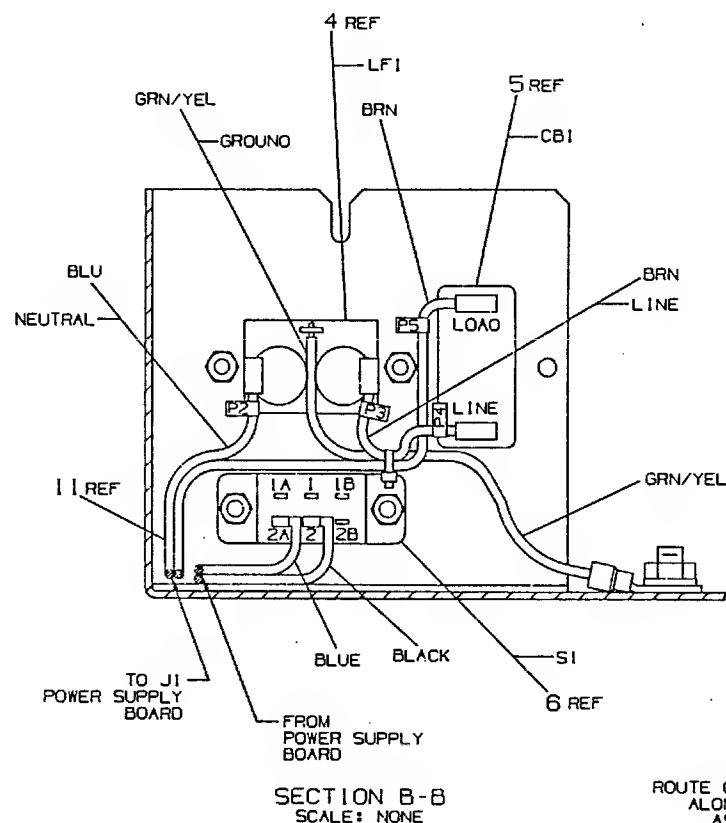
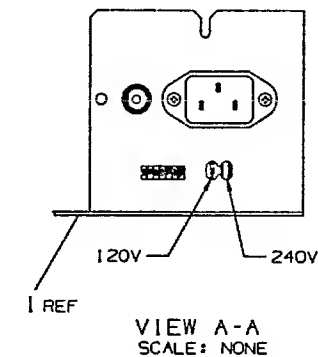
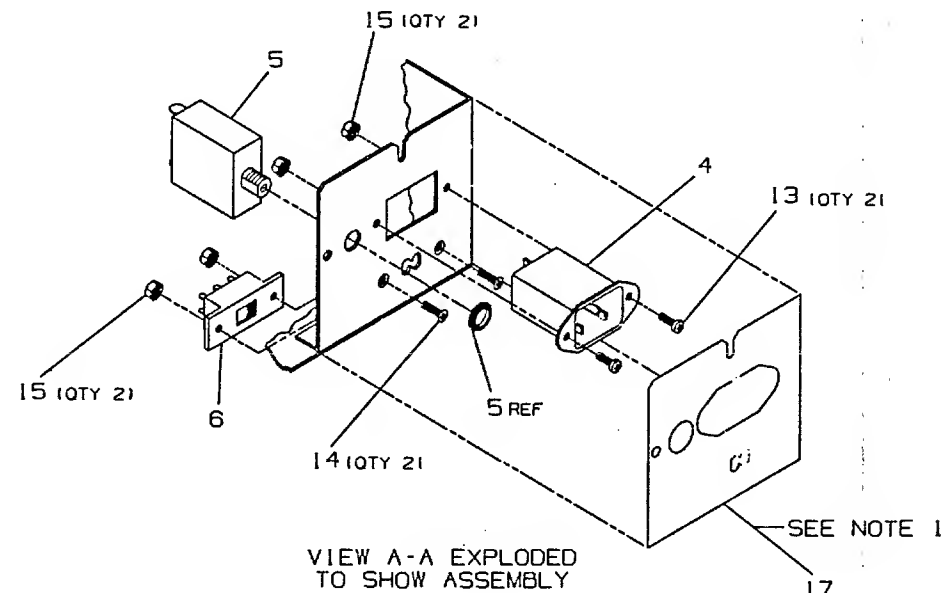
REV	DATE	BY	CHKD	APP'D
1	INITIAL	A		
2	INITIAL	B		
3	INITIAL	C		
4	INITIAL	D		
5	INITIAL	E		
6	INITIAL	F		
7	INITIAL	G		
8	INITIAL	H		
9	INITIAL	I		
10	INITIAL	J		
11	INITIAL	K		
12	INITIAL	L		
13	INITIAL	M		
14	INITIAL	N		
15	INITIAL	O		
16	INITIAL	P		
17	INITIAL	Q		
18	INITIAL	R		
19	INITIAL	S		
20	INITIAL	T		
21	INITIAL	U		
22	INITIAL	V		
23	INITIAL	W		
24	INITIAL	X		
25	INITIAL	Y		
26	INITIAL	Z		

		DESCRIPTION		DRAWING NO.		PART NO.		REV.	
		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)							
		INCHES		FRACTIONS		DIMENSION RANGE IN INCHES		TOLERANCE	
		.000 - .009		1/16		.001			
		.010 - .049		1/32		.002			
		.050 - .099		1/16		.003			
		.100 - .199		1/16		.004			
		.200 - .299		1/16		.005			
		.300 - .399		1/16		.006			
		.400 - .499		1/16		.007			
		.500 - .599		1/16		.008			
		.600 - .699		1/16		.009			
		.700 - .799		1/16		.010			
		.800 - .899		1/16		.011			
		.900 - .999		1/16		.012			
		1.000 - 1.999		1/16		.015			
		2.000 - 2.999		1/16		.020			
		3.000 - 3.999		1/16		.025			
		4.000 - 4.999		1/16		.030			
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		7.000 - 7.999		1/16		.045			
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		10.000 - 10.999		1/16		.060			
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		12.000 - 12.999		1/16		.070			
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		102.000 - 102.999		1/16		.520			
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		104.000 - 104.999		1/16		.530			
		105.000 - 105.999		1/16		.535			
		106.000 - 106.999		1/16		.540			
		107.000 - 107.999		1/16		.545			
		108.000 - 108.999		1/16		.550			
		109.000 - 109.999		1/16		.555			
		110.000 - 110.999		1/16		.560			
		111.000 - 111.999		1/16		.565			
		112.000 - 112.999		1/16		.570			
		113.000 - 113.999		1/16		.575			
		114.000 - 114.999		1/16		.580			
		115.000 - 115.999		1/16		.585			
		116.000 - 116.999		1/16		.590			
		117.000 - 117.999		1/16		.595			
		118.000 - 118.999		1/16		.600			
		119.000 - 119.999		1/16		.605			
		120.000 - 120.999		1/16		.610			
		121.000 - 121.999		1/16		.615			
		122.000 - 122.999		1/16		.620			
		123.000 - 123.999		1/16		.625			
		124.000 - 124.999		1/16		.630			
		125.000 - 125.999		1/16		.635			
		126.000 - 126.999		1/16		.640			
		127.000 - 127.999		1/16		.645			
		128.000 - 128.999		1/16		.650			
		129.000 - 129.999		1/16		.655			
		130.000 - 130.999		1/16		.660			
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		133.000 - 133.999		1/16		.675			
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		135.000 - 135.999		1/16		.685			
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		138.000 - 138.999		1/16		.700			
		139.000 - 139.999		1/16		.705			
		140.000 - 140.999		1/16		.710			
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		160.000 - 160.999		1/16		.810			
		161.000 - 161.999		1/16		.815			
		162.000 - 162.999		1/16		.820			
		163.000 - 163.999		1/16		.825			
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		166.000 - 166.999		1/16		.840			
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		171.000 - 171.999		1/16		.865			
		172.000 - 172.999		1/16		.870			
		173.000 - 173.999		1/16		.875			
		174.000 - 174.999		1/16		.880			
		175.000 - 175.999		1/16		.885			
		176.000 - 176.999		1/16		.890			
		177.000 - 177.999		1/16		.895			
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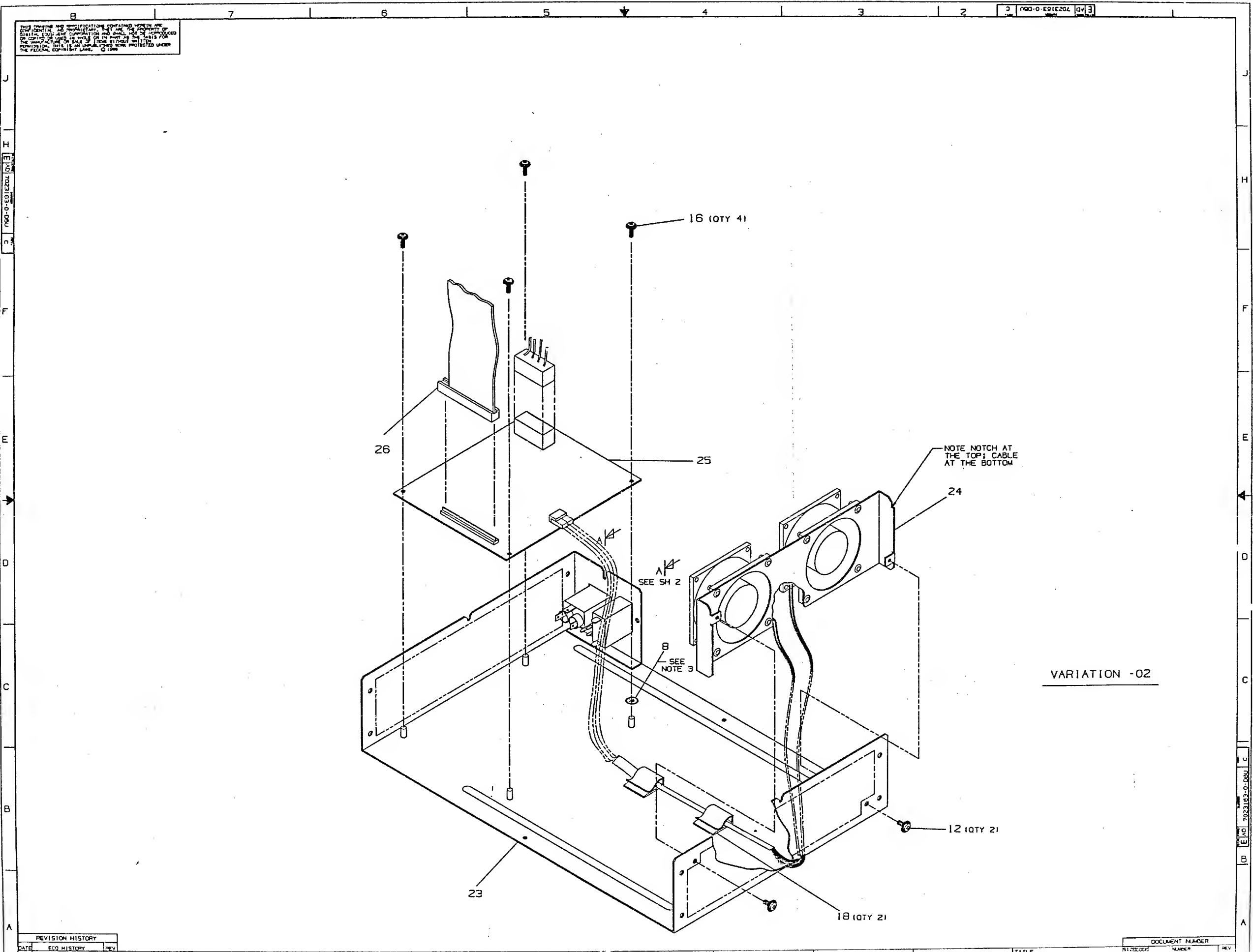
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WIRING INTERCONNECTION LIST		
ITEM	FROM	TO
AC INPUT HARNESS 11	P1	PWR SPLY 1J11
	POINT P2 (BLU)	LF1 (NEUTRAL)
	GROUND WIRE 1G/Y1	LF1 (GND W/SOLDER)
	POINT P3 (BRN)	LF1 (LINE)
	POINT P4 (BRN)	CB1 (LINE)
	TERMINAL 1G/Y1	COVER GND STUO
POWER SUPPLY 3	BLUE WIRE	S1 (2A)
	BLACK WIRE	S1 (2)
	2 PIN MATE-N-LOK	FAN ASSY (P3)
	GROUND WIRE 1G/Y1	COVER GND STUO
DC POWER CABLE 7	RIBBON CABLE	PWR SPLY J2



VARIATION -01

PLOT AT: 1.00

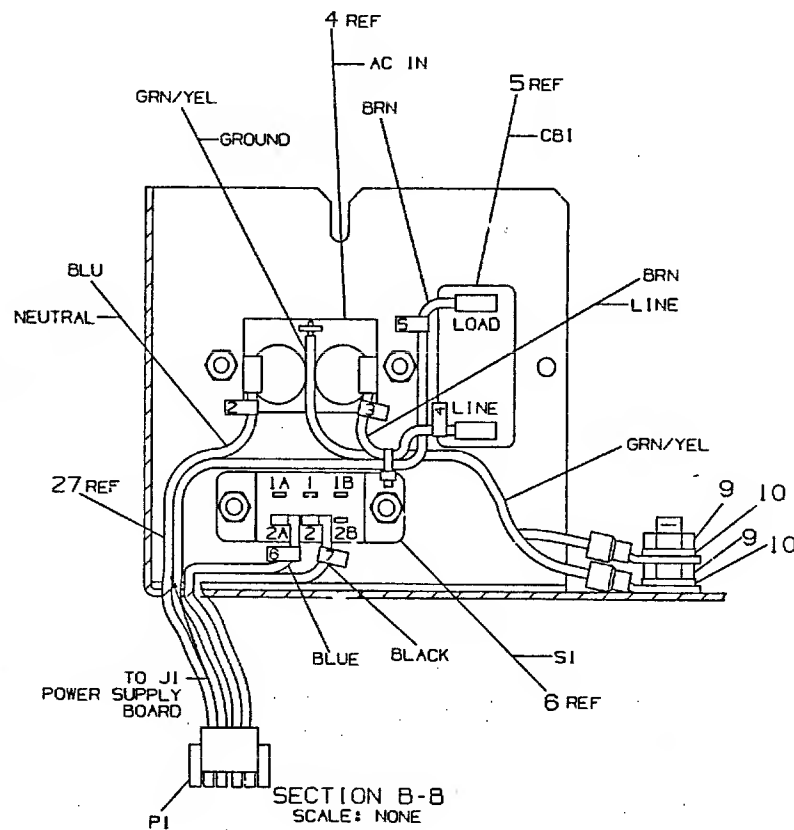
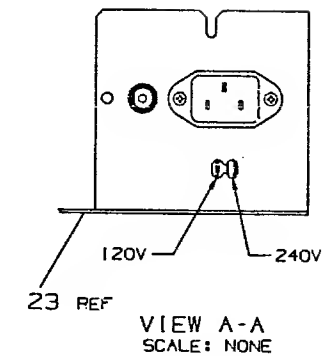
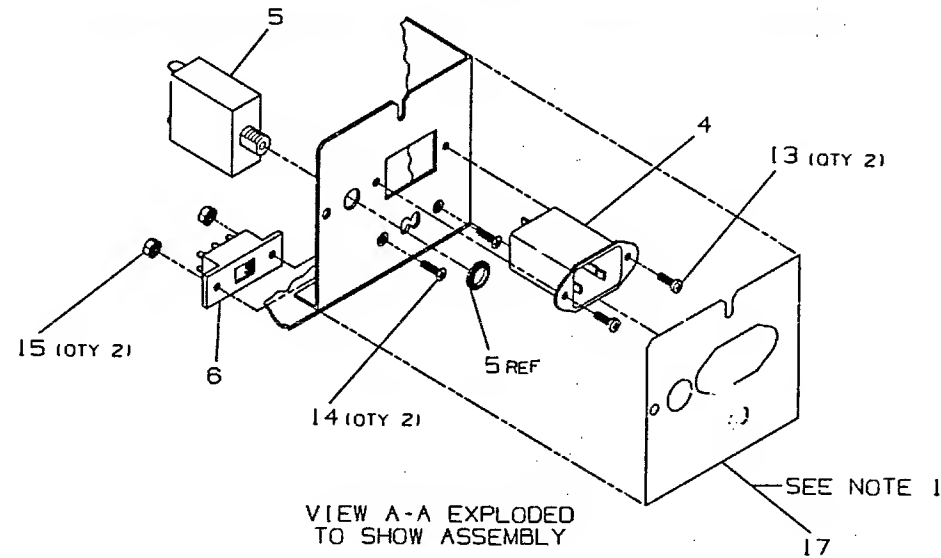


REVISION HISTORY		
DATE	ECO HISTORY	REV

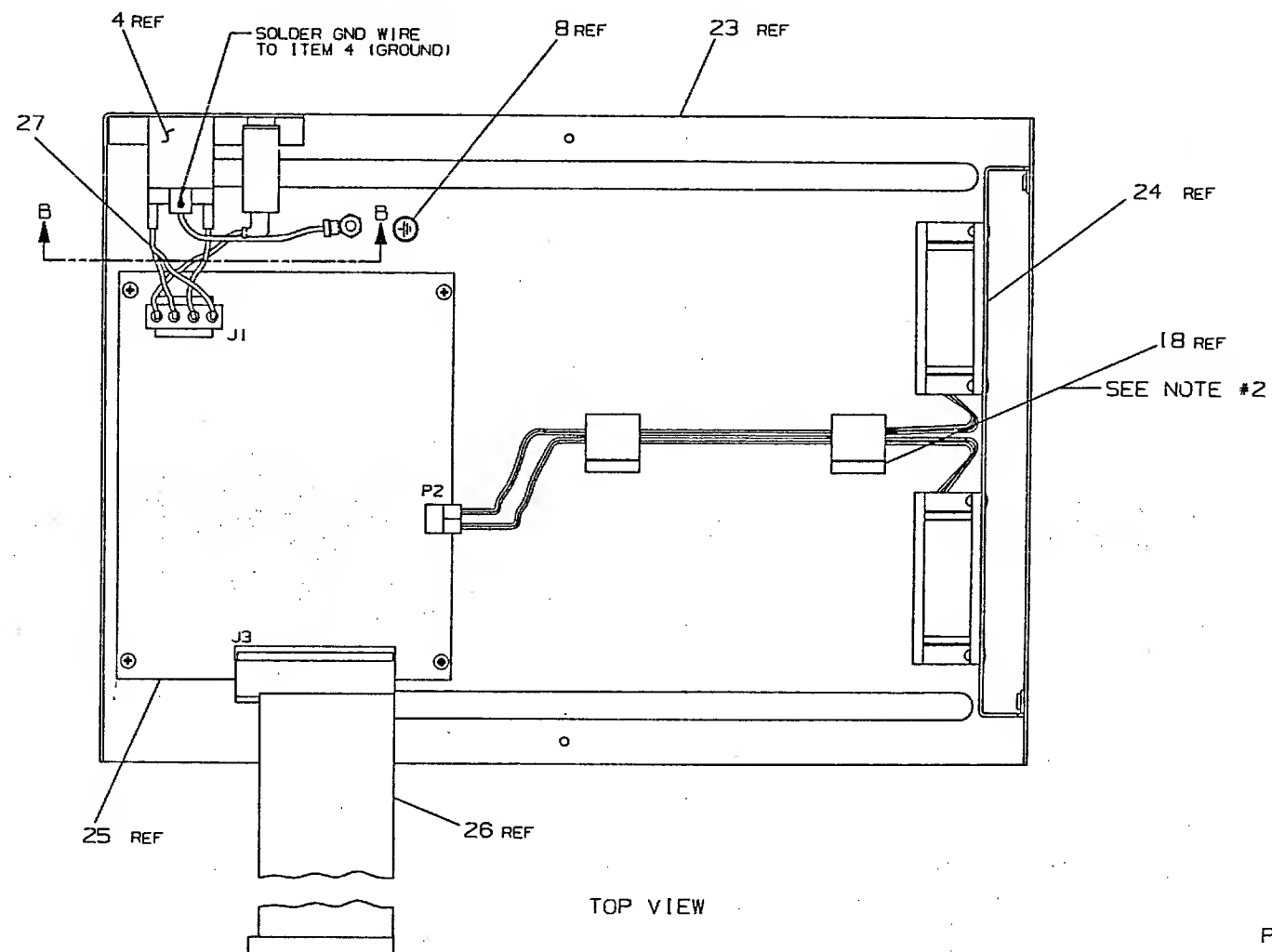
DOCUMENT NUMBER		REV
5125000	NUMBER	

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WIRING INTERCONNECTION LIST		
ITEM	FROM	TO
AC INPUT HARNESS 27	P1	PWR SPLY (J1)
	POINT P2 (BLU)	LFI (NEUTRAL)
	GROUND WIRE (G/Y)	AC IN (GND W/SOLDER)
	POINT 3 (BRN)	LFI (LINE)
	POINT 4 (BRN)	CB1 (LINE)
	TERMINAL (G/Y)	COVER GND STUD
	POINT 5 (BRN)	CB1 (LOAD)
PS 25	POINT 6 (BLU)	SI (2A)
	POINT 7 (BRN)	SI (2)
OC PWR 26	RIBBON CABLE	PWR SUPPLY (J3)
FAN ASSY	FANS	PS (P2)



variation -02



PLOT AT: 1.00

REVISION HISTORY		
DATE	ECO HISTORY	REV

DOCUMENT NUMBER

AUTOMATED BY VAXPL (V1.3)

## P A R T S   L I S T

SHEET A1 OF A1

QUANTITY PER VARIATION/REVISION

LINE ITEM	TOP DOCUMENT	PART NUMBER	DESCRIPTION	01 B1	02 A1
1	E-1A-7432939-0-DBU	74-02939-01	COVER, DECSERVER 200	1	-
2	D-1A-7023165-0-DBU	70-23165-01	FAN, ASSY.	1	-
3	A-PS-3021383-0-0	30-21383-02	P.S. MULTIPLE OUTPUT	1	-
4	A-PS-1215905-0-0	12-15905-31	FILTER, LINE EMI/RFI 115/250V 50-60	1	1
5	A-PS-1219912-0-0	12-19912-07	BKR IF 2.0A 250V	1	1
6	A-PS-1216901-0-0	12-16901-04	SW, SLIDE 3POT VOLTAGE SELECT 6A25	1	1
7	A-PS-1701392-0-0	17-01392-01	CABLE ASSY, 15 COND, FLAT, 18AWG, 19PS	1	-
8	A-PS-3612680-0-0	36-12680-02	LABEL, GROUND SYMBOL W/CIRCLE BLACK	1	1
9		90-06565-00	NUT, HEX EXT TOOTH LOCKSHR 10-32X	2	2
10		90-06564-00	WASHER, FLAT SST	2	2
11	A-PS-1701310-0-0	17-01310-01	WIRE HARN ASSY 18COND	1	-
12		90-10174-00	SCREW, SEMS PAN PHIL 6-32	3	2
13		90-06011-01	SCREW, MACH PAN PHIL 4-40	2	2
14		90-00039-19	SCREW, MACH FLAT PHIL 4-40	2	2
15		90-06557-00	NUT, HEX EXT TOOTH LOCKSHR 4-40X	4	2
16		90-10174-03	SCREW, SEMS PAN PHIL 6-32	4	4
17		36-25984-01	LABEL, REAR COMMON BOX POWER SUPPLY	1	1
18		90-09936-00	CLAMP, CABLE ADH INT	2	2
19			*** THIS ITEM IS NOT USED ***	-	-
20	A-PS-3612680-0-0	36-12680-00	LABEL, GROUND SYMBOL BLACK	1	-
21		90-00001-44	STANDOFF, MALE/FEMALE HEX 6-32 X	4	-
22	D-1A-7024395-0-DBU	70-24395-01	COVER ASSY, POWER SUPPLY	1	-
23	E-1A-7433798-02-DBU	74-33798-02	COVER, DECSERVER 200	-	1
24	D-1A-7023165-0-DBU	70-23165-02	FAN ASSEMBLY	-	1
25	A-PS-3027484-0-0	30-27484-01	P.S. 59W 3 OUTPUT	-	1
26	A-PS-1701558-0-0	17-01558-01	CABLE ASSY, 15 COND, FLAT, 18AWG 16PH	-	1
27	A-PS-1701756-0-0	17-01756-01	WIRE HARN ASSY	-	1

REVISION HISTORY			KPL MATRIX FORMAT SECTION A OF A		DRN: E. TENANDER		D I G I T A L			
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 11-FEB-86		TITLE PARTS LIST			
---	INITIAL	A	[A] 01,02		CHK'D: BOURGAULT/WM		COVER ASSY			
WM	DSRVB-A-LK001	B	[B]		DATE: 04-JUN-86					
ML	DSRVB-A-MK002	C	[C]		DES. ENG: W. MCCARTHY					
			[D]		DATE: 04-JUN-86		DOCUMENT NUMBER			
			[E]		RESP. ENG.: A. PARIKH		SIZE	CODE	NUMBER	REV
			[F]		DATE: 04-JUN-86		K	PL	7023163-0-DBP	C
			[H]		MFG. ENG: J. SEQUINOT		RELEASE DATE: 19-OCT-87			
			[J]		DATE: 04-JUN-86		RELEASE STATUS: RELEASED			
			BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	
			7023163		E-AD-7023163-0-DBU		E-AD-7023163-0-DBU		7023163C.PLS	
									EDIT #	
									1	

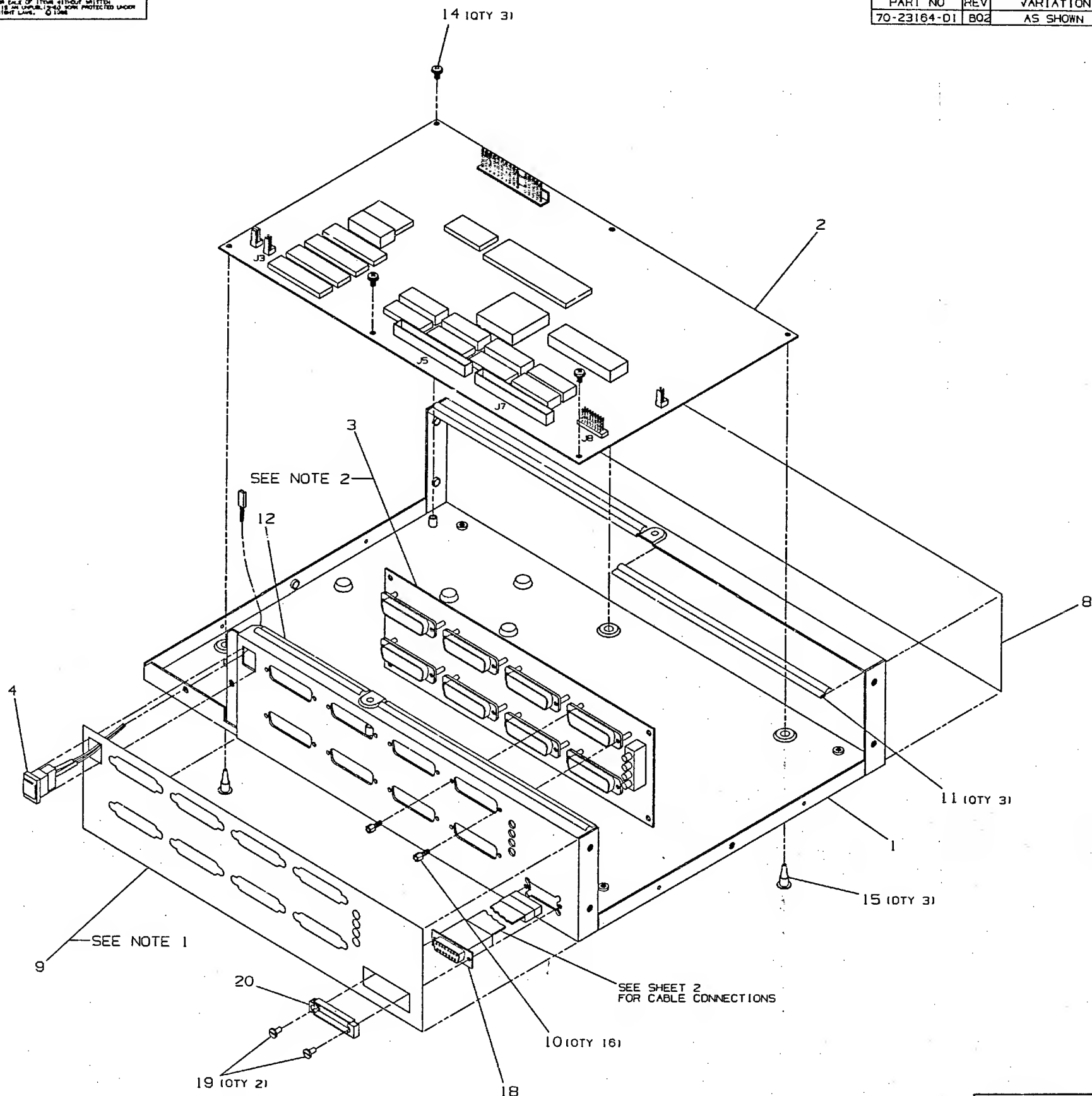
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LEGEND		
PART NO	REV	VARIATION
70-23164-01	B02	AS SHOWN

- NOTES:
1. INSTALL REAR LABEL (ITEM #9) BEFORE INSTALLING RESET SWITCH ASSY (ITEM #4), AND SLIDE LATCH (ITEM #20).
  2. INSTALL RS232 BD (ITEM #3) BEFORE INSTALLING MOTHER BD (ITEM #21).
  3. LOCATE ADHESIVE BACKED CABLE CLAMP (ITEM 17) APPROXIMATELY WHERE SHOWN.

J  
H  
F  
E  
D  
C  
B  
A



LAYER 1: WORK LAYER (SHT 1)  
LAYER 2: FORMAT LAYER (SHT 1)  
LAYER 3: WORK LAYER (SHT 2)  
LAYER 4: FORMAT LAYER (SHT 2)

PLOT AT: 1.00  
CAUTION: OFF SHEET PARTS LIST EXISTS  
SEE K-PL-7023164-0-DBP

PROVISIONS	CHANGE NO.	REV.
INITIAL	A	
SRVB-A-LK1	B	
AYLOR 40ECB8		
F. MCCARTHY		
SRVB-A-MK2	C	
EEO 14OCT87		
LAWRENCE		



AUTOMATED BY VAXKFL (V1.3)

## P A R T S   L I S T

SHEET A1 OF A1

LINE ITEM		TOP DOCUMENT	PART NUMBER	MIN REV.	DESCRIPTION	QTY	PER VARIATION/REVISION
						B2	
1	1	E-IA-7433799-02-DBU	74-33799-02		BASE, DECSERVER 200	1	
2	2		54-18034-01		DECSERVER MAIN BOARD	1	
3	3		54-18027-01		RS232 INTERFACE BD W/ESD PROTECTIO	1	
4	4	A-PS-1701393-0-0	17-01393-01		WIRE HARN ASSY 02COND 22AWG PB SW-	1	
5	5	A-PS-1218489-0-0	12-18489-02		*** THIS ITEM IS NOT USED ***	-	
6	6	A-PS-1700559-0-0	17-00559-01		*** THIS ITEM IS NOT USED ***	-	
7	7	A-PS-1701338-0-0	17-01338-01		CABLE ASSY, 50COND, FLAT, 50(12X25) REC	2	
8	8	A-PS-3626084-0-0	36-26084-01		LABEL, FRONT COMMON BOX DSRV 200/MC	1	
9	9	A-PS-3625984-0-0	36-25984-02		LABEL, REAR, COMMON BOX DESERVER 200	1	
10	10		90-08451-05		SCREW LOCK, SCREW LOCK ONLY, 060THK	16	
11	11	A-PS-1226160-0-0	12-26160-04		SHIELD, EMI/RFI, COPPER FINGERS 8.00	3	
12	12	A-PS-1226160-0-0	12-26160-01		SHIELD, EMI/RFI, COPPER FINGERS 4.12	1	
13	13	A-PS-1226489-0-0	12-26489-01		SHIELD, EMI/RFI AD	1	
14	14		90-10174-00		SCREW, SEMS PAN. PHIL 6-32	3	
15	15	A-PS-1223075-0-0	12-23075-03		STANDOFF PC BOARD NYLON	3	
16	16		90-09538-00		*** THIS ITEM IS NOT USED ***	-	
17	17		12-21249-05		CLIP, CABLE, ADH BACKED	1	
18	18	A-PS-1701675-0-0	17-01675-01		CABLE ASSY, 15COND, FLAT,	1	
19	19	A-PS-1218489-0-0	12-18489-04		LATCH ASSY, SLIDE .29 LG. POST	2	
20	20	A-PS-1218489-0-0	12-18489-03		LATCH ASSY, SLIDE	1	
21	21		90-10076-00		BUMPER, DOME HD ADH BACK POLYUR.	10	

1 GEN: NOTE: WHEN MADE IN IRELAND, SUBSTITUTE ITEM 9 WITH THE FOLLOWING LABEL: 36-25984-05.

REVISION HISTORY			KPL MATRIX FORMAT		SECTION A OF A		DRN: E. TENANDER		DIGITAL			
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 26-FEB-86		CHK'D: BOURGAULT/MM		TITLE PARTS LIST			
---	INITIAL	A	(A) 01		DATE: 04-JUN-86		DATE: 04-JUN-86		BASE, ASSY.			
MM	DSRVB-A-LK001	B	(B)				DES.ENG: W. MCCARTHY		DECSERVER 200/MC			
ML	DSRVB-A-MK002	C	(C)				DATE: 04-JUN-86		DOCUMENT NUMBER			
			(D)				RESP.ENG.: A. PARIKH		SIZE CODE		NUMBER	REV
			(E)				DATE: 04-JUN-86		K	PL	7023164-0-DBP	C
			(F)				MFG.ENG: J. SEQUINOT		RELEASE DATE: 14-OCT-87			
			(H)				DATE: 04-JUN-86		RELEASE STATUS: RELEASED			
			(J)									
			BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
			7023164		E-AD-7023164-0-DBU		E-AD-7023164-0-DBU		7023164C.PLS		2	

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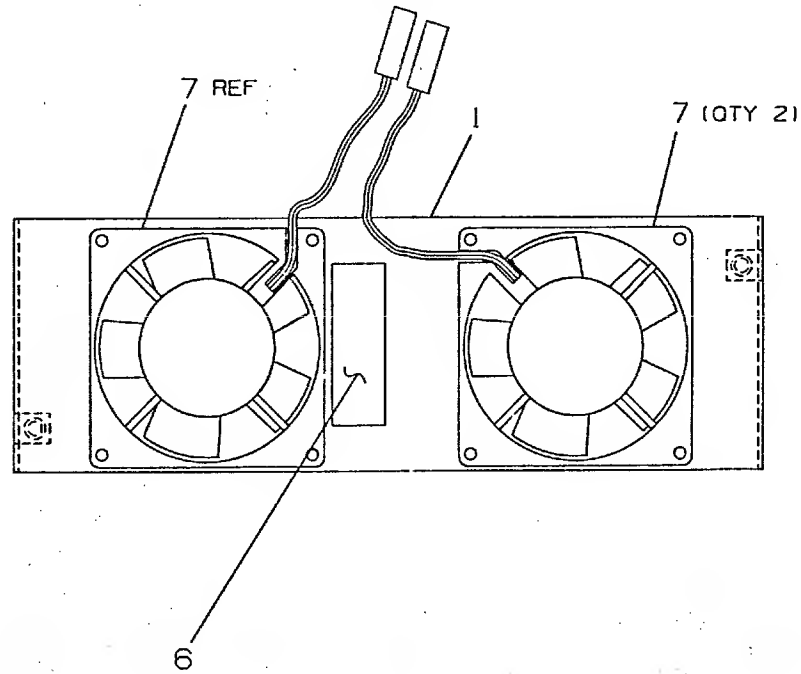
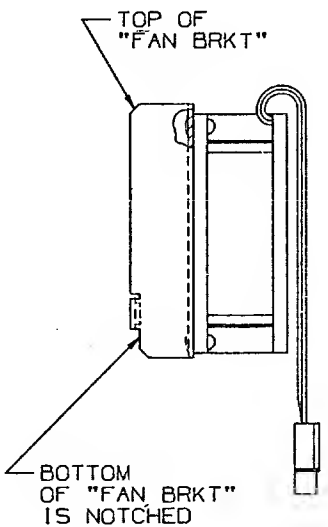
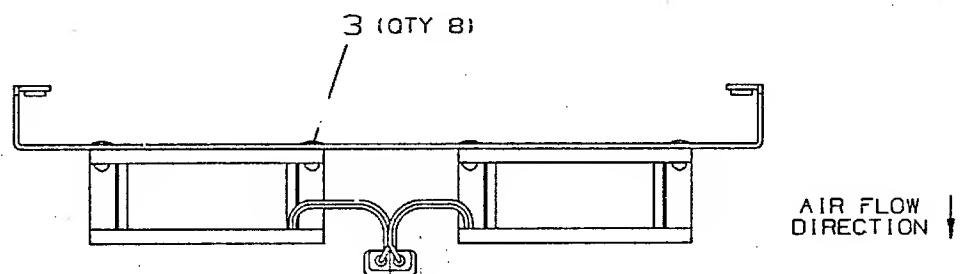




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7023165-0-DBU

7023165-0-DBU



VARIATION -02

REVISIONS		
CHK	CHANGE NO	REV

TITLE		FAN ASSY	SIZE CODE		DIA	NUMBER		7023165-0-DBU	REV	B
SCALE		1/1	SHEET		2	OF		2	DIST	

MKO

AUTOMATED BY VAXKPL (V1.3)

## P A R T S L I S T

SHEET A1 OF A1

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QUANTITY PER VARIATION/REVISION	
						01 A1	02 A1
1	1	D-1A-7432931-0-DBU	74-32931-01		BRACKET,FAN	1	1
2	2	A-PS-1221125-0-0	12-21125-01		FAN,TUBE AXIAL 3.125" 12VDC	2	-
3	3		90-00030-37		RIVET,BLIND LOBRK 5/320x	8	8
4	4	A-PS-1210822-0-0	12-10822-02		MATE-N-LOK 02PIN(1X02) HSG	1	-
5	5	A-PS-1209378-0-0	12-09378-08		MATE-N-LOK 01PIN 22-18AWG .08500	2	-
6	6		90-09255-03		LABEL,P.S./SUB ASSY 10	1	1
7	7		12-21125-03		FAN,TUBE AXIAL 12VDC	-	2

REVISION HISTORY			KPL MATRIX FORMAT		SECTION A OF A		DRN: D. CHIASSON		D I G I T A L					
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX		DATE: 09-JAN-86		CHK'D: BOURGAULT/WM		TITLE PARTS LIST					
---	INITIAL	A	(A)	01,02	DATE: 04-JUN-86		DES.ENG: W. MCCARTHY		DOCUMENT NUMBER					
ML	DSRVE-A-MK002	B	(B)		DATE: 04-JUN-86		DATE: 04-JUN-86		SIZE CODE NUMBER REV					
			(C)				RESP.ENG.: A. FARIAH		K PL 7023165-0-DBF B					
			(D)				DATE: 04-JUN-86		RELEASE DATE: 14-OCT-87					
			(E)				MFG.ENG: J. SEQUINOT		RELEASE STATUS: RELEASED					
			(F)				DATE: 04-JUN-86							
			(H)											
			(J)											
			BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #			
			7023165		D-1A-7023165-0-DBU		D-1A-7023165-0-DBU		7023165B.PLS		2			

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DRAWING NO.	NO. SHTS	PART NO.	DESCRIPTION	REVISIONS
		5418027-01	PS232 I/O BOARD	A1 A2
E-UA-5418027-0-0	2		PS232 I/O BOARD UNIT ASSEMBLY	A A
K-PL-5418027-0-DBP	1		RS232 I/O BOARD PARTS LIST	A B
K-PC-5418027-0-DBV	-		PC DATA BASE	A A
K-CS-5418027-0-DBD	-		DAISY DATA BASE	A A
K-BD-5418027-0-ESD1	1		RS232 I/O BOARD BLOCK DIAGRAM	A A
K-CS-5418027-0-ESD2	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD3	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD4	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD5	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD6	1		RS232 I/O BOARD SCHEMATIC	A A
K-CS-5418027-0-ESD7	1		RS232 I/O BOARD SCHEMATIC	A A
K-DD-5018026-0-0	1		DRAWING DIRECTORY	A A
NOTES:			R E V	
			R E V I S I O N S	
			D A J A P U T R L E 8 8 6 7	
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			CHK'D: DENK GARAFOLO	DATE: 29-apr-86
			DES.ENG.: R. BATTERSBY	DATE: 29-apr-86
			<i>Martin Smith</i>	16-SEP-87
			RESP.ENG.: R. BATTERSBY	DATE: 29-apr-86
			<i>Martin Smith</i>	16-SEP-87
			MFG.ENG.: JAIME SEGUINOT	DATE: 29-apr-86
			TITLE RS232 I/O BOARD	
			SHEET 1 OF 1 EDIT: 1	
			DOCUMENT NUMBER	
			DD FILENAME: 5418027B.DDF	
			SIZE CODE NUMBER REV K DD 5418027-0-0 B	



REWORK INSTRUCTIONS  
WIRE ADDS - SIDE 1  
0-2. FROM E4-16 TO J9-50

REVISION HISTORY			DOCUMENT NUMBER										
REV	ECO NUMBER	DATE								TITLE	SIZE CODE	NUMBER	REV
										RS232 I/O BOARD	DIA	5418027-0-0	A
										SCALE	SHEET 2 OF 2		



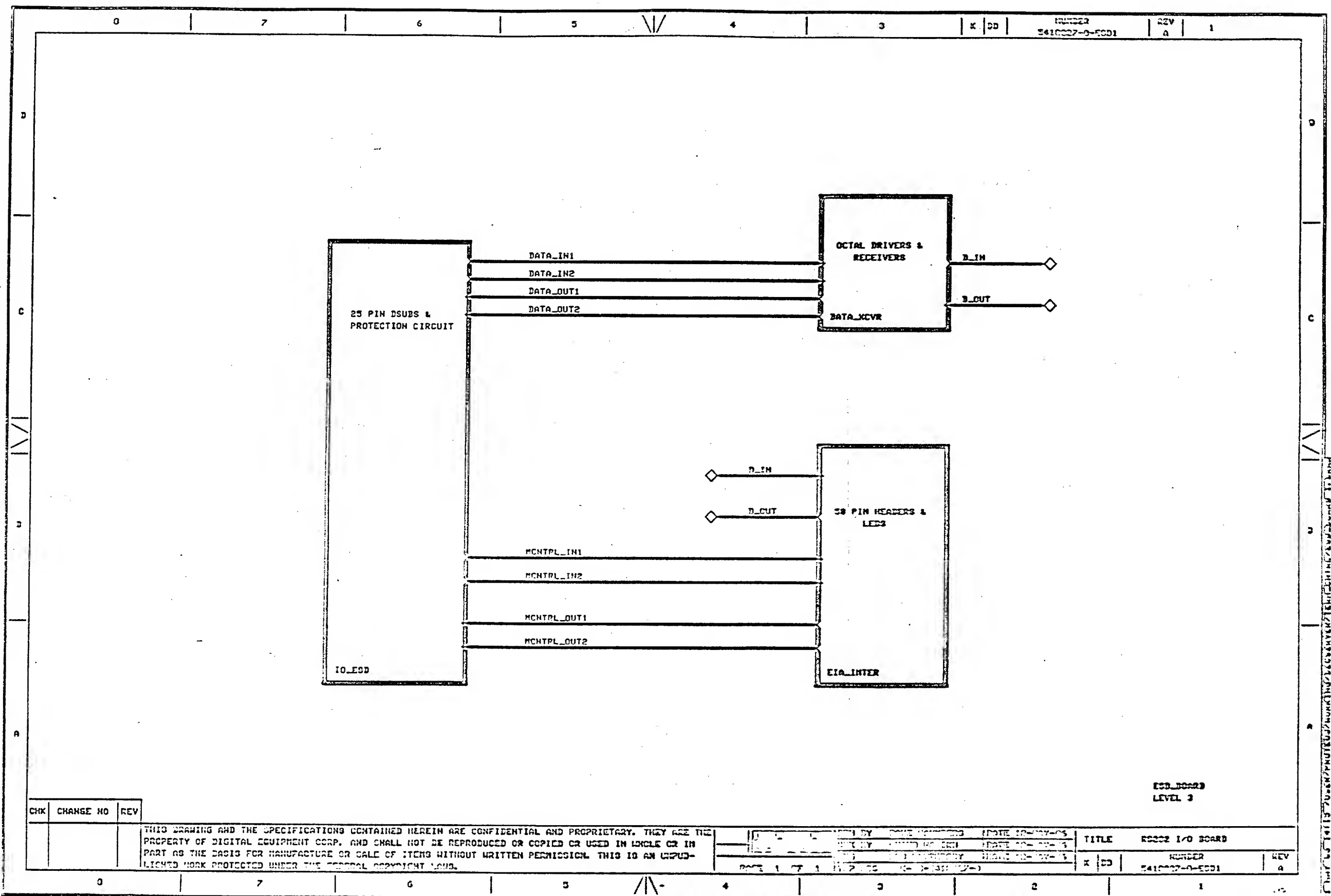
AUTOMATED BY VAXKPL (V1.3)

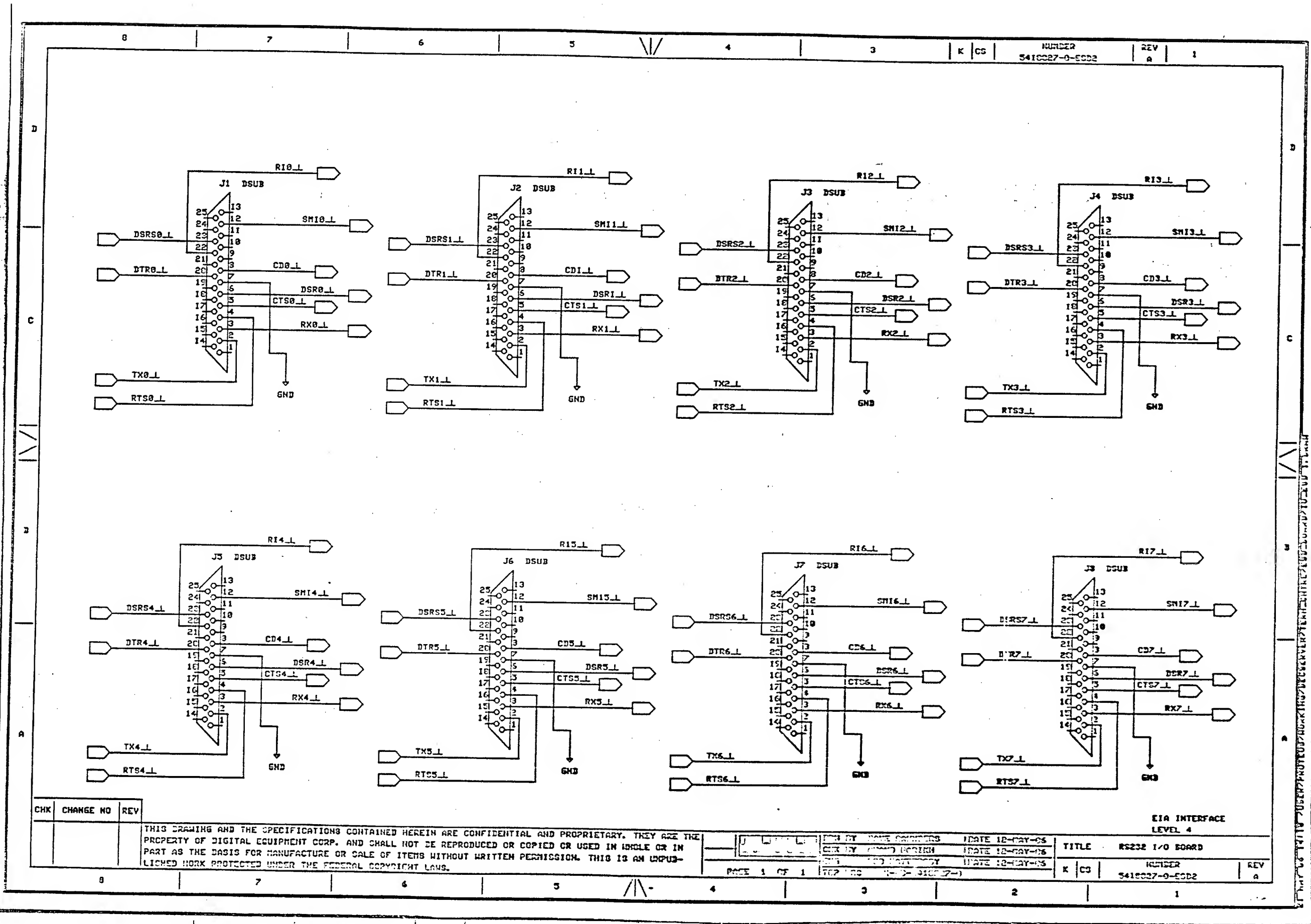
## P A R T S L I S T

SHEET A1 OF A1

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VAR/REV	REFERENCE DESIGNATORS
1	1	D-MD-5018026-0-0	50-18026-01		DRILL AND ETCH BOARD	1	
2	2		10-13466-22		0.10 MFD 50V +80/-20% TSU C	3	C1-C3
3	3		10-23486-01		2.2 MFD 50V +/-20% ALU	3	C4-C6
4	4		11-26094-01		QUAD GREEN LED ASSEMBLY .456H0US	1	D1
5	5		12-25898-03		CONN,D SUB 11POS ASSY STR GOLD P	8	J1-J8
6	6		12-23637-01		BLOWER, EVAPORATOR	2	J9, J10
7	7		13-00250-00		150.0 .25 W 5.0 % CF	4	R1-R4
8	8		13-03303-00		2.61 K .25 W 1.0 % RN55D-F10	1	R5
9	9		19-25894-01		PROTECTION DEVICE-TR	10	E1, E2, E5-E12
10	10		19-23839-02		EIA OCTAL LINE DRIVE	1	E3
11	11		19-23890-02		EIA OCTAL LINE RECEI	1	E4
12	12		91-05740-55		WIRE(WRAP) 30AWG KYNAR UL14	A/R	

REVISION HISTORY		KPL MODULE FORMAT		SECTION A OF A		S. MANSOR		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX	CHK'D	DATE	DATE	03-JUN-86	TITLE	PARTS LIST
ML	5418027-MK001	A	[A] 01 [M]	DATE	18-NOV-85	DES. ENG	R. GARAFOLO	RS232 I/O BOARD	
		B	[B] [N]	DATE	18-NOV-85	RESP. ENG	R. BATTERSBY	DOCUMENT NUMBER	
			[C] [P]	DATE	15-JAN-86	DATE	15-JAN-86	SIZE	CODE
			[D] [Q]	DATE	15-JAN-86	DATE	15-JAN-86	NUMBER	REV
			[E] [R]	DATE	15-JAN-86	DATE	15-JAN-86	NUMBER	REV
			[F] [S]	DATE	15-JAN-86	DATE	15-JAN-86	NUMBER	REV
			[H] [T]	DATE	15-JAN-86	DATE	15-JAN-86	NUMBER	REV
			[J] [V]	DATE	15-JAN-86	DATE	15-JAN-86	NUMBER	REV
			[K] [W]	DATE	15-JAN-86	DATE	15-JAN-86	NUMBER	REV
			[L] [Y]	DATE	15-JAN-86	DATE	15-JAN-86	NUMBER	REV
BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
5418027		E-UA-5418027-0-0		K-DD-5418027-0-0		5418027B.PLS			
<p>THIS DRAWING AND THE SPECIFICATIONS CONTAINED HEREIN ARE CONFIDENTIAL AND PROPRIETARY. THEY ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. THIS IS AN UNPUBLISHED WORK PROTECTED UNDER THE FEDERAL COPYRIGHT LAWS.</p>									





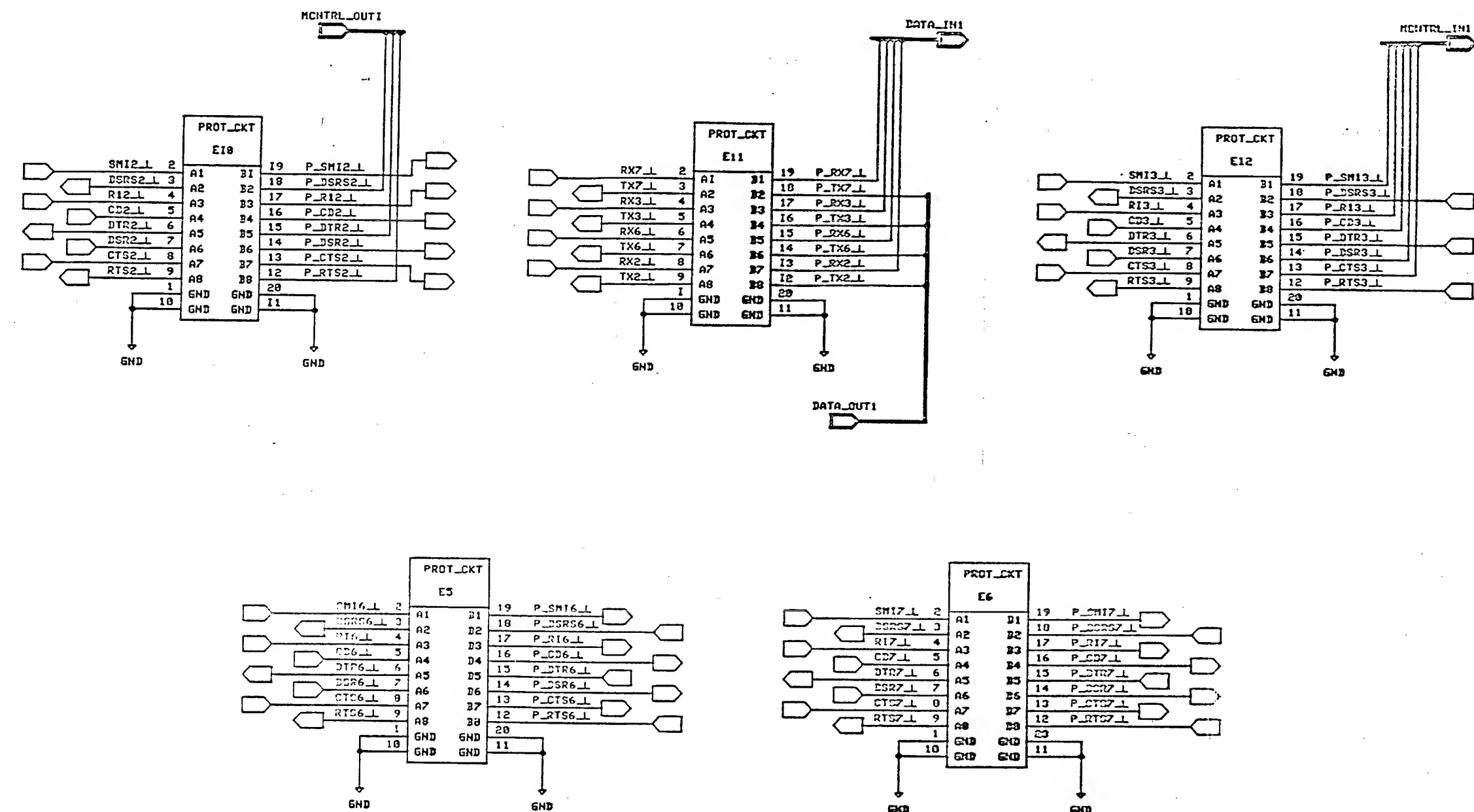
CHK	CHANGE NO	REV

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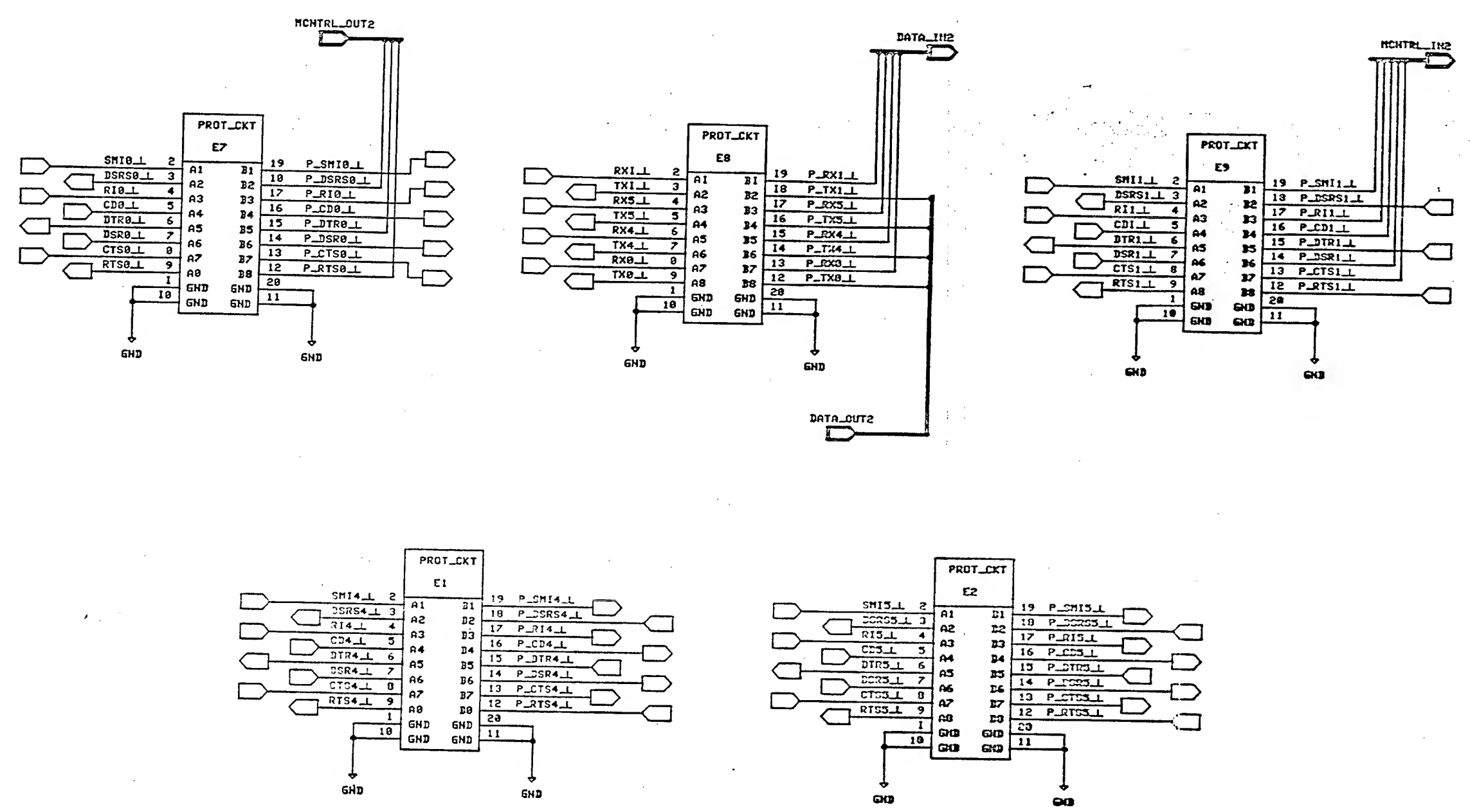
DESIGNED BY	DATE	12-MAY-86
CHECKED BY	DATE	12-MAY-86
APP'D BY	DATE	12-MAY-86
PAGE 1 OF 1		

EIA INTERFACE LEVEL 4	
TITLE RS232 I/O BOARD	
NUMBER	REV
5410027-0-0002	A

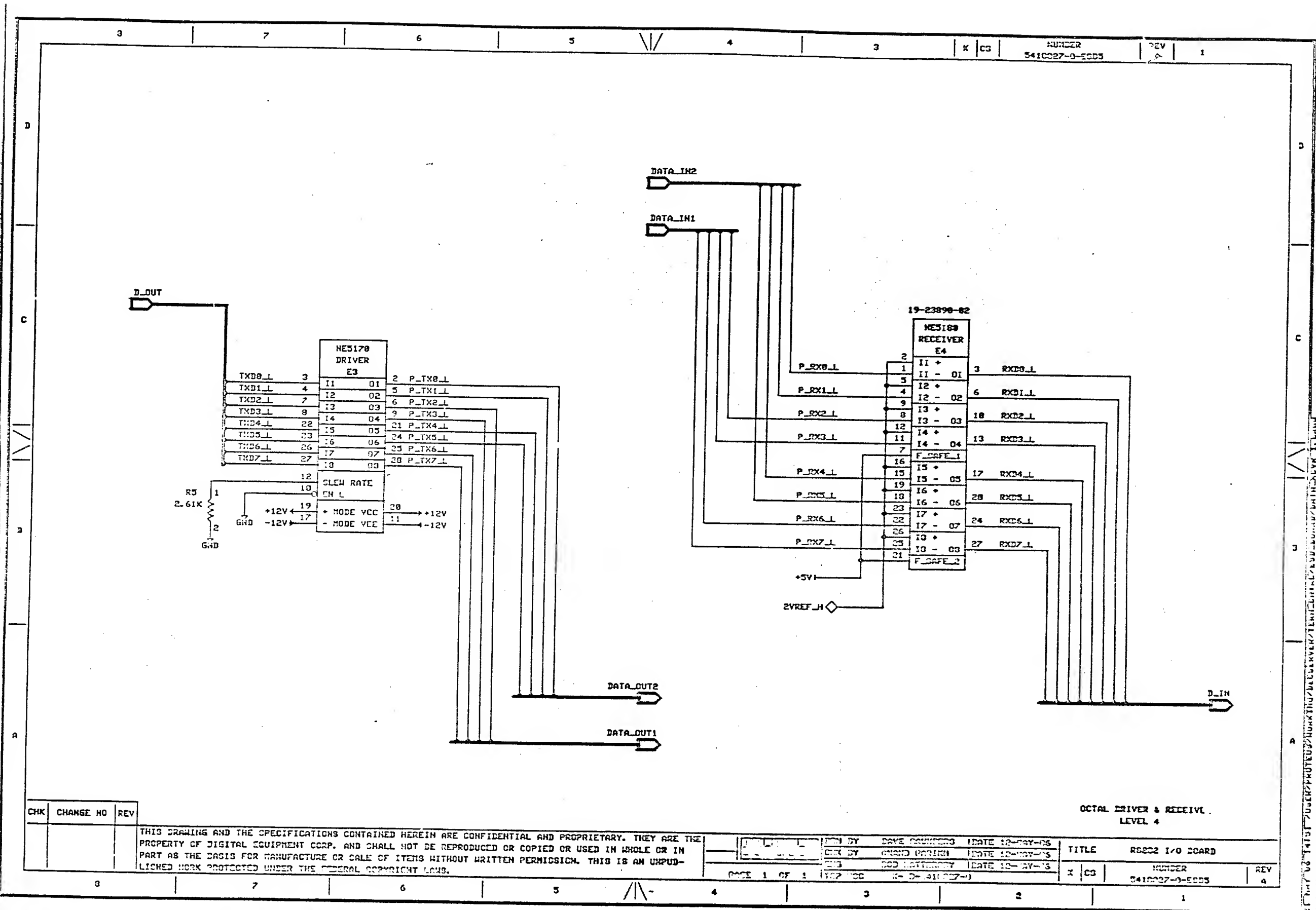


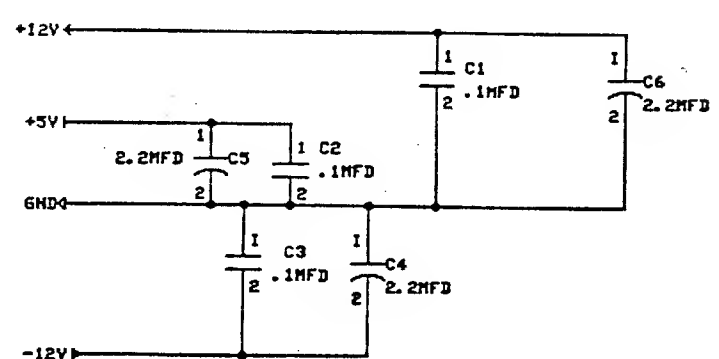
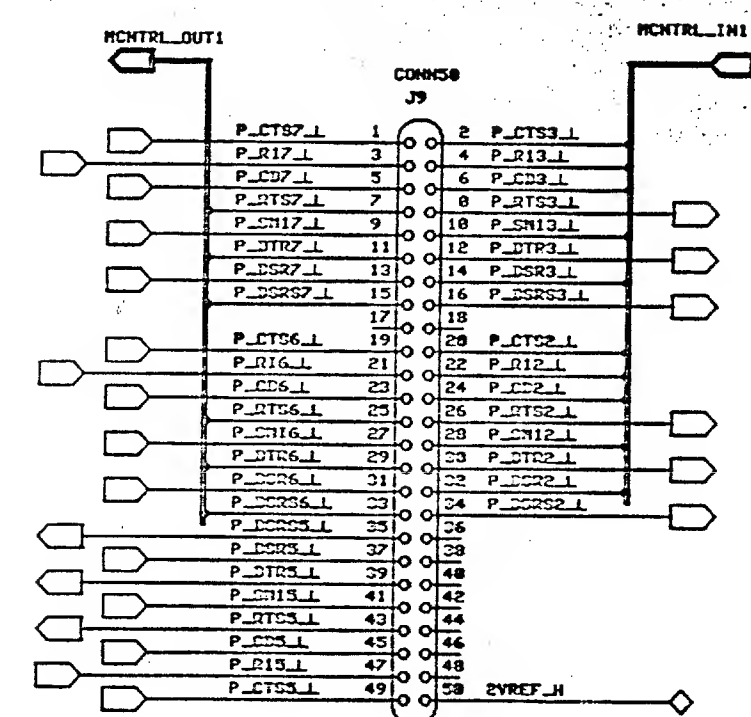
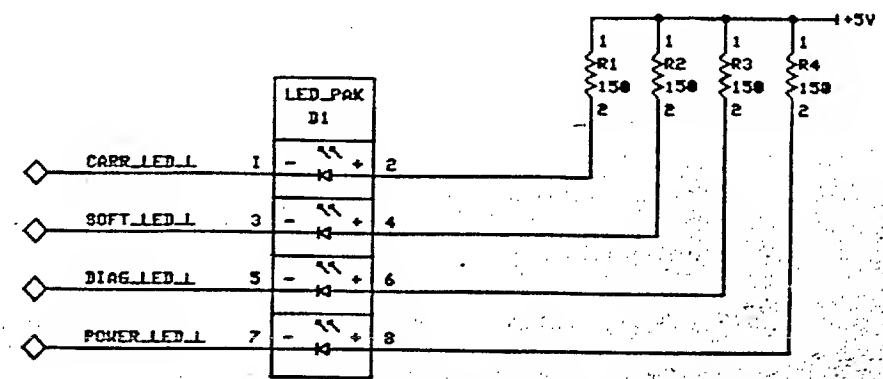
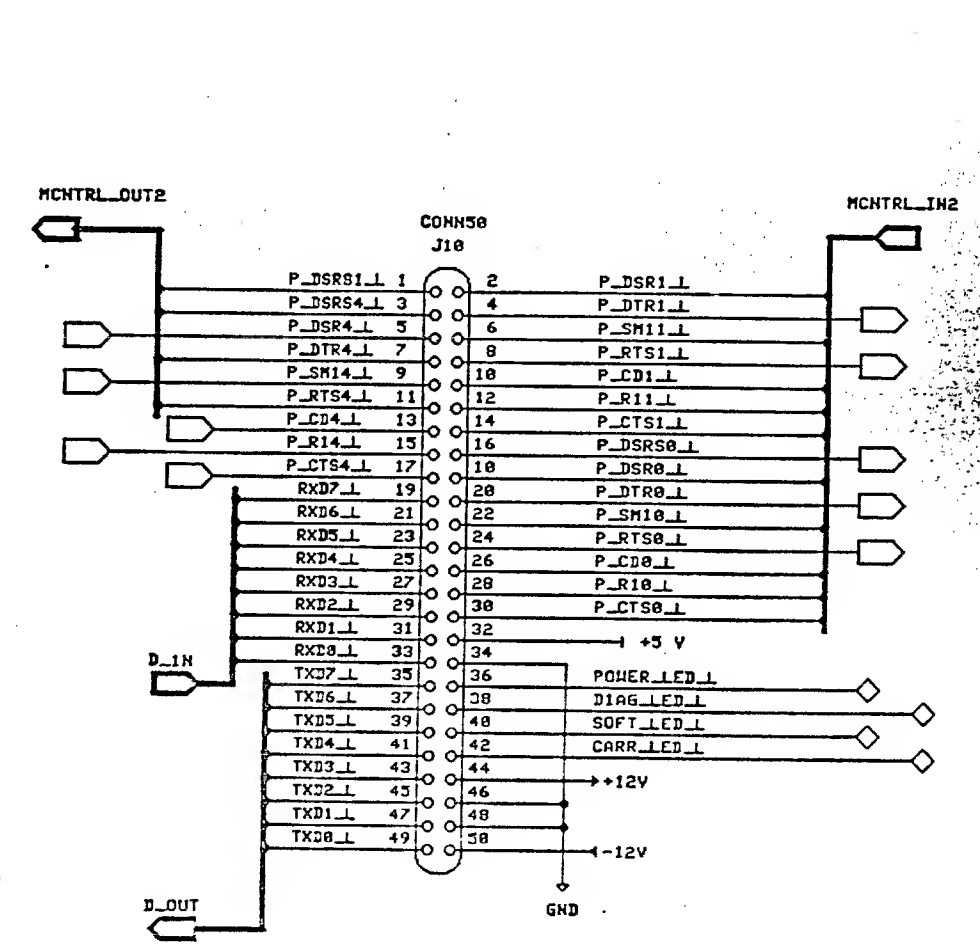


PROTECTION CIRCUIT  
LEVEL 4

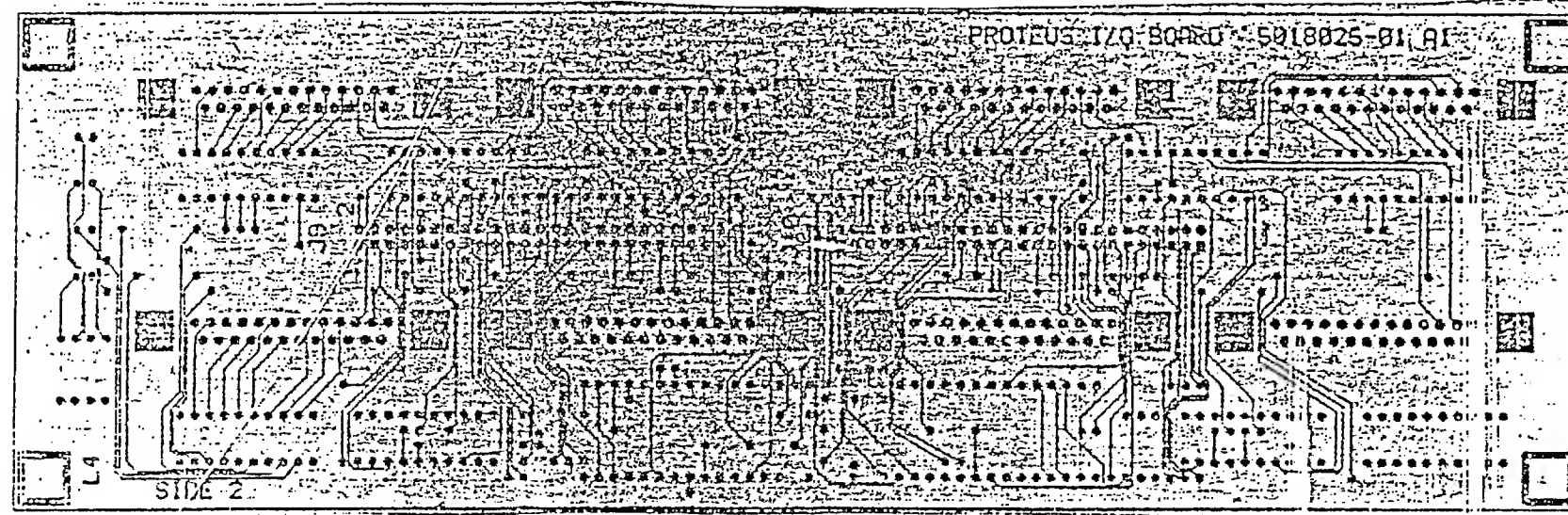
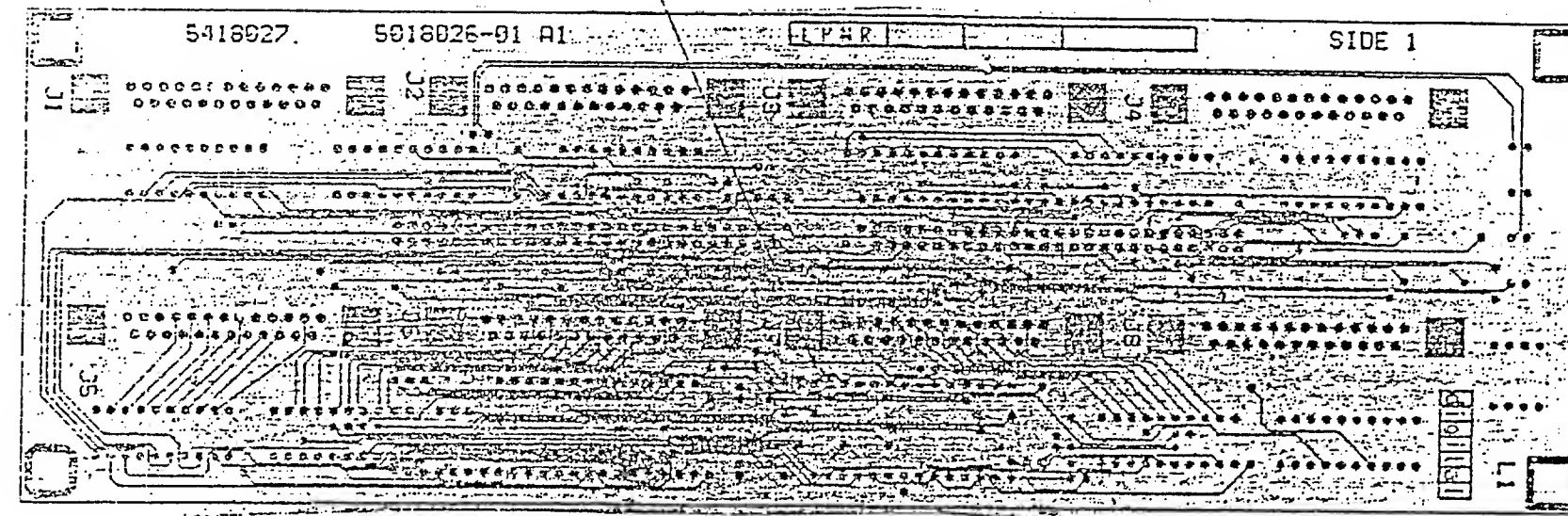


PROTECTION CIRCUIT  
LEVEL 4









digital
DEC 5018026-0-0 A

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REWORK INSTRUCTIONS  
ETCH CUTS - SIDE 1:  
Q-1 AT E4-14

REVISION HISTORY		
REV	ECO NUMBER	DATE

TITLE  
ETCH CUT DRAWING

DOCUMENT NUMBER		REV
SIZE CODE	NUMBER	
DEC	5018026-0-0	A
SCALE	SHEET	

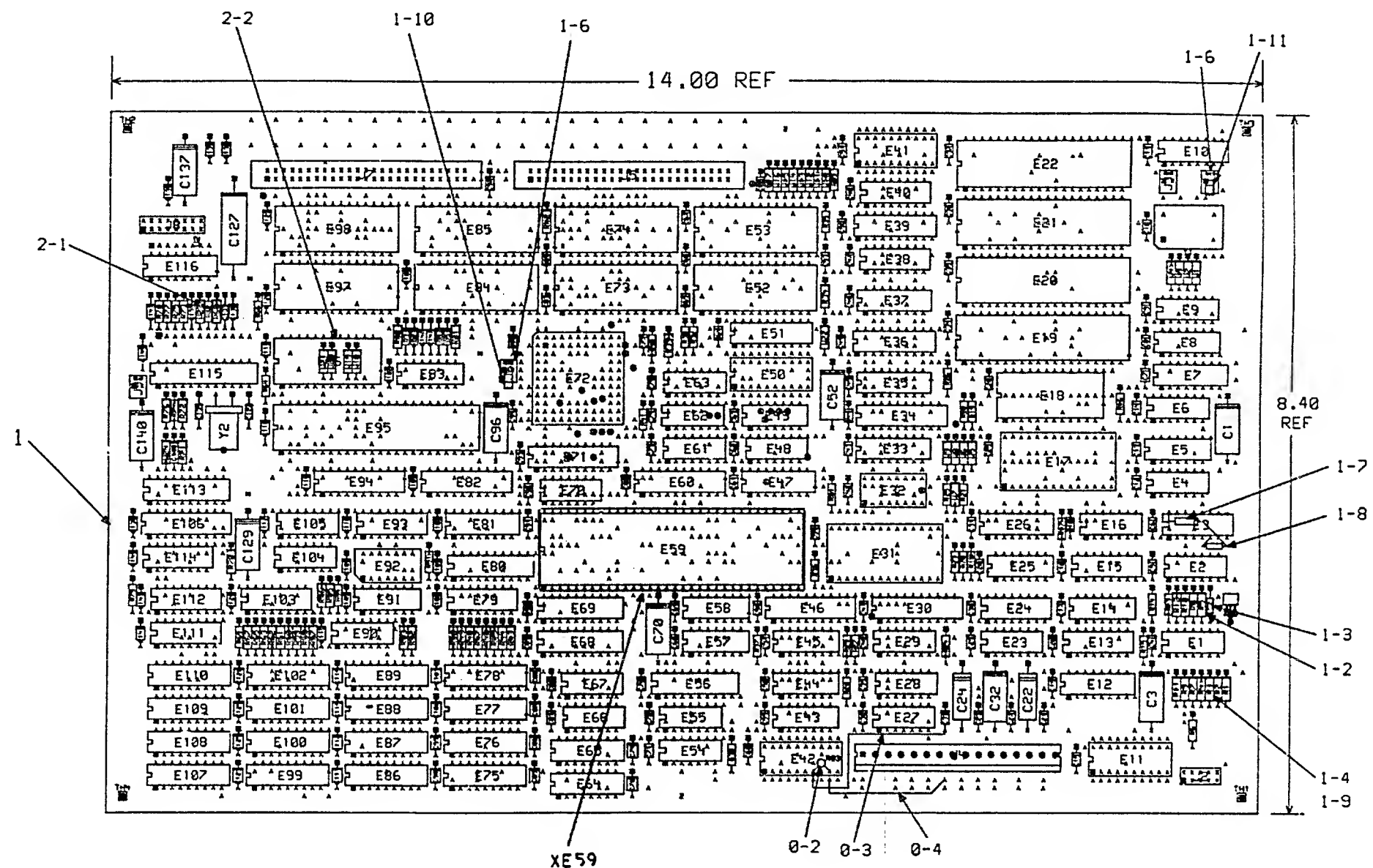
DRAWING NO.	NO. SHTS	PART NO.	DESCRIPTION	REVISIONS						
		5418034-01	DECSERVER 200	A1 B1 B2 B3						
		5418034-02	DECSERVER 200	A1 B1 B2 B3						
		5418034-03	DECSERVER 200	A1 B1 B2 B3						
D-UA-5418034-0-0	2		DECSERVER 200 UNIT ASSEMBLY	A B C D						
K-PL-5418034-0-08P	4		DECSERVER 200 PARTS LIST	A B C D						
K-PC-5418034-0-08V	-		DECSERVER 200 PC DATA BASE	C C C C						
K-DD-5018033-0-0	1		DRAWING DIRECTORY	A B B B						
K-CS-5418034-0-08D	-		DAISY DATA BASE	A B B B						
K-CS-5418034-0-AR31	1		DECSERVER 200 SCHEMATIC	A A A A						
K-BD-5418034-0-81	1		DECSERVER 200 BLOCK DIAGRAM	A A A A						
K-BD-5418034-0-62	1		DECSERVER 200 BLOCK DIAGRAM	A A A A						
K-BD-5418034-0-83	1		DECSERVER 200 BLOCK DIAGRAM	A A A A						
K-BD-5418034-0-84	1		DECSERVER 200 BLOCK DIAGRAM	A A A A						
K-BD-5418034-0-CPU1	1		DECSERVER 200 BLOCK DIAGRAM	A A A A						
K-CS-5418034-0-CPU2	1		DECSERVER 200 SCHEMATIC	A A A A						
K-CS-5418034-0-CPU3	1		DECSERVER 200 SCHEMATIC	A A A A						
K-CS-5418034-0-CPU4	1		DECSERVER 200 SCHEMATIC	A A A A						
K-CS-5418034-0-CPU5	1		DECSERVER 200 SCHEMATIC	A B B B						
NOTES:			A B C D							
			R E V							
			E M S I L M H							
			V I C N K K K							
			I S O I O O O							
			S T O T O O O							
			I O N 1 2 3							
			O R O							
			N Y							
			D O O O O							
			A 4 1 8 1							
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			E 8 9 8 8							
			6 7 7 8							
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			CHK'D: D. JANUSKIEWICZ		DATE: 27-JAN-88		TITLE		DEC SERVER 200	
			DES.ENG.: L. GRISWOLD		DATE: 27-JAN-88		SHEET 1 OF 2		EDIT: 3	
			RESP.ENG.: R. BLANCHETTE Thomas A. Gelli		DATE: 27-JAN-88		DOCUMENT NUMBER			
			MFG.ENG.: A. RIVERA		DATE: 27-JAN-88		SIZE CODE		NUMBER REV	
				K DD		5418034-0-0 D				



DRAWING NO.		NO. SHTS	PART NO.	DESCRIPTION	REVISIONS			
K-CS-5418034-O-CPU6	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-CPU7	1		DEC SERVER 200 SCHEMATIC	A	9	8	B	
K-CS-5418034-O-CPU8	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-BD-5418034-O-MEM1	1		DEC SERVER 200 BLOCK DIAGRAM	A	A	A	A	
K-CS-5418034-O-MEM2	1		DEC SERVER 200 SCHEMATIC	A	B	9	B	
K-CS-5418034-O-MEM3	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-MEM4	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-MEM5	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-MEM6	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-MEM7	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-MEM8	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-MEM9	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-MEM10	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-BD-5418034-O-INT1	1		DEC SERVER 200 BLOCK DIAGRAM	A	A	A	A	
K-CS-5418034-O-INT2	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-INT3	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-INT4	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-BD-5418034-O-TRM1	1		DEC SERVER 200 BLOCK DIAGRAM	A	A	A	A	
K-CS-5418034-O-TRM2	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM3	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM4	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM5	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM6	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM7	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM8	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM9	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM10	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-TRM11	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-BD-5418034-O-NET1	1		DEC SERVER 200 BLOCK DIAGRAM	A	A	A	A	
K-CS-5418034-O-NET2	1		DEV SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-NET3	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-NET4	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-NET5	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-PWR	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-CAP1	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-CAP2	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-D1	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-D2	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	
K-CS-5418034-O-D3	1		DEC SERVER 200 SCHEMATIC	A	A	A	A	

d	i	g	i	t	a	l	TITLE	SHEET 2 OF 2	SIZE	CODE	DOCUMENT NUMBER	REV
							DEC SERVER 200		K	DD	5418034-O-0	0

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CHK	DRAWN BY	ENGINEER	CHANGE NO	DATE	REV
			LK001		B
TH	M. VACHEROT		MK002	2.11.87	C
	H. DENSMORE		MK003	1.11.87	D

CHK	DRAWN BY	ENGINEER	CHANGE NO	DATE	REV

NOTES

1. UL RATING IS LPHR.
2. SPARE LOCATIONS: E11, E41, E50, E113.
4. FOR VARIATION -01, COMPONENTS NOT TO BE INSTALLED ARE: E78, E89, E102, E110, H1, H3-H12, H22, H24.
5. FOR VARIATION -02, COMPONENTS NOT TO BE INSTALLED ARE: E52, E53, E72-E74, E78, E84, E85, E89, E97, E98, E102, E110, H1, H4.

H2, H24, C53, C59, C65-C67, C83, C84, C95, C97, C106, C120, C121, C146, C147, Q2-Q5, R34, R39, R42, R80, R81, J7.

6. FOR VARIATION -03, COMPONENTS NOT TO BE INSTALLED ARE: H2, H3, H5-H17, H22, H24.

SIGNATURES	DATE
DRAWN S. VACHOT	1/26/87
CHECKED F. DROFALO	2/23/87
MECH ENG D. CEDRONE	2/26/86
PROD ENG D. CEDRONE	2/26/86
PROD J. SEGUNO	6/13/86
SCALE 1.5/1	
SHEET 1 OF 2	
ITSP DOC NO K-00-5418034-0-0	

TITLE DEC SERVER 202

MAIN BOARD

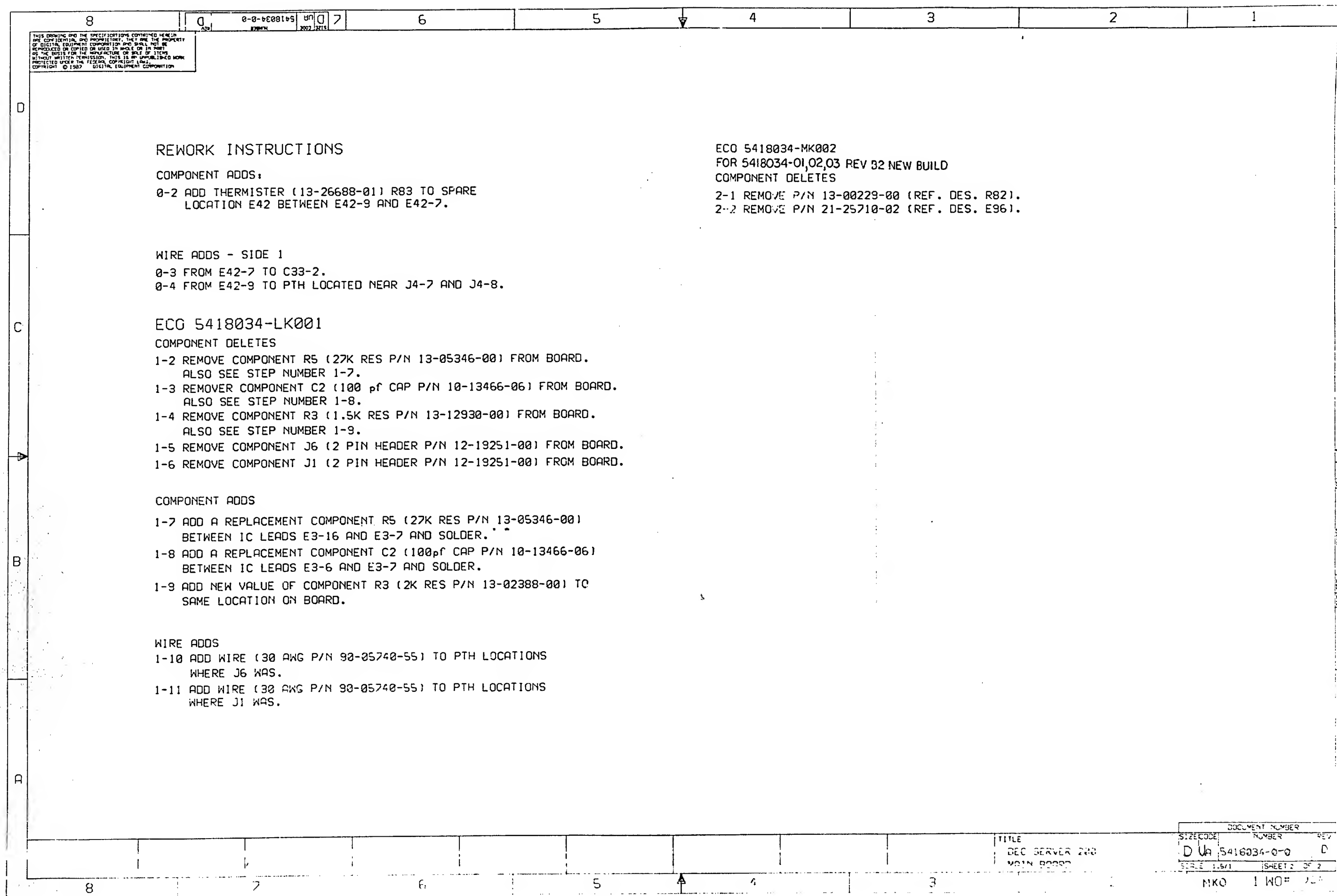
ST. 25 CODE

REV

D UA 5418034-0-0 D

MKO

AC=1924



# REWORK INSTRUCTIONS

## COMPONENT ADDS:

0-2 ADD THERMISTOR (13-26688-01) R83 TO SPARE  
LOCATION E42 BETWEEN E42-9 AND E42-7.

## WIRE ADDS - SIDE 1

0-3 FROM E42-7 TO C33-2.  
0-4 FROM E42-9 TO PTH LOCATED NEAR J4-7 AND J4-8.

## ECO 5418034-LK001

### COMPONENT DELETES

1-2 REMOVE COMPONENT R5 (27K RES P/N 13-05346-00) FROM BOARD.  
ALSO SEE STEP NUMBER 1-7.  
1-3 REMOVE COMPONENT C2 (100 pF CAP P/N 10-13466-06) FROM BOARD.  
ALSO SEE STEP NUMBER 1-8.  
1-4 REMOVE COMPONENT R3 (1.5K RES P/N 13-12930-00) FROM BOARD.  
ALSO SEE STEP NUMBER 1-9.  
1-5 REMOVE COMPONENT J6 (2 PIN HEADER P/N 12-19251-00) FROM BOARD.  
1-6 REMOVE COMPONENT J1 (2 PIN HEADER P/N 12-19251-00) FROM BOARD.

### COMPONENT ADDS

1-7 ADD A REPLACEMENT COMPONENT R5 (27K RES P/N 13-05346-00)  
BETWEEN IC LEADS E3-16 AND E3-7 AND SOLDER.  
1-8 ADD A REPLACEMENT COMPONENT C2 (100pF CAP P/N 10-13466-06)  
BETWEEN IC LEADS E3-6 AND E3-7 AND SOLDER.  
1-9 ADD NEW VALUE OF COMPONENT R3 (2K RES P/N 13-02388-00) TO  
SAME LOCATION ON BOARD.

### WIRE ADDS

1-10 ADD WIRE (30 AWG P/N 90-05740-55) TO PTH LOCATIONS  
WHERE J6 WAS.  
1-11 ADD WIRE (30 AWG P/N 90-05740-55) TO PTH LOCATIONS  
WHERE J1 WAS.

## ECO 5418034-MK002

FOR 5418034-01,02,03 REV 32 NEW BUILD

### COMPONENT DELETES

2-1 REMOVE P/N 13-00229-00 (REF. DES. R82).  
2-2 REMOVE P/N 21-25710-02 (REF. DES. E96).

TITLE  
DEC SERVER 200  
MAIN BOARD

DOCUMENT NUMBER	
SIZE/CODE	NUMBER
D Un	5418034-0-0
SHEET 1 OF 1	SHEET 2 OF 2

MKO 1 WO# 100

AUTOMATED BY VAXCPL (01.3)

P A R T S L I S T

SHEET A1 OF A4

LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY	PER VAR/REV	REFERENCE DESIGNATORS
					01 02 03	01 02 03	
1	1	50-18073-01		DRILL AND ETCH BOARD	1	1	
2	3	10-01610-00		0.010MFD 50V +50/-20% ZSU C	2	2	C132,C133
3	~	10-0217-00		2.0 MFD 25V +75/-10% ALU	2	2	C3,C129
4	5	10-11405-00		100 MFD 10V +/-10% SOL	1	1	C127
5	6	10-12084-01		5.0 MFD 25V +75/-10% ALU	7	7	C1,C32,C52,C70,C95,C137,C140
6	7	10-12734-00		0.047MFD 50V +30/-20% ZSU C	103	- 103	C4-C21,C23,C25-C31,C33-C51, CONT C53-C59,C71-C77,C80-C84, CONT C89-C95,C97,C102-C110, CONT C115-C117,C119-C121,C123,C134, CONT C139,C142,C143,C145
		CONT			- 80	-	C4-C21,C23,C25-C31,C33-C51, CONT C54-C58,C60-C64,C63,C67, CONT C71-C76,C80-C82,C87-C93, CONT C102-C105,C107-C110,C115-C117, CONT C119,C124,C134,C139,C142,C143, CONT C145
7	8	10-13446-05	100	PFD 50V +/- 5% NPO C	3	3	C2,C122,C135
8	9	10-13446-22		0.10 MFD 50V +50/-20% ZSU C	7	- 7	C118,C130,C136,C141,C144,C145, CONT C147
		CONT			- 5	-	C118,C130,C136,C141,C144
9	10	10-13446-34	4700	PFD 50V +/- 5% X7R C	1	1	C131
10	11	10-13446-52	680	PFD 50V +/- 5% NPO C	1	1	C133
11	12	10-17472-00	10	MFD 35V +75/-10% ALU	2	2	C22,C24
12	13	10-25453-01		0.22 MFD 50V +50/-20% ZSU C	-	- 18	C73,C79,C85-C88,C93-C101, CONT C111-C114,C123-C125
		CONT			14	14	C75,C79,C85-C87,C93-C100, CONT C111-C113,C123-C125
13	14	11-16475-00	P1V= 75	- SWS 144148	4	- 4	92-05
14	15	12-13438-00		PCB/HEADER 10PIN(2X055).100CC	1	1	J2
15	16	12-13448-13		PCB/HEADER 16PIN(2X033).100CC STR	1	1	J3
16	18	12-15006-07		*** THIS ITEM IS NOT USED ***	-	-	
17	19	12-15006-19		SKT/IC 64PIN DIP TIN SOLDER	1	1	XE59

REVISION HISTORY		KPL MODULE FORMAT		SECTION A OF ADDRESS		S. MANSOR	
REV	ECO NUMBER	REV	SECTION/VARIATION INDEX	DATE: 14-JAN-86	DATE: 14-JAN-86	DATE: 14-JAN-86	DATE: 14-JAN-86
INITIAL		A	(A) 31,02,03	(M)	(M)	(M)	(M)
DC 1541-034-LK001		B	(B)	(M)	(M)	(M)	(M)
PL 1541-034-LK002		C	(C)	(M)	(M)	(M)	(M)
PL 1541-034-LK003		D	(D)	(M)	(M)	(M)	(M)
		E	(E)	(M)	(M)	(M)	(M)
		F	(F)	(M)	(M)	(M)	(M)
		G	(G)	(M)	(M)	(M)	(M)
		H	(H)	(M)	(M)	(M)	(M)
		I	(I)	(M)	(M)	(M)	(M)
		J	(J)	(M)	(M)	(M)	(M)
		K	(K)	(M)	(M)	(M)	(M)
		L	(L)	(M)	(M)	(M)	(M)
BASIC PART NO: 1541-034				ASSEMBLY NUMBER: 12-0A-5413034-0-0		STOP DOCUMENT NUMBER: 1K-00-5413034-0-0	
FILE NAME: 15413034D.PLS				EDIT: *			
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## P A R T S L I S T

SHEET A2 OF A4

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY 01 93	PER 02 93	VAR/REV 03 93	REFERENCE DESIGNATORS
18	20		12-16652-12		PCB HEADER 16PIN(2X05).156CC STR	1	1	1	J4
19	21		12-19251-00		PCB HEADER 02PIN(1X02).100CC STR	1	1	1	J3
20	22		12-23627-01		PCB HEADER 50POS(2X25).100CC STR	2	-	2	J5,J7
			CONT			-	1	-	J5
21	23		13-00005-07		P. NET 4.7K-15 5.0	3	3	3	E7,E37,E31
22	24		13-00197-00		33.0 .25 W 5.0 X CF	21	21	21	R43-R50,R52,R53,R55-R63,R65,R67
23	25		13-00229-00		100.0 .25 W 5.0 X CF	21	21	21	R14-R17,R19,R20,R22,R24,R26, CONT R28,R31,R33,R35,R37,R39,R51, CONT R54,R56,R59,R74,R79
24	26		13-00247-00		120.0 .25 W 5.0 X CF	2	2	2	R29,R30
25	27		13-00316-00		470.0 .25 W 5.0 X CF	1	1	1	R9
26	28		13-00363-00		1.0 K .25 W 5.0 X CF	1	1	1	R8
27	29		13-00432-00		3.0 K .25 W 5.0 X CF	1	1	1	R71
28	30		13-00539-00		120.0 K .25 W 5.0 X CF	1	1	1	R11
29	31		13-01322-00		180.0 .25 W 5.0 X CF	2	2	2	R27,R32
30	32		13-02332-00		2.0 K .25 W 5.0 X CF	11	11	11	R3,R6,R13,R15,R21,R23,R25,R36, CONT R40,R41,R66
31	33		13-02397-00		180.0 K .25 W 5.0 X CF	2	2	2	R1,R2
32	34		13-02371-00		1.21 K .25 W 1.0 X RN55D-F10	1	1	1	R4
33	35		13-03114-00		1.0 K .25 W 1.0 X RN55D-F10	1	1	1	R7
34	36		13-03179-00		8.20 K .25 W 5.0 X CF	2	2	2	R10,R12
35	37		13-03303-00		2.61 K .25 W 1.0 X RN55D-F10	3	-	3	R36,R39,R42
36	38		13-04341-00		75.0 K .25 W 5.0 X CF	1	1	1	R70
37	39		13-05346-00		27.0 K .25 W 5.0 X CF	1	1	1	R5
38	40		13-05621-00		111.1 .25 W 0.1 X RN55D-B10	2	-	2	R50,R51
39	41		13-09422-00		5.10 .25 W 5.0 X CF	1	1	1	R68
40	42		13-11522-00		200.0 .25 W 5.0 X CF	1	1	1	R76
41	43		13-14150-75		40.20 .25 W 1.0 X RN55D-F10	4	4	4	R73,R75,R77,R78
42	44		13-16247-00		510.0 .25 W 5.0 X CF	1	1	1	R72
43	45		13-26578-01		THERMISTOR PTC 0.062 15K	1	1	1	R83
44	46		16-16555-01		DELAY= 100NS,10TAPS WITH TTLBU	1	1	1	E92
45	47		16-23072-01		XF49,75UH	1	1	1	E115
46	48		18-11660-50		OSCILLATOR, XTAL 29.4912 MHZ	1	1	1	Y1
47	49		18-22515-01		XTAL 20.000 MHZ	1	1	1	Y2
48	50		19-12108-01		LM 339ANVOLT. CMPTR,QUAD	1	1	1	E1
49	51		19-12779-00		LS00 NAND-GATE-QUAD 2IN,P	1	1	1	E9
50	52		19-12875-00		LS08 AND GATE-QUAD 2IN,P	2	2	2	E5,E49
51	53		19-12810-00		LS20 NAND GATE-DUAL 4IN	1	1	1	E16
52	54		19-12811-00		LS21 AND GATE-DUAL 4IN,P	1	1	1	E4
53	55		19-12816-00		LS32 OR GATE-QUAD 2IN,POS	2	2	2	E8,E43
54	56		19-12812-00		LS33 NAND BUFFER-QUAD 2IN	1	1	1	E25
55	57		19-12824-00		LS74 FF-D DUAL,EDGE TRIGG	2	2	2	E2,E28
56	58		19-12837-00		LS123 ONE SHOT-DUAL,RETRIG	2	2	2	E3,E114
57	59		19-12845-00		LS153 MUX 1 OF 4 (DUAL)	1	1	1	E15
58	60		19-12847-00		LS157 MUX 1 OF 2 (QUAD)	3	3	3	E13,E33,E40
59	61		19-12842-00		LS158 MUX 1 OF 2 (QUAD)	1	1	1	E12
60	62		19-12843-00		LS273 FF-D OCTAL W/CLEAR	1	1	1	E69
61	63		19-13114-00		LS14 INVERTER GATE-HEX SC	1	1	1	E104
62	64		19-14045-00		74S260 NOR GATE-DUAL,POS	1	1	1	E45
63	65		19-15193-00		LS244 DRIVER,LINE,OCTAL,TR	1	1	1	E34

SIZE	CODE	DOCUMENT NUMBER	REV
K	PL	5418034-0-DBP	D

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## P A R T S   L I S T

SHEET A3 OF A4

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VAR/REV			REFERENCE DESIGNATORS
						Q1	Q2	Q3	
						93	93	93	
64	67		19-19006-00	LM	336A VOLT REF. 2.49V 1X	1	1	1	D1
65	68		19-19016-00		LS143 PRIORITY ENCODER, 3 T	4	4	4	E26, E33, E35, E47
66	69		19-20441-01		74F245 TRANSCEIVER, BI-DIREC	2	2	2	E60, E71
67	70		19-20442-01		74F374 FF-D, OCTAL, T41-STATE	3	3	3	E36, E39, E51
68	71		19-20572-01		74F244 BUFFER/LINE DRIVER, 0	2	2	2	E56, E68
69	72		19-21009-01		74F241 BUFFER/LINE DRIVER, 4	1	1	1	E80
70	73		19-21010-01		74F373 OCTAL TRANSPARENT LA	4	4	4	E45, E82, E94, E106
71	74		19-21305-01		74F00 NAND GATE, QUAD, 2-IN	1	1	1	E57
72	75		19-21306-01		74F02 NOR GATE, QUAD, 2-IN	1	1	1	E6
73	76		19-21307-01		74F04 HEX INVERTER	3	3	3	E16, E44, E91
74	77		19-21308-01		74F08 AND GATE, QUAD, 2-IN	1	1	1	E58
75	78		19-21311-01		74F20 NAND GATE, DUAL, 4-IN	2	2	2	E27, E90
76	79		19-21312-01		74F32 OR GATE, QUAD, 2-IN	3	3	3	E23, E29, E62
77	80		19-21314-01		74F74 FF-D, DUAL, POS., EDGE	6	6	6	E63, E61, E63, E66, E70, E105
78	81		19-21315-01		74F55 EXCLUSIVE OR GATE, QU	1	1	1	E54
79	82		19-21316-01		74F135 DECODER/DEMUX, 1-OF-3	1	1	1	E24
80	83		19-21317-01		74F139 DECODER, DUAL, 1-OF-4	1	1	1	E79
81	84		19-21322-01		74F161 BINARY COUNTER, PRES	1	1	1	E10
82	85		19-21331-01		74F253 MUX, QUAD, 2-IN	3	3	3	E103, E111, E112
83	86		19-21332-01		74F250 PARITY GEN/CHECK, 2-B	2	2	2	E55, E67
84	87		19-21671-03		INTERFACE, SERIAL ADAPTOR 300MIL	1	1	1	E115
85	88		19-22191-01		74F353 MULTIPLEXER, DUAL 4I	1	1	1	E93
86	89		19-23339-02		EIA OCTAL LINE DRIVE	3	-	3	E53, E74, E85
87	90		19-23390-02		EIA OCTAL LINE RECI	5	-	5	E52, E73, E84, E97, E98
88	91		19-25705-01		74F37 QUAD 2-INPUT NAND BU	1	1	1	E83
89	92		21-20057-01		68000 MICROPROCESSOR, 16BIT	1	1	1	E59
90	93		21-20063-01		2431 REC./TRANS., ASYNC., D	4	4	4	E19-E22
91	94		21-21414-02		50256 RAM 256KX1 DYNAMIC M	2	2	2	E64, E65
92	95		21-21672-07		CONTROLLER, LOCAL AREA NET	1	1	1	E95
93	96	SEE NOTE 4	21-22421-02		*** THIS ITEM IS NOT USED ***	-	-	-	
94	97	SEE NOTE 4	21-22422-02		*** THIS ITEM IS NOT USED ***	-	-	-	
95	98		21-24412-02		RAM, 64KX4, DYNAMIC 15	12	12	-	E75-E77, E86-E88, E99-E101,
			CONT			-	-	16	CONT E107-E109
									E75-E78, E86-E89, E92-E102,
									CONT E107-E110
96	99	SEE NOTE 2	21-24414-02		*** THIS ITEM IS NOT USED ***	-	-	-	
97	100				*** THIS ITEM IS NOT USED ***	-	-	-	
98	101		21-25874-02		0070539 DEC SERVER GATE ARRAY	1	-	1	E72
99	102		23-16343-00		K3-03 PAL, QUAD, AND-0	1	1	1	E30
100	103		23-27365-00		65-01, 65-02, 65-03 ROM	1	1	1	E31
101	104		23-27365-00		65-01, 65-02, 65-03 ROM	1	1	1	E17
102	105		23-36541-00		A1-09	1	1	1	E32
103	106		23-441 -00		N1-04	1	1	1	E18
104	107		70-09125-00		JUMPER, WIRE, INSULATED, BLACK 8	6	-	-	W2, W15-W21, W23
			CONT			-	-	7	W1, W4, W13-W21, W23
						-	20	-	W2, W3, W5, W6, W8-W23
105	108	NOTE	91-35740-55		WIPE(WRAP) 30AWG KYNAR UL14	1	1	1	
106	109	NOTE	91-37727-00		WIPE(WRAP) 25AWG KYNAR UL14	6	6	6	

DATE	BY	CHKD	APPD	TITLE	SECTION	SIZE	CODE	DOCUMENT NUMBER	REV
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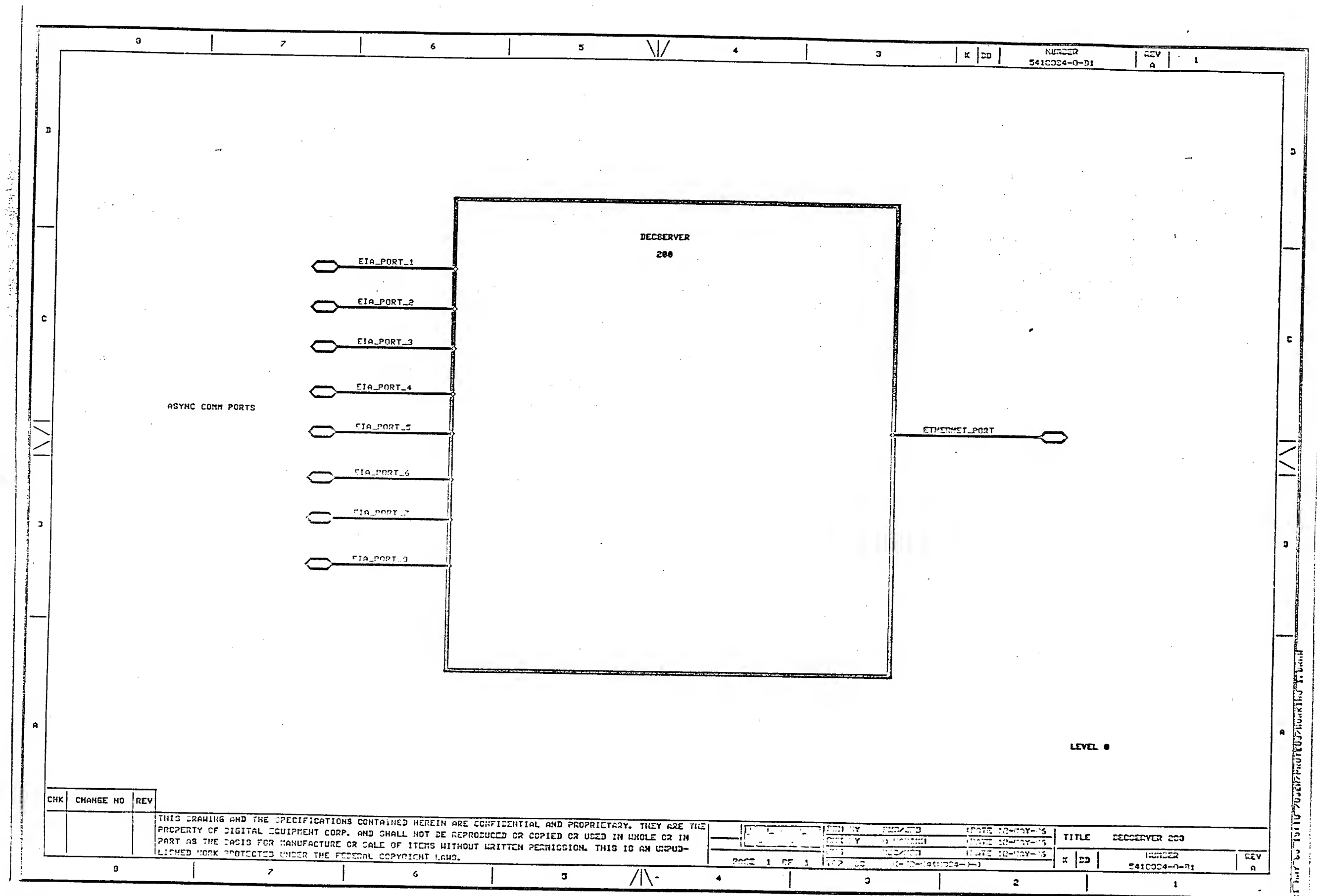
# PARTS LIST

SHEET A4 OF A4

LINE ITEM	TSP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VAR/REV 01 02 03 93 93 93	REFERENCE DESIGNATORS
-----------	--------------	-------------	------------	-------------	---	-----------------------

1 GEN: 1. SPARE LOCATIONS: E11, E41, E50, E113.  
2 GEN: 2. P/N 21-24414-02 MAY BE SUBSTITUTED FOR P/N 21-24412-02, USING THE SAME REFERENCE DESIGNATORS.  
3 GEN: 3. P/N 21-22421-02 OR 21-22422-02 MAY BE SUBSTITUTED FOR P/N 21-21414-02, USING THE SAME REFERENCE DESIGNATORS.  
4 GEN: 4. FOR VARIATION -01, COMPONENTS NOT TO BE INSTALLED ARE: E78, E39, E102, E110, W1, W3, W17, W22, W24, C88, C101, C114, C126.  
5 GEN: 5. FOR VARIATION -02, COMPONENTS NOT TO BE INSTALLED ARE: E52, E53, E72, E74, E78, E84, E85, E89, E97, E99, E102, E110, W1, W4, W7,  
6 GEN: CONTINUED W24, C53, C59, C65, C67, C77, C33, C34, C94, C95, C97, C105, C120, C121, C146, C147, C83, C101, C114, C126, D2, D5, R34,  
7 GEN: CONTINUED R39, R42, R50, R51, J7.  
8 GEN: 6. FOR VARIATION -03, COMPONENTS NOT TO BE INSTALLED ARE: W2, W3, W5, W17, W22, W24.  
9 GEN: NOTE: QUANTITY OF ITEM #103 AND ITEM #109 IS SHOWN IN INCHES.

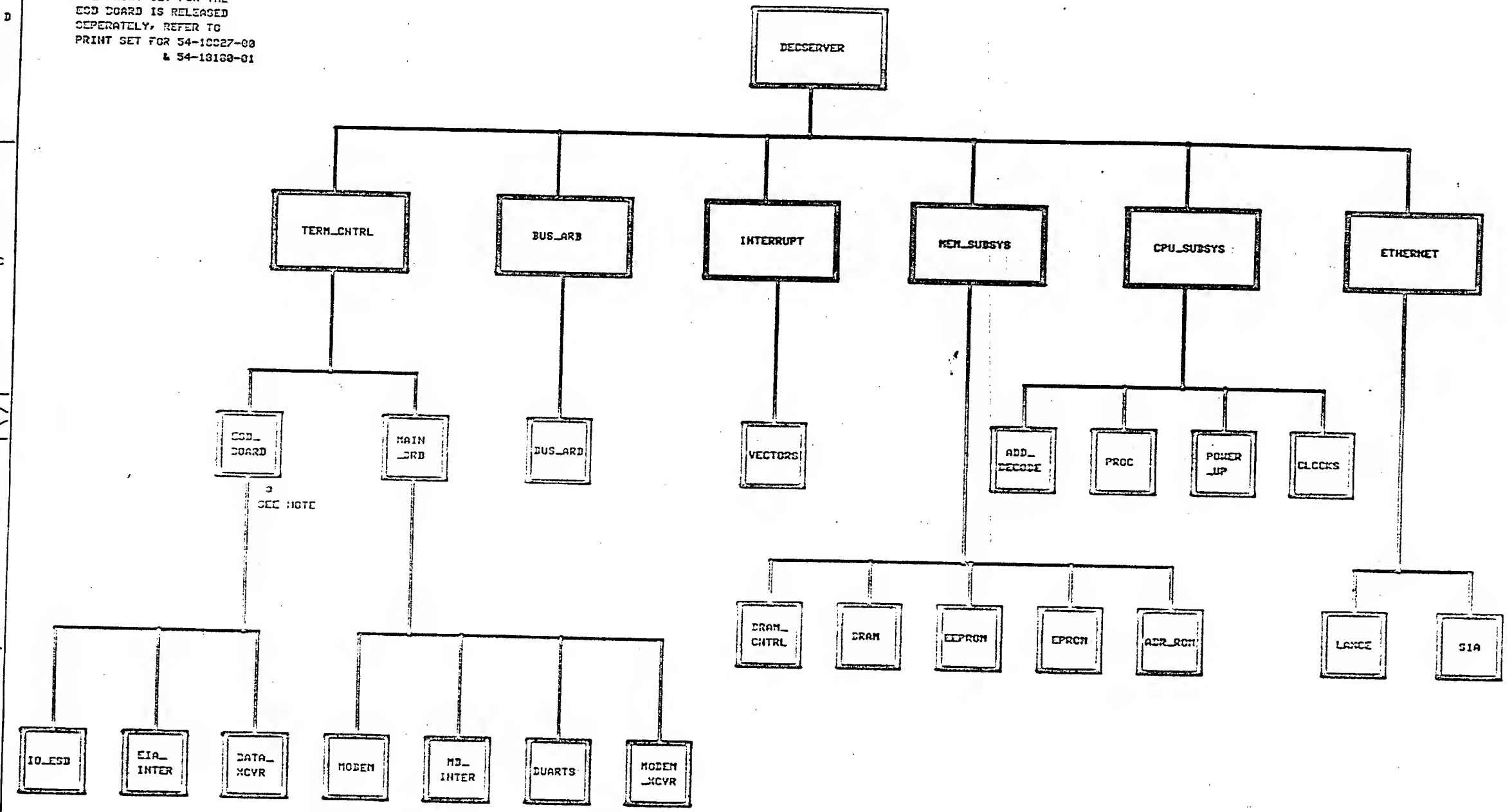
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NOTE:

THE PRINT SET FOR THE  
ESD BOARD IS RELEASED  
SEPARATELY. REFER TO  
PRINT SET FOR 54-10027-00  
& 54-18180-01

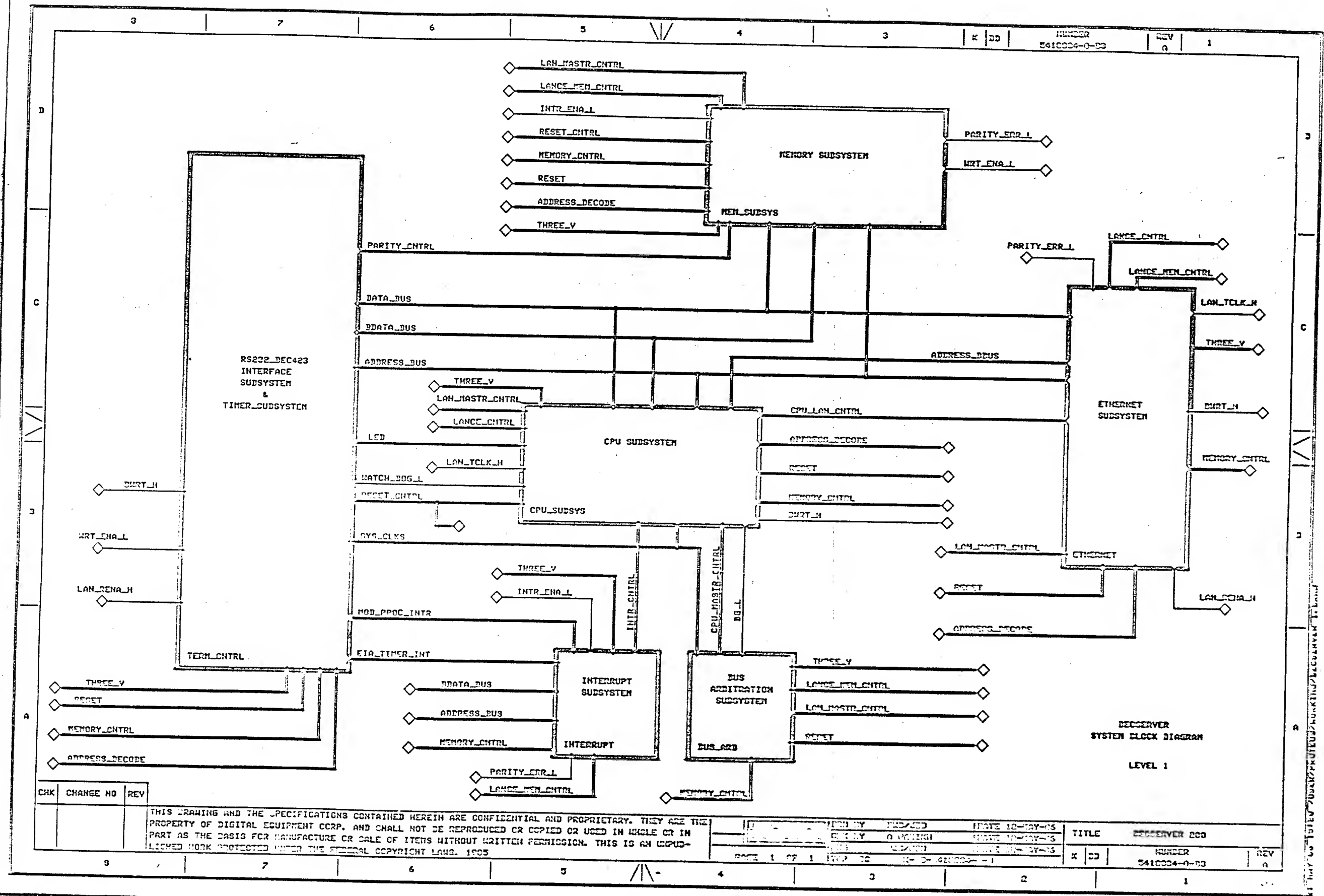


CHK	CHANGE NO	REV

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CHK BY	CHK BY	CHK BY	NUMBER	5410024-0-02
DATE 12-11-86	DATE 12-11-86	DATE 12-11-86	REV	0
DATE 12-11-86	DATE 12-11-86	DATE 12-11-86	REV	0

DOCUMENTATION TREE

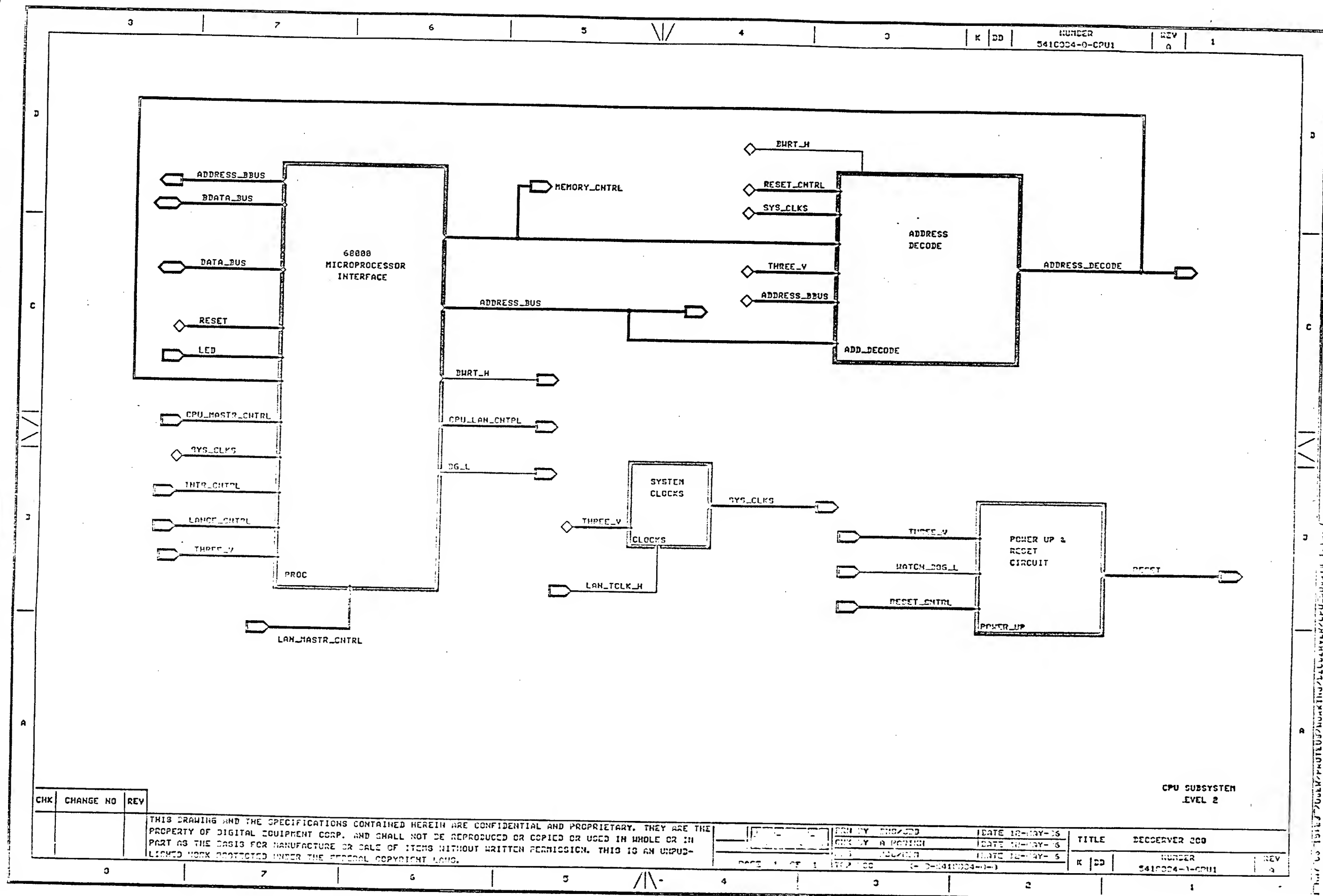


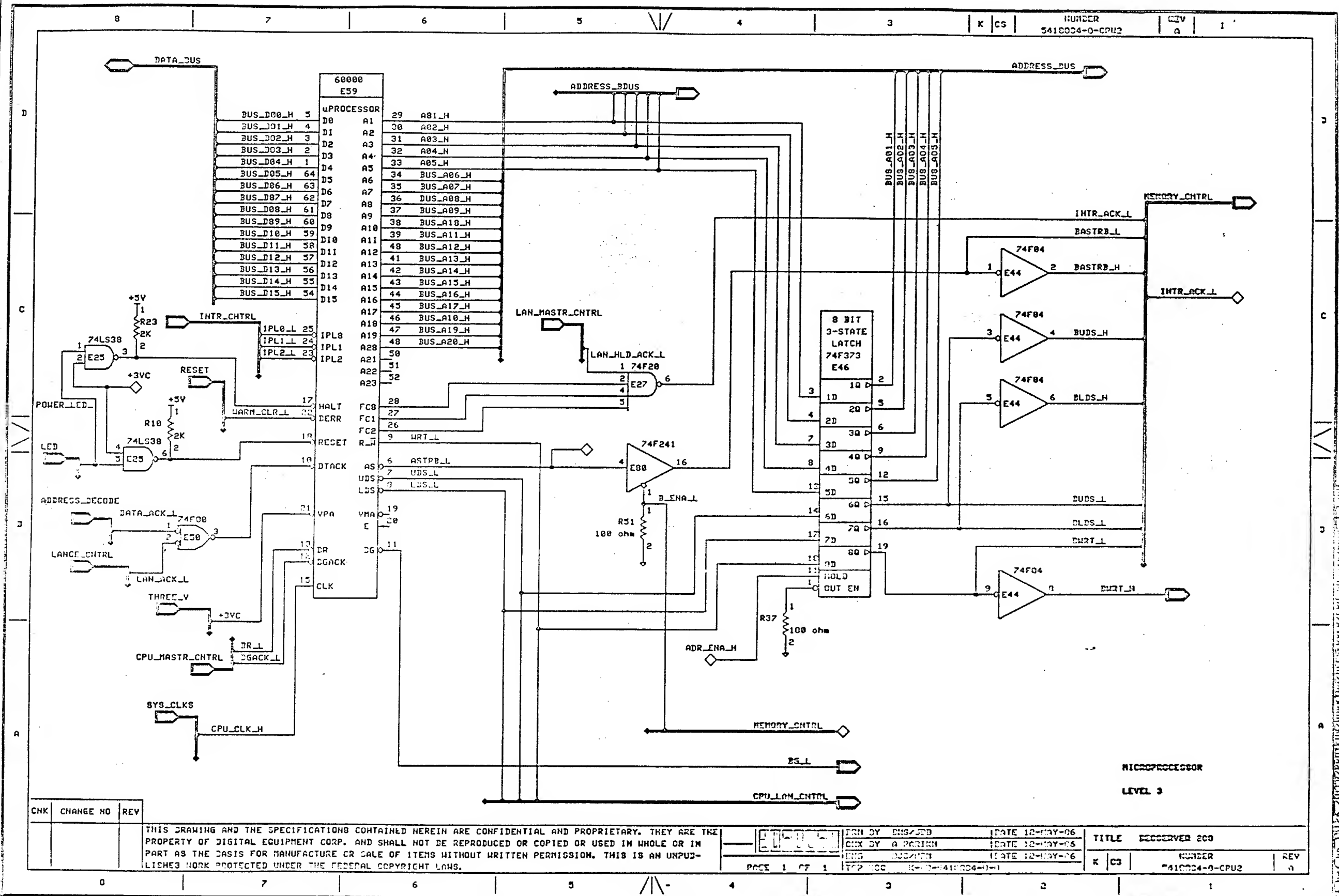
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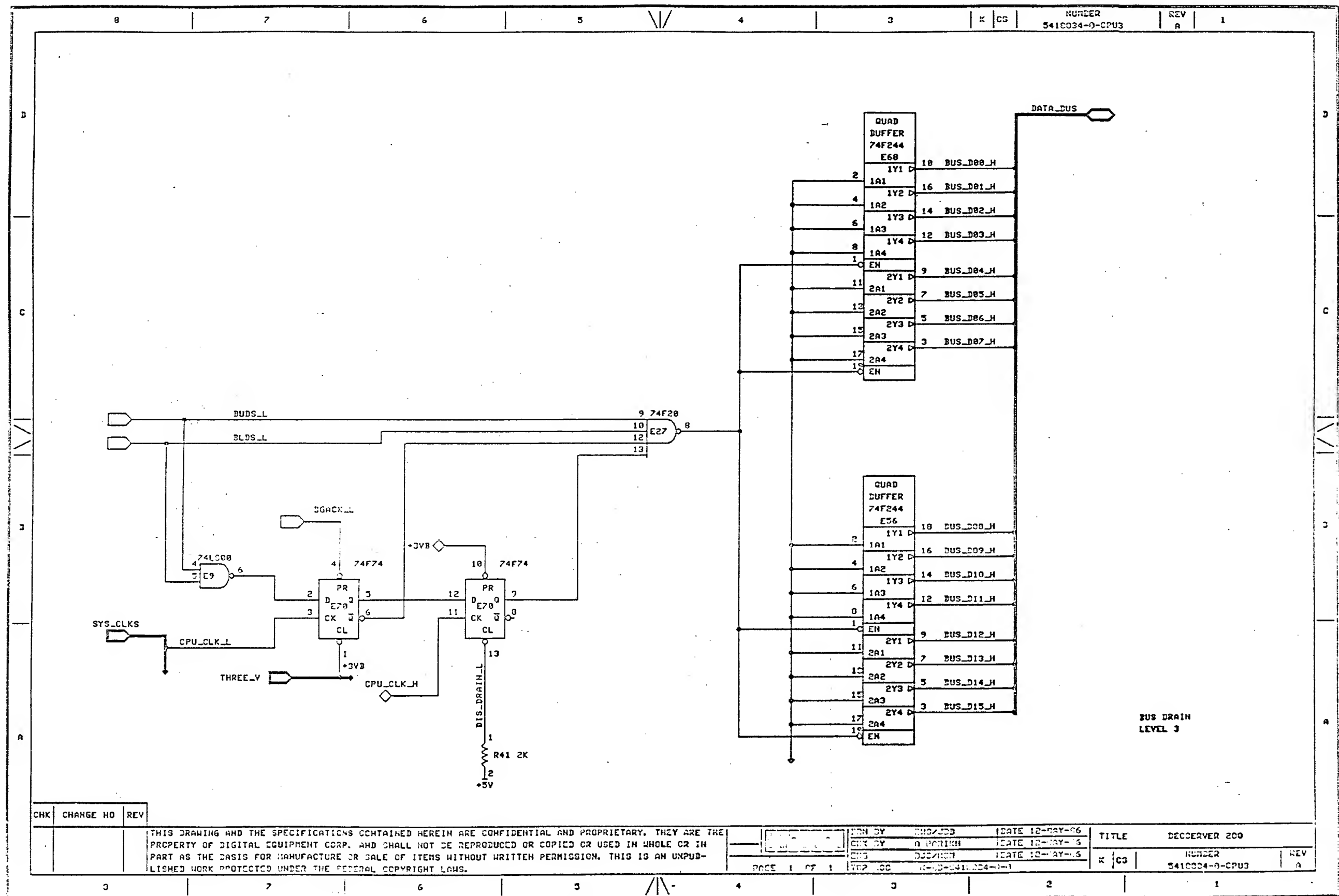
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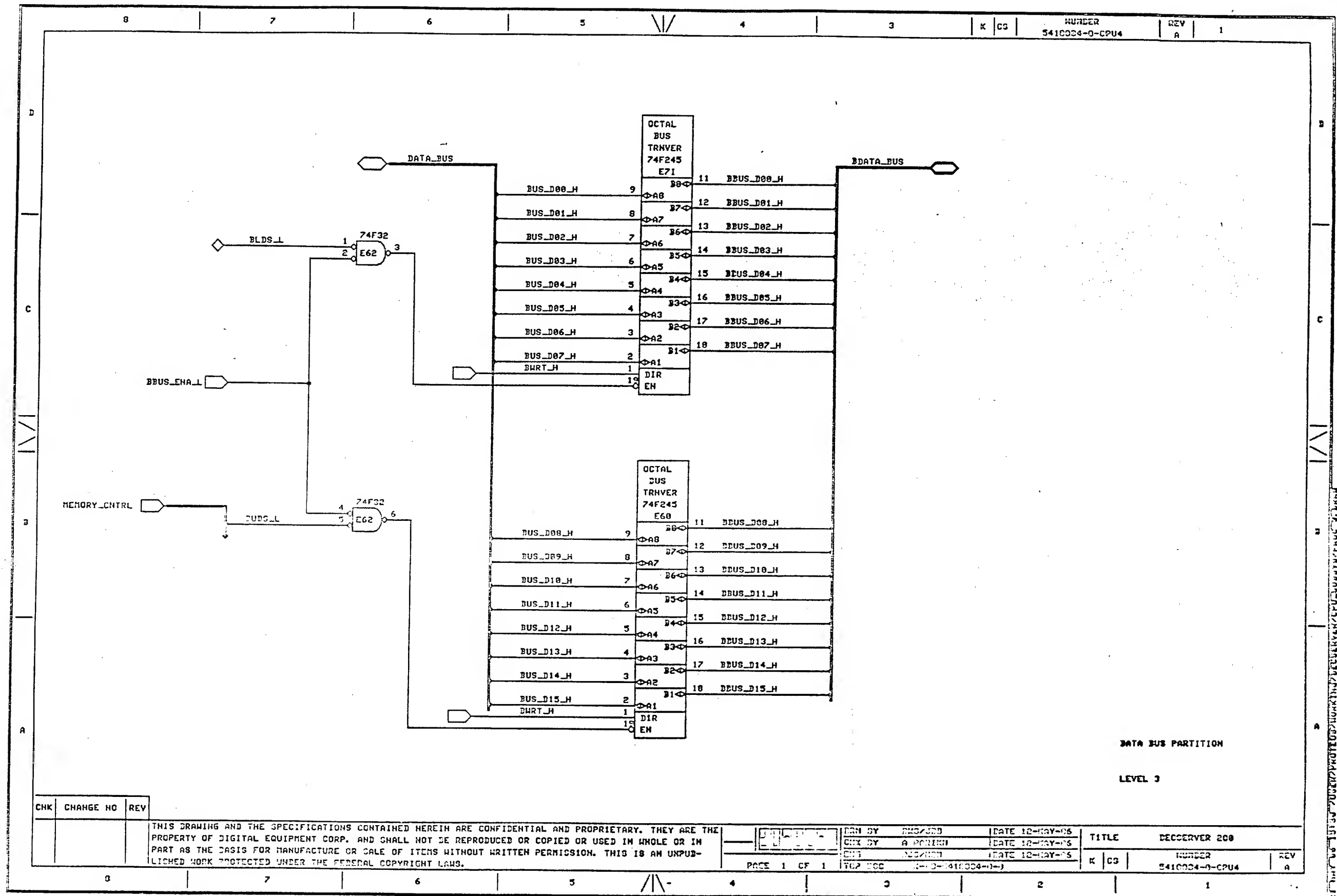
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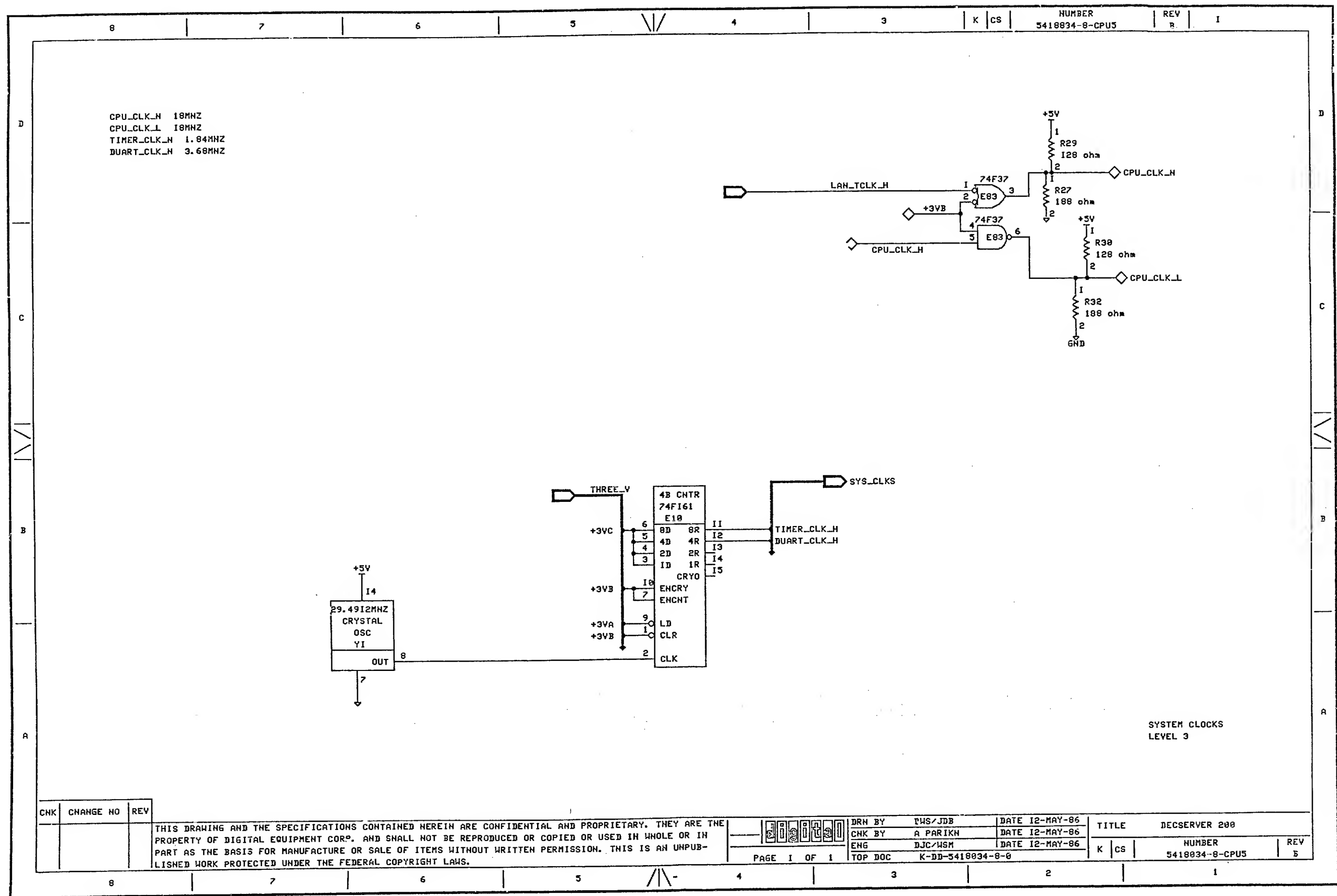
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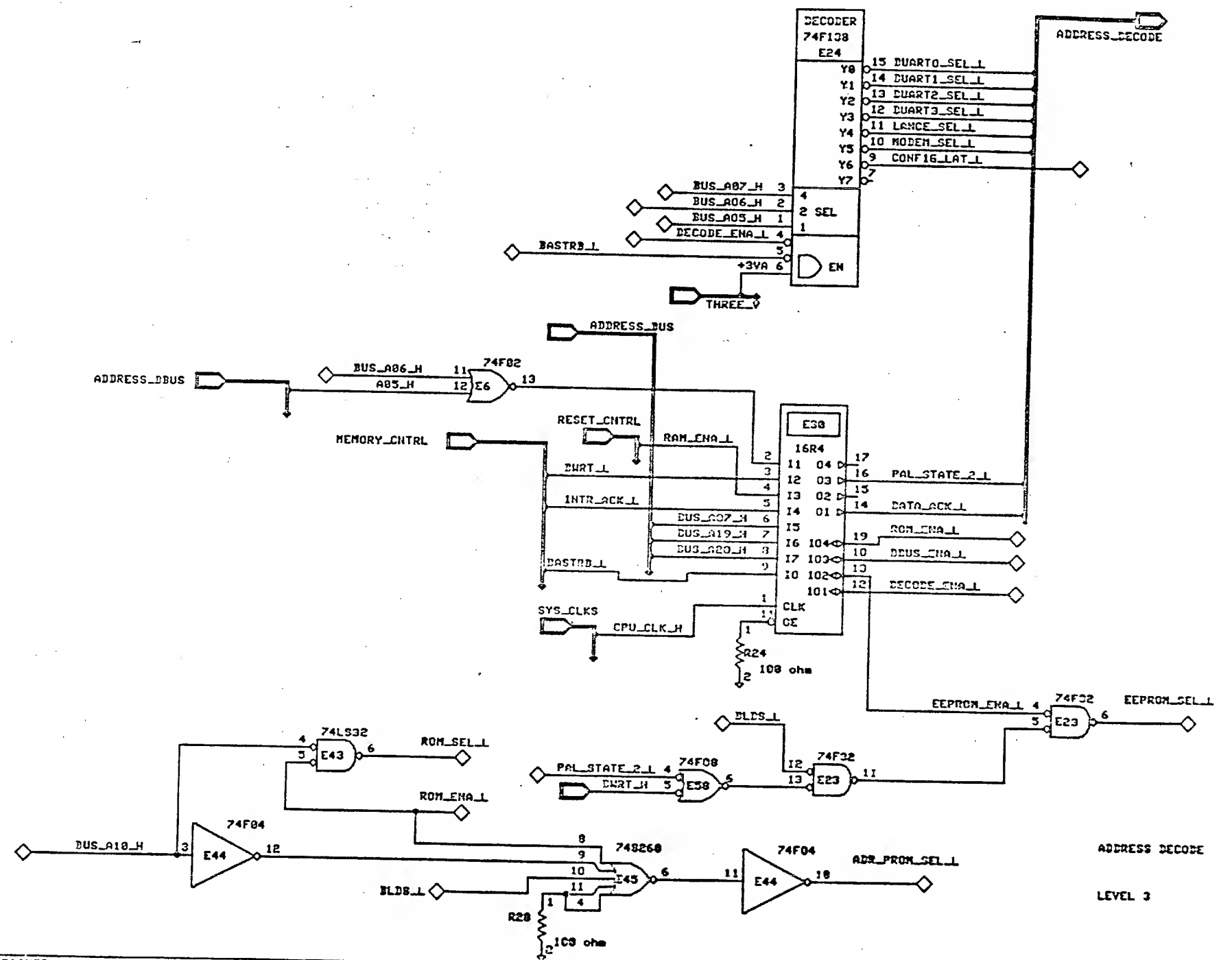






DEVICE	ADDRESS
RAM	0-1FFFF BANK 0 20000-3FFFF BANK 1 40000-5FFFF BANK 2 60000-7FFFF BANK 3
EEPROM	80001-80FFF
DUART 0	100001-10001F
DUART 1	100021-10003F
DUART 2	100041-10005F
DUART 3	100061-10007F
LANCE	100080-100002
CONFIG LATCH	1000C1
GATE ARRAY	1000A1-1000AB
EPROM	100000-107FFF
ADR PROM	1C0001-1C003F

RAM\_SEL\_L DOES NOT APPEAR ON THIS PAGE  
RAM IS DECODED ON THE DRAM CONTROLLER PAGES

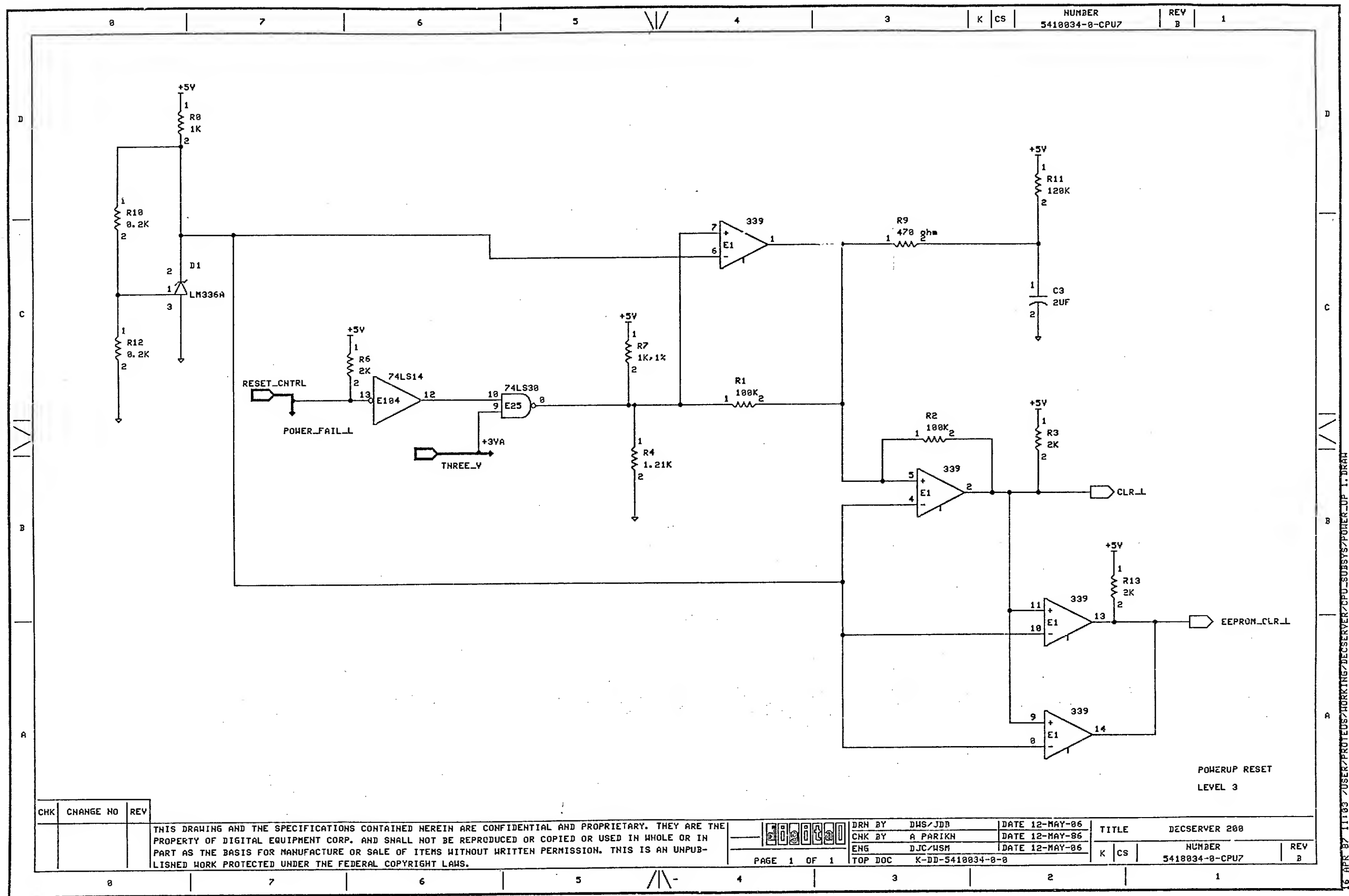


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DATE 12-MAY-86	DATE 12-MAY-86	DATE 12-MAY-86	TITLE	DISCOVER 200
CHK BY A. P. BROWN	DATE 12-MAY-86	DATE 12-MAY-86	NUMBER	5410034-0-CPU5
DATE 12-MAY-86	DATE 12-MAY-86	DATE 12-MAY-86	REV	a
PAGE 1 OF 1	YCP 100	K-10-3410034-0-1		





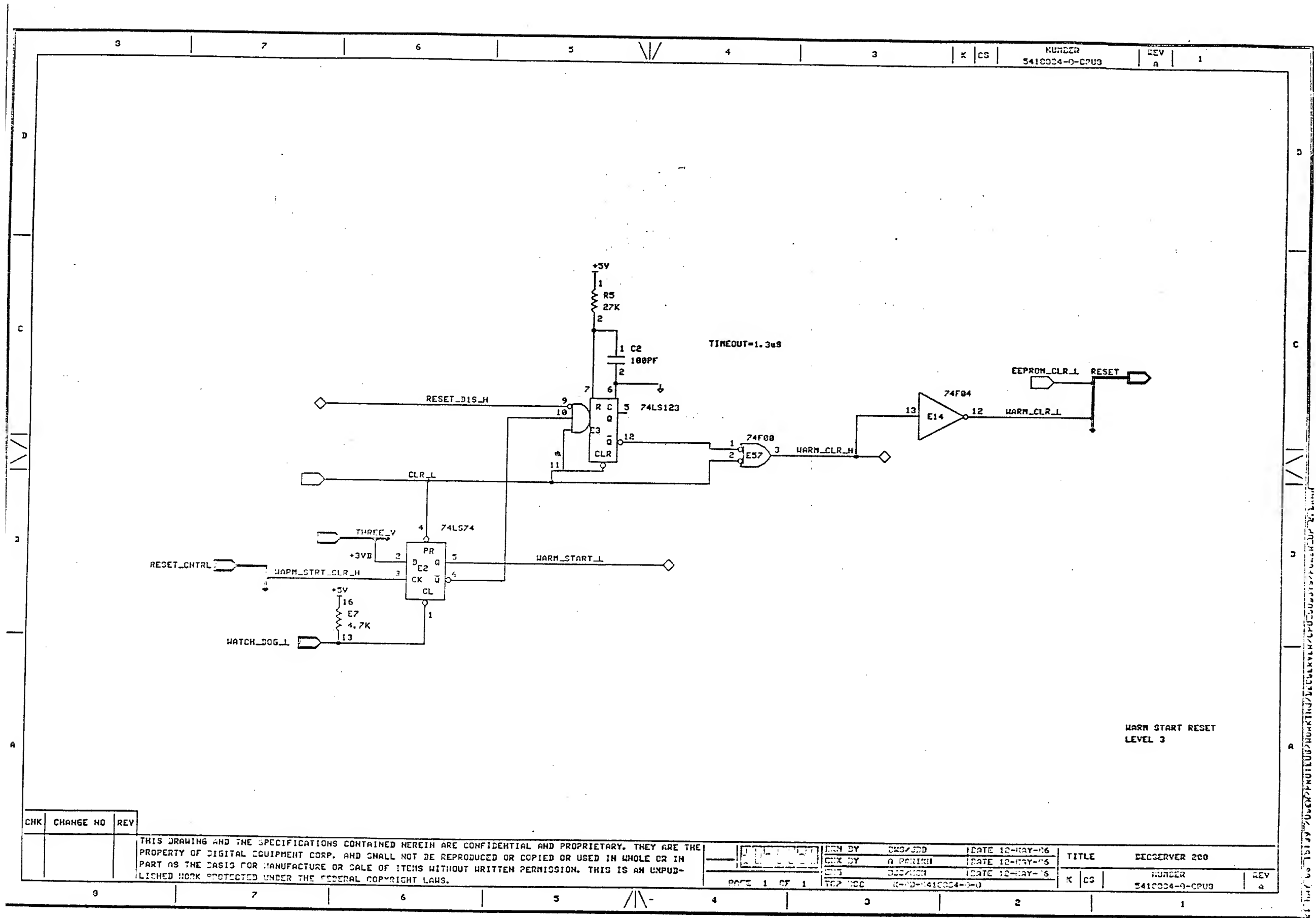
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16 APR 87 11:03 / USER:PROTEUS/HOKING/DECSEVER/CPU\_S01SYS/POWERUP 1.DRAW

DRN BY	DWS/JDB	DATE	12-MAY-86
CNK BY	A PARIKH	DATE	12-MAY-86
ENG	DJC/HSM	DATE	12-MAY-86
TOP DOC	K-DD-5418034-0-0		

TITLE		DECSEVER 200	
K	CS	NUMBER	5418034-0-CPU7
		REV	B



WARM START RESET  
LEVEL 3

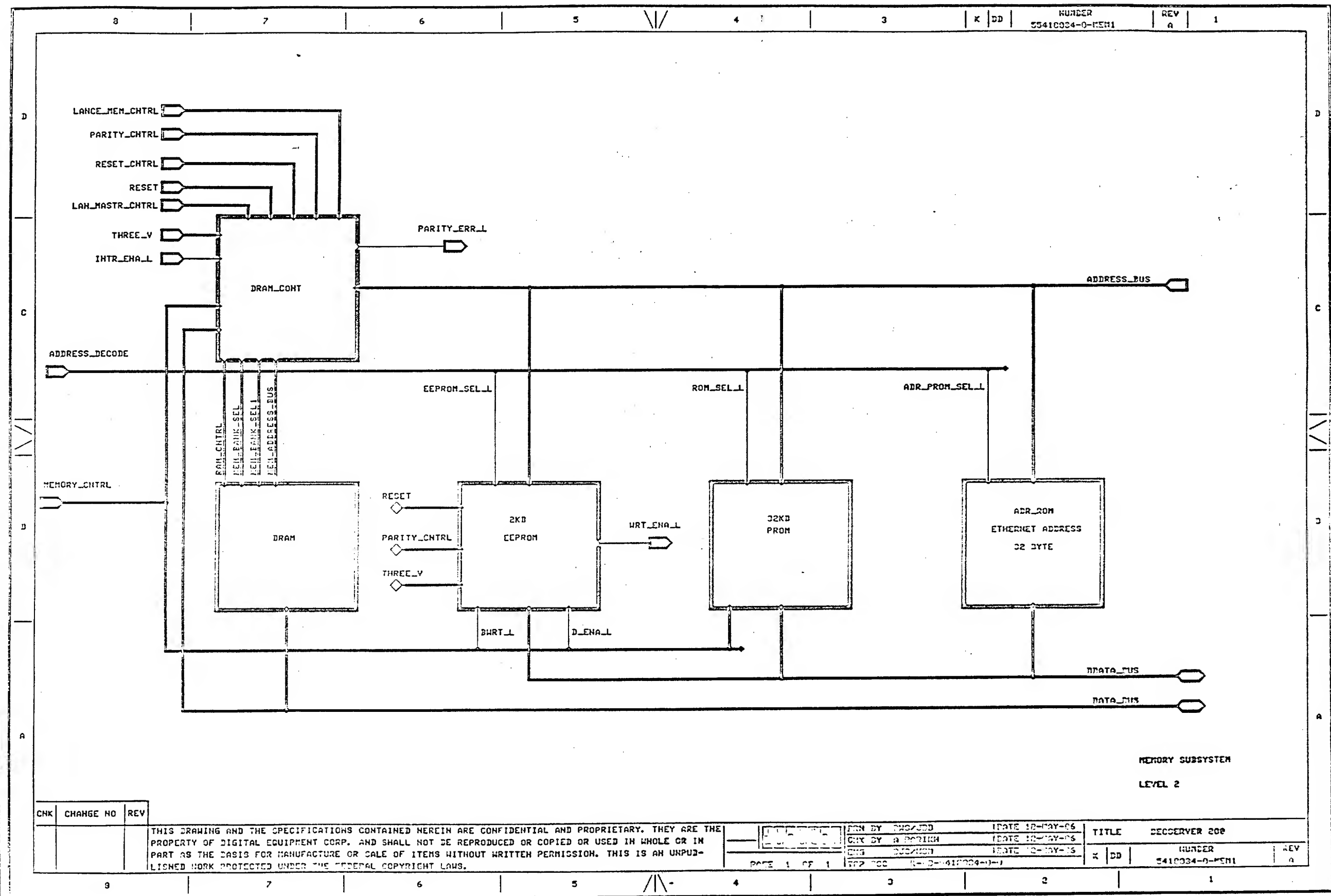
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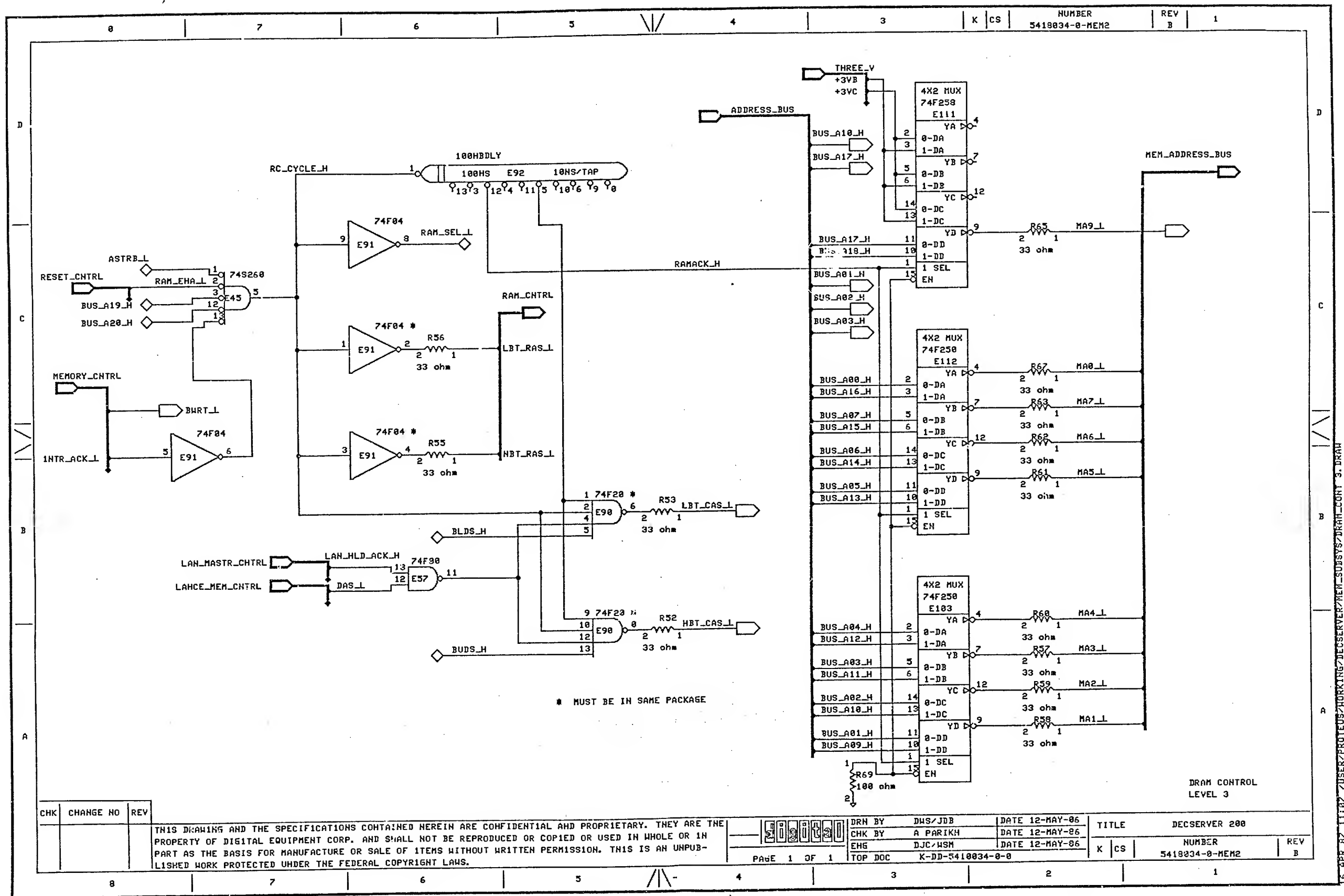
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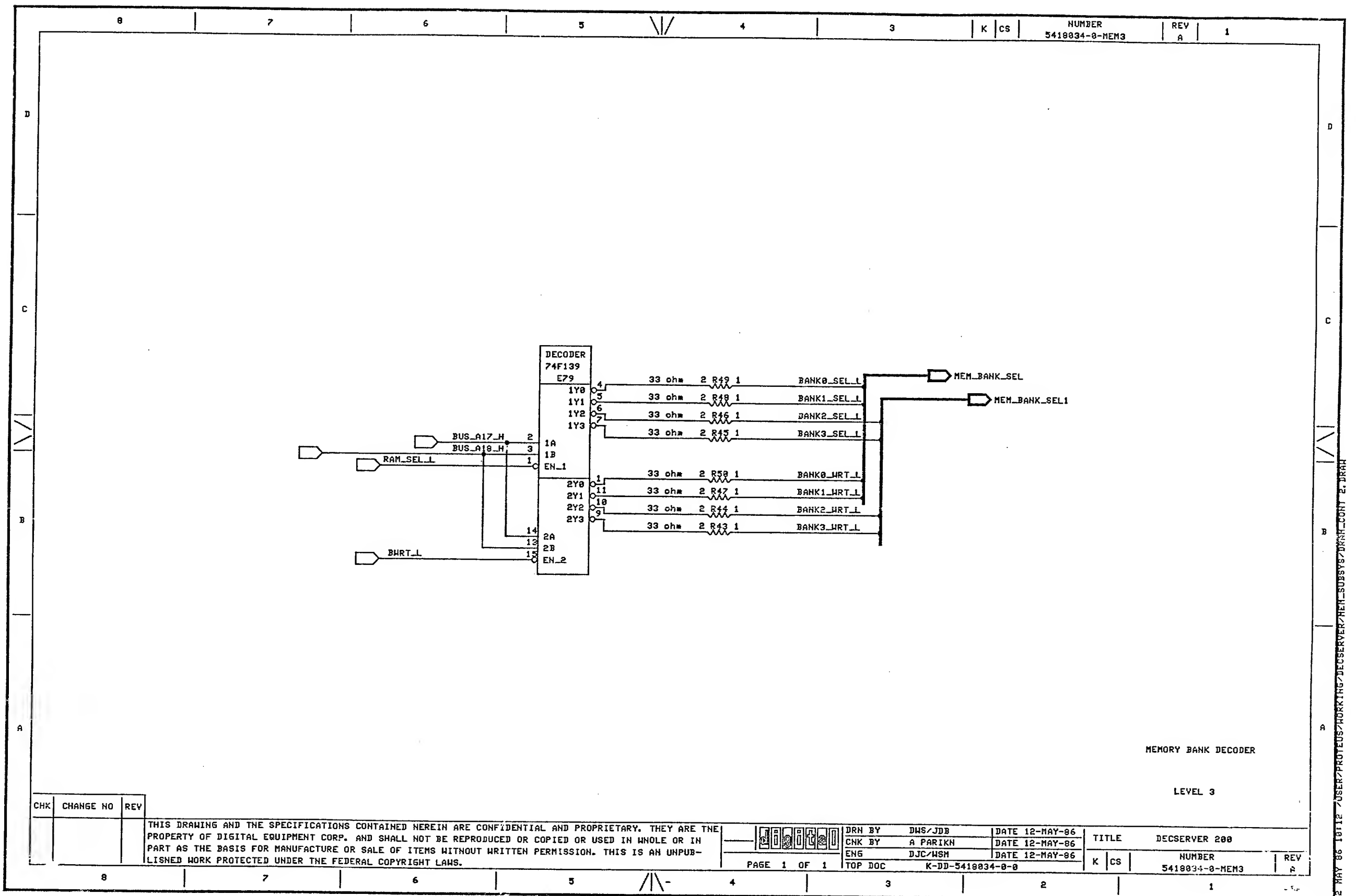
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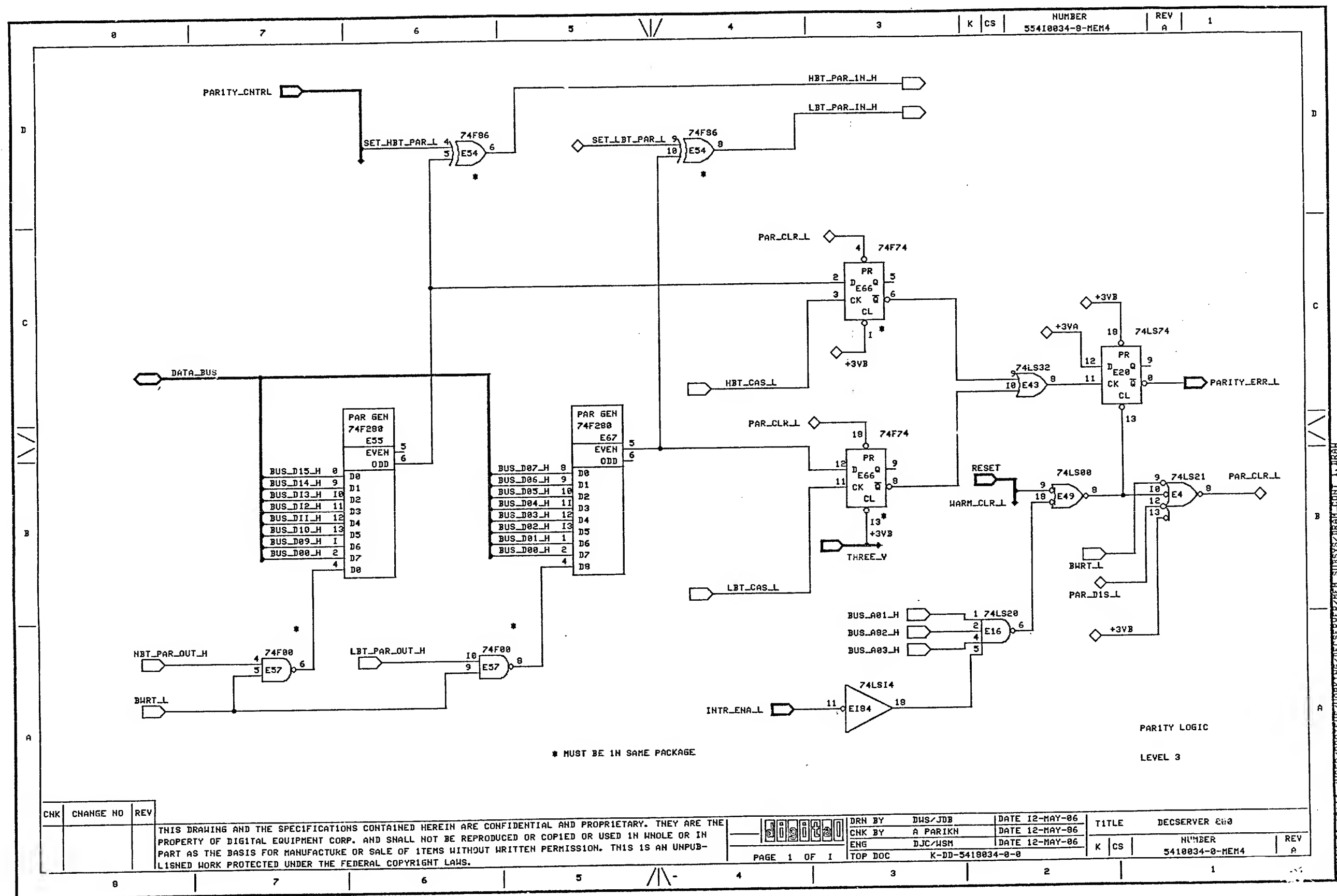
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CHECKED BY	DATE
ENG	DATE
TOP DOC	DATE

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NUMBER	5410004-0-CPU3
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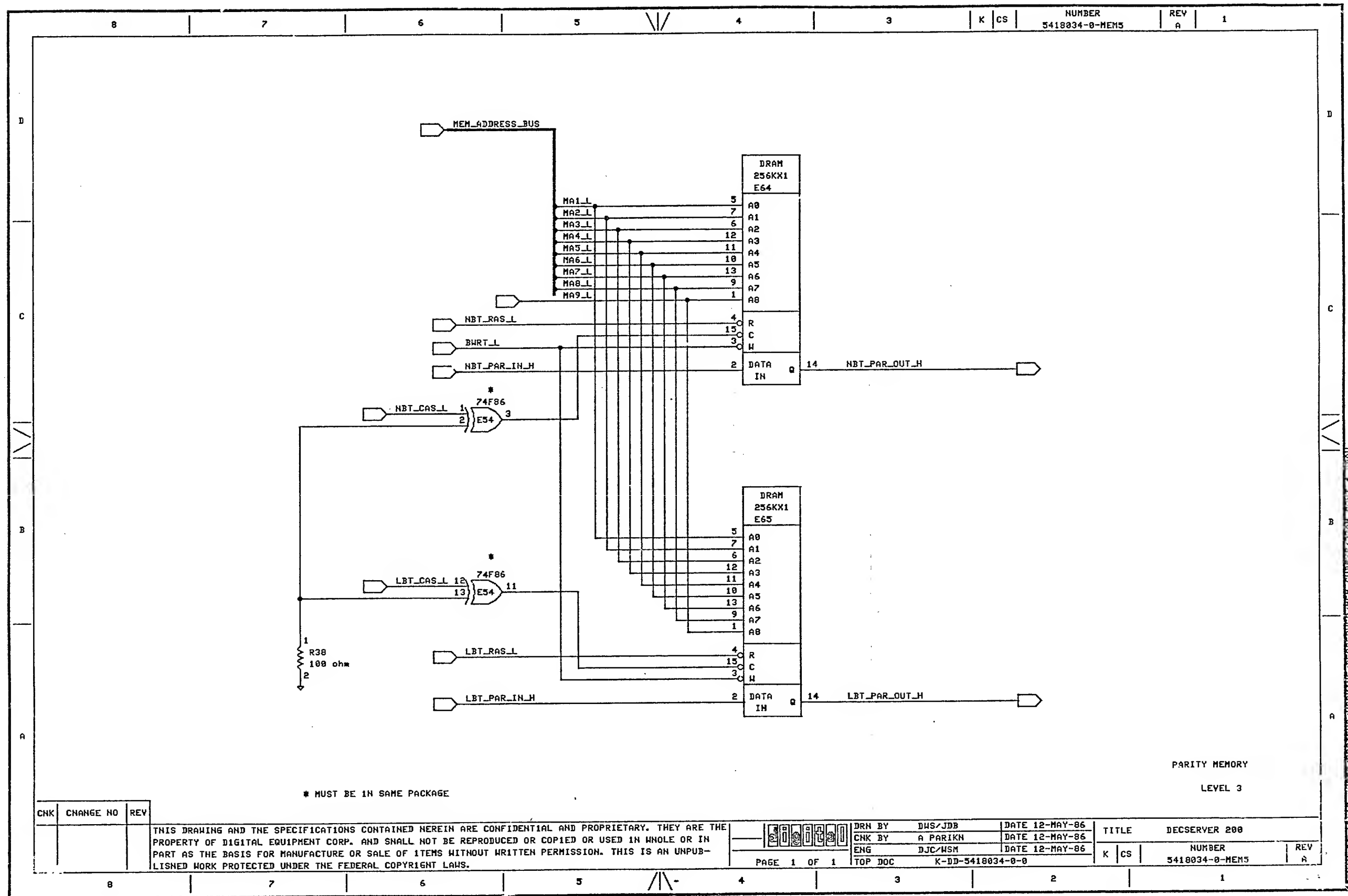






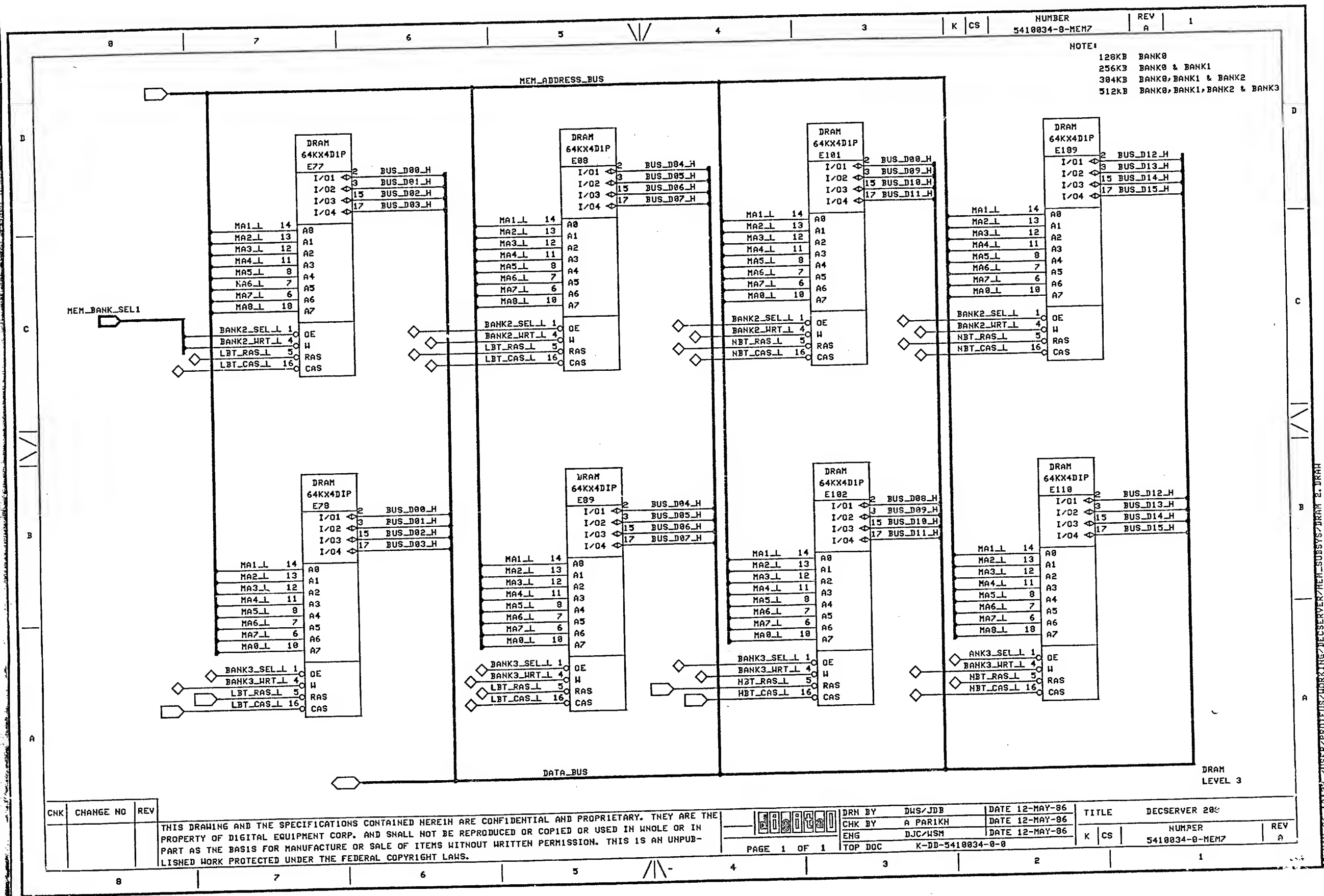


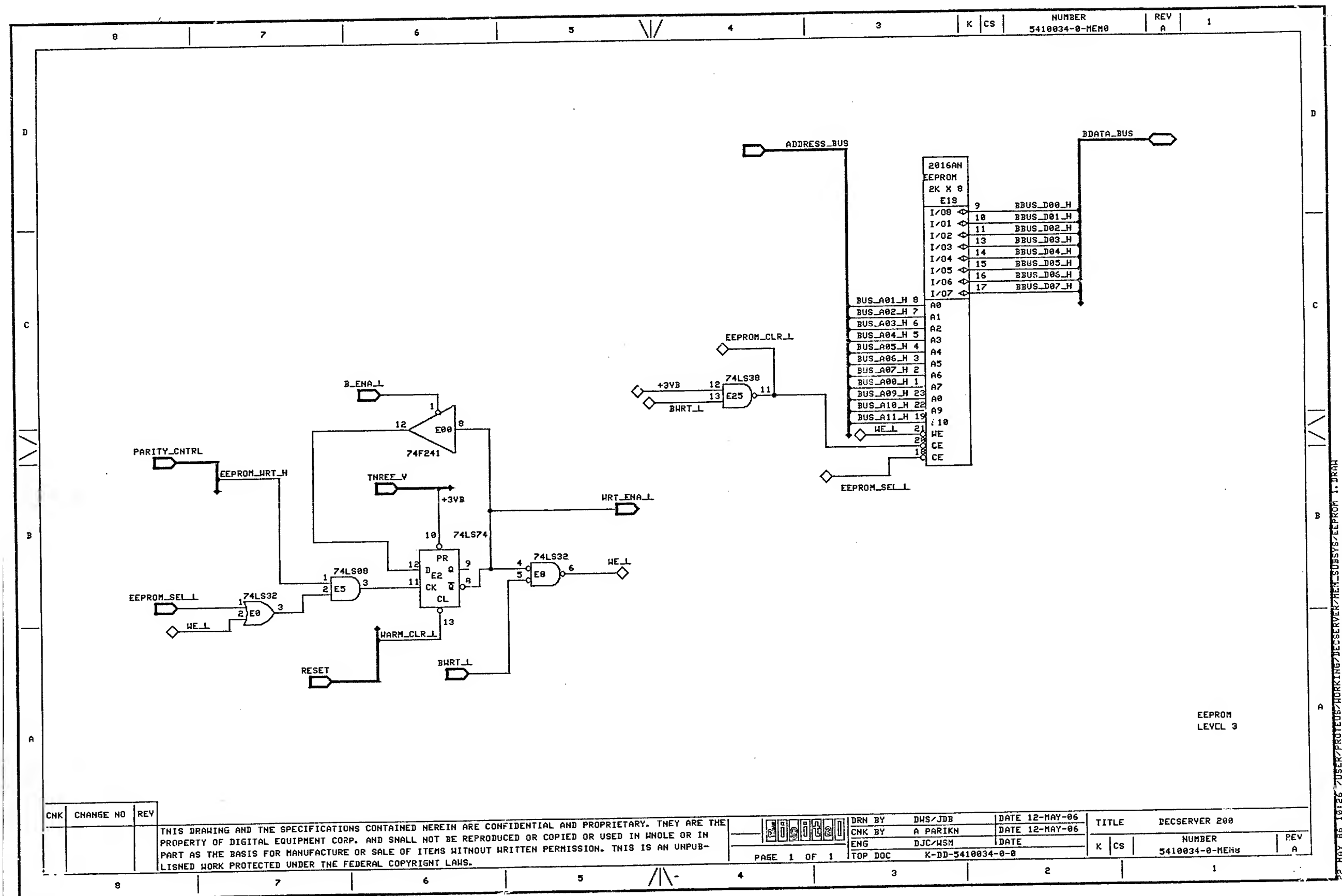
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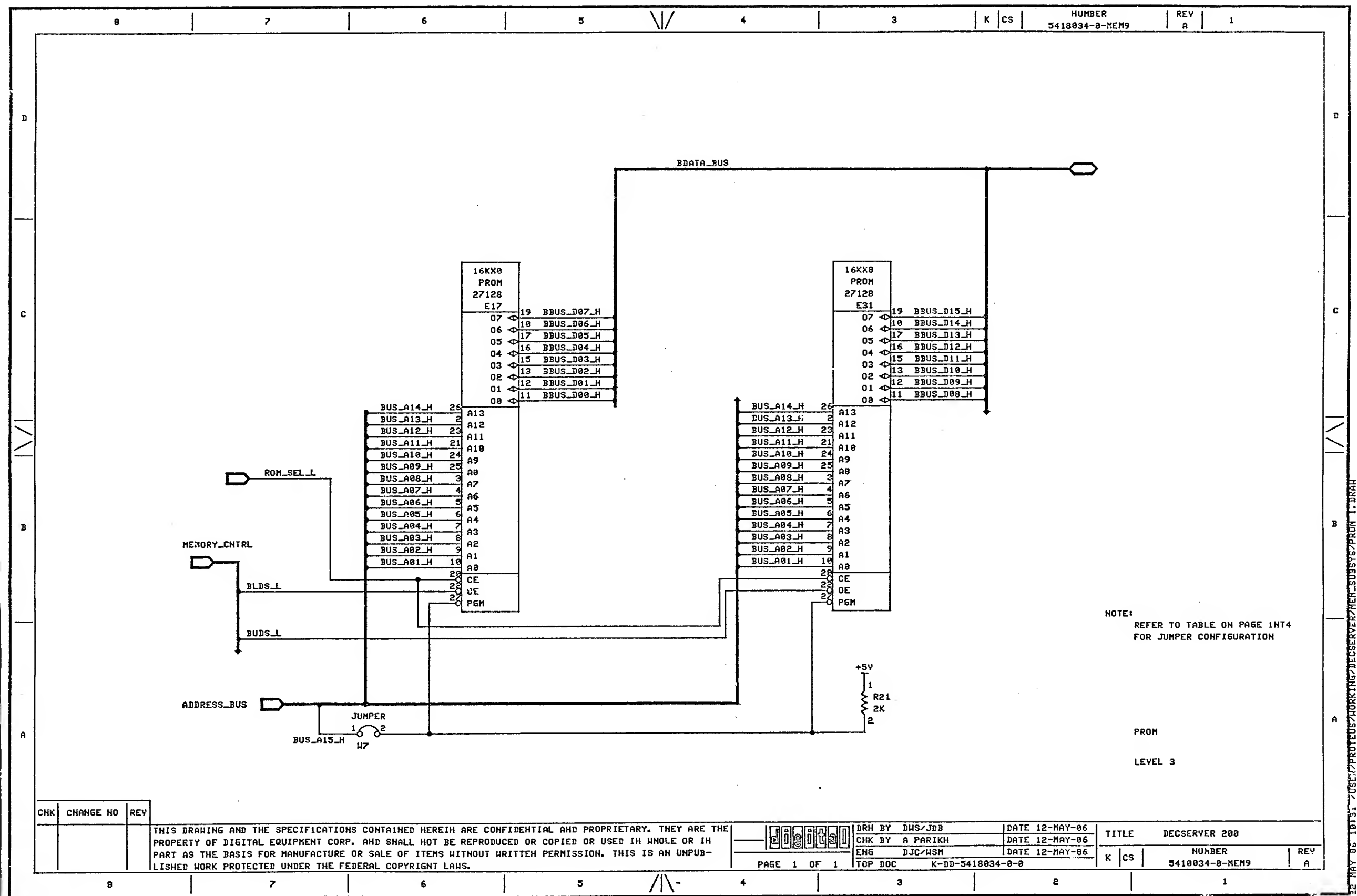




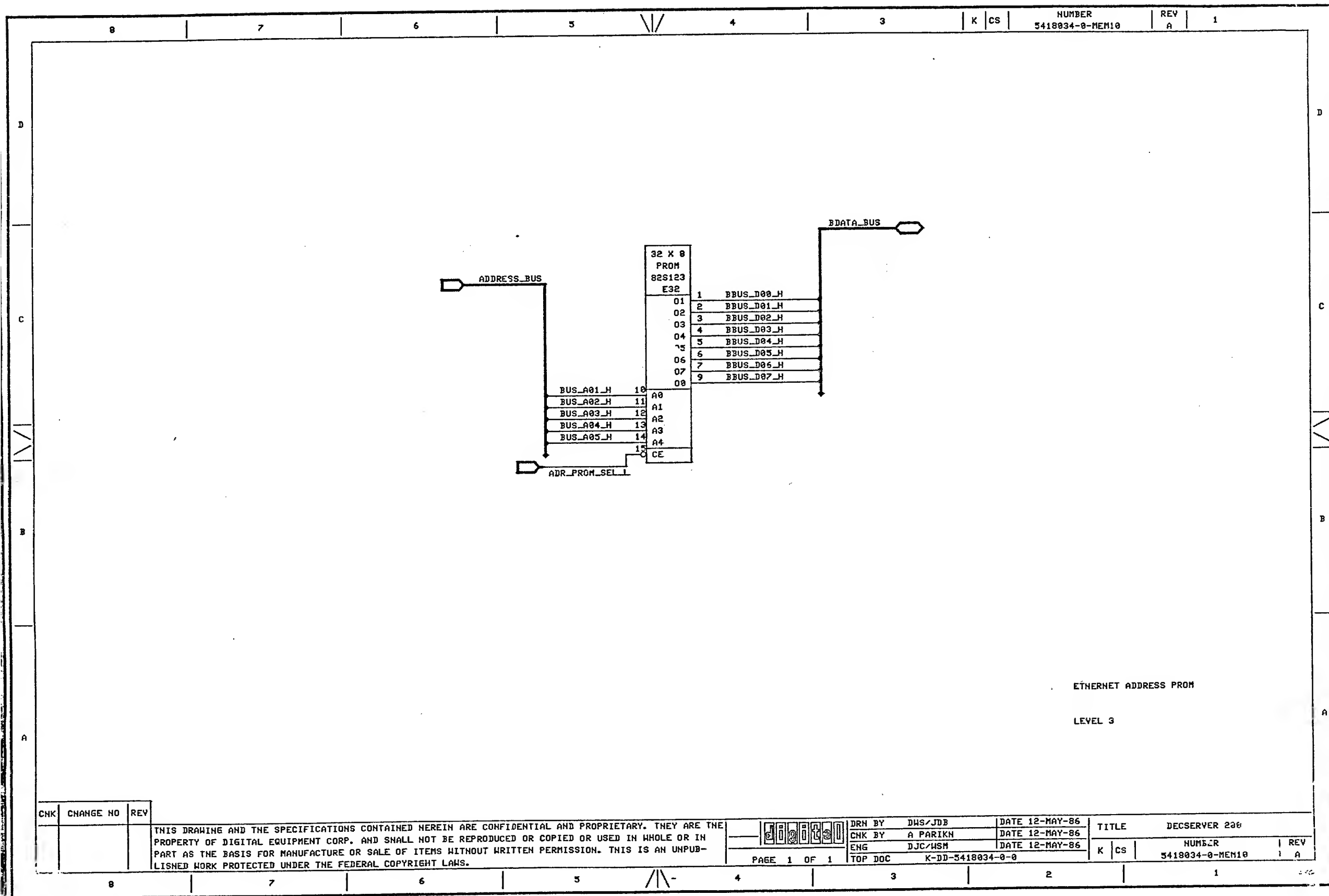


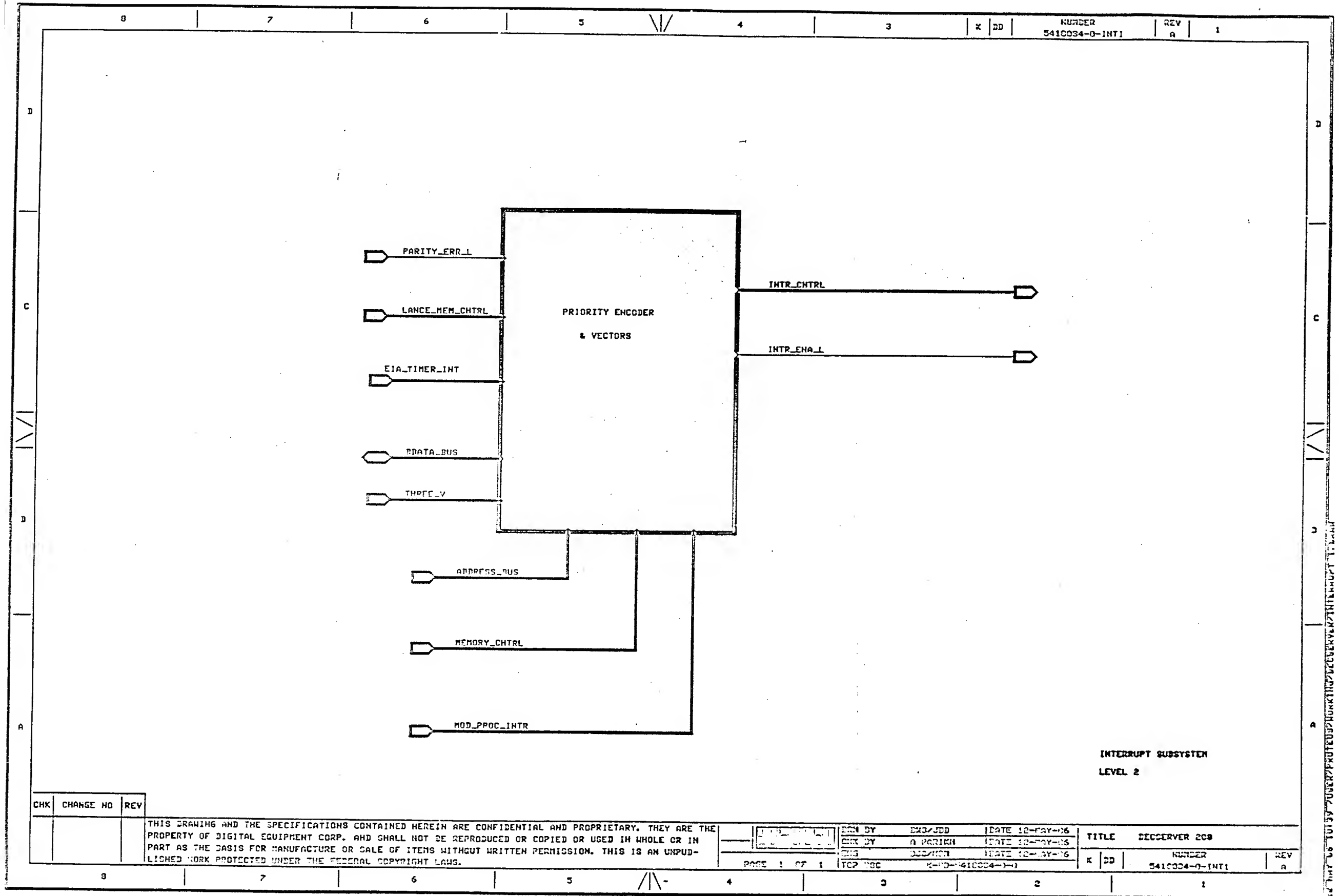






32 MAY 86 10:31 - USE: \\PROTEUS\WORKING\DECserver200\MEM3SUBSYS\FROM 1.DRAW



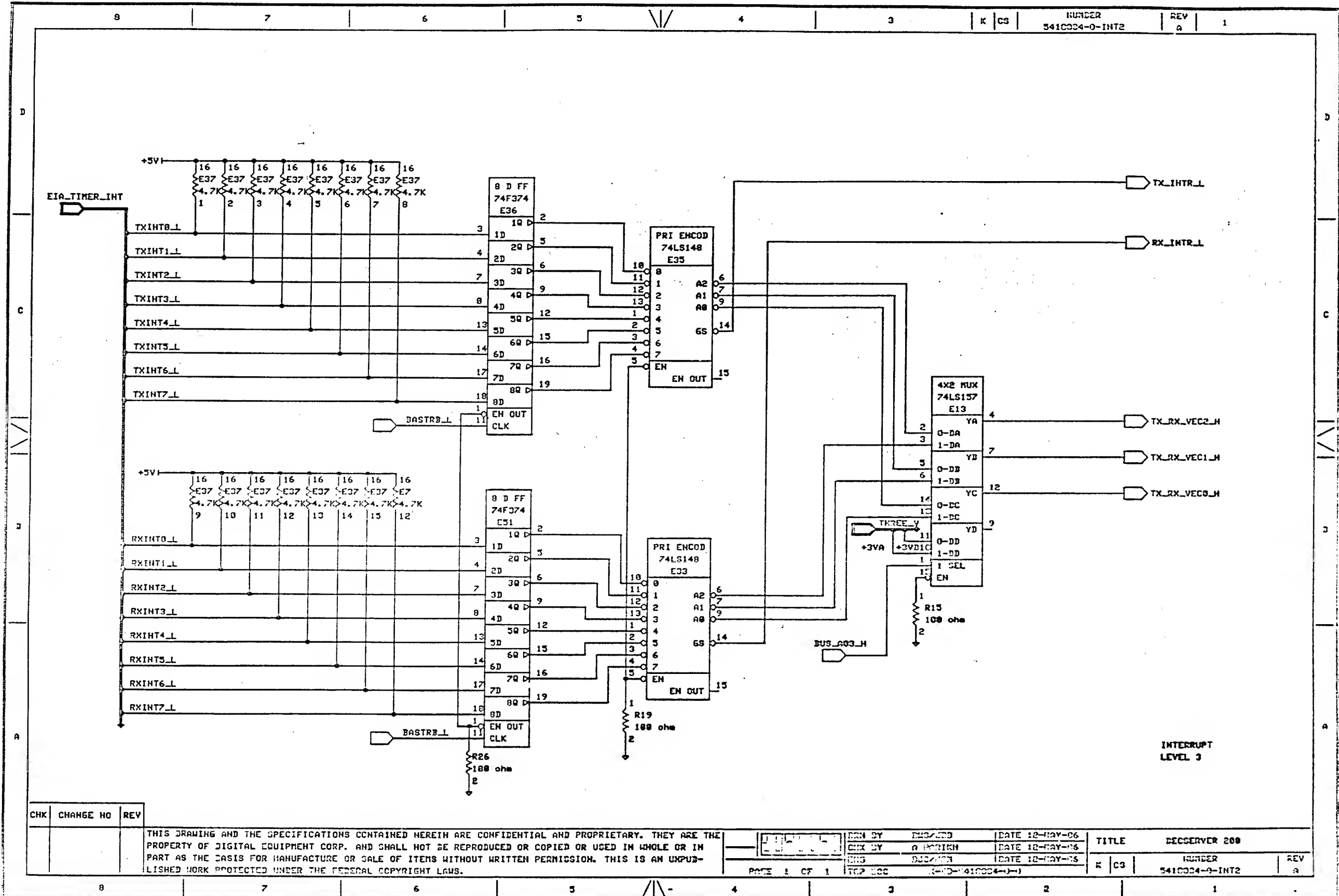


INTERRUPT SUBSYSTEM  
LEVEL 2

CHK	CHANGE NO	REV

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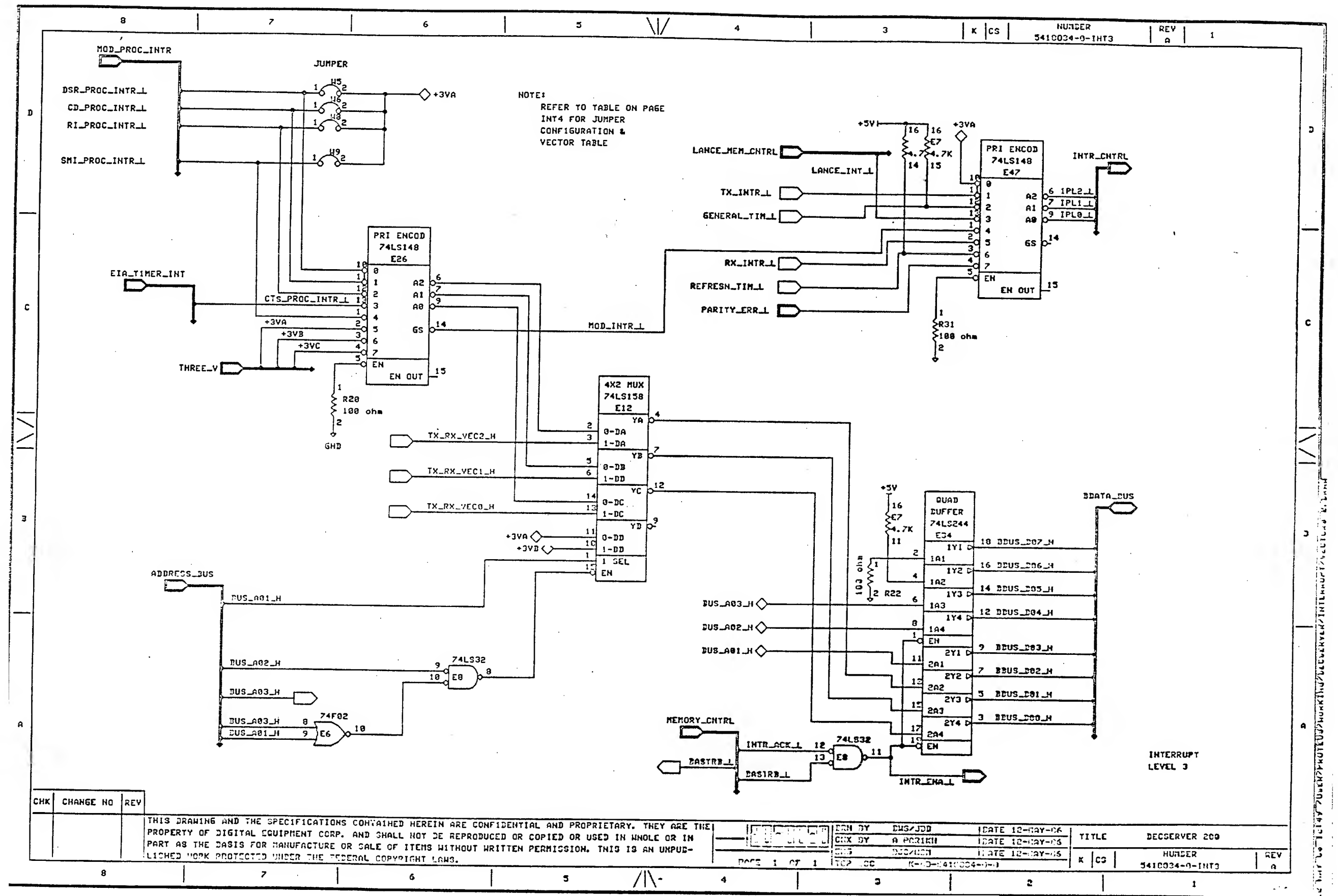
DESIGNED BY	ENG/JDD	DATE	12-MAY-86
CHK BY	A. PARSON	DATE	12-MAY-86
ENG	SEC/MCH	DATE	12-MAY-86
TOP DOC	K-10-410004-1		
PAGE 1 OF 1			
TITLE		DECCSERVER 209	
NUMBER		5410004-0-INT1	
REV		A	



CHK	CHANGE NO	REV
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DESIGNED BY	DESIGNED	DATE 12-MAY-86	TITLE	DECSERVER 200
CHECKED BY	A. BOGREN	DATE 12-MAY-86	NUMBER	5410034-0-INT2
ENG	DESIGNED	DATE 12-MAY-86	REV	A
PAGE 1 OF 1	TOP LOG	(5410034-0-INT2)		



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541C034-0-INT4

REV  
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COMPONENT CONFIGURATIONS

COMPONENT IN	COMPONENT OUT	FUNCTION	PRODUCT
	W1, W2, E77, E78, E88, E89, E101, E102, E109, E110	256KB DRAM	ANY
W2	E78, E89, E102, E110, W1	384KB DRAM	ANY
W1	W2	512KB DRAM	ANY
W4		ROUTER	DSRVC-AA DSRVC-AB
	W4	SERVER	DSRVB-AA DSRVB-AB DSRVB-BA DSRVB-BB
E52, E53, E72, E73, E74, E84, E85, E97, E98 J7, D2-D5, C146, C147, R34, R39, R42 R80, R81	W3, W5, W6, W8-W17, W22	RS232 INTERFACE	DSRVB-AA DSRVB-AB
W3, W5, W6, W8-W17, W22	E52, E53, E72, E73, E74, E84, E85, E97, E98 J7, D2-D5, C146, C147, R34, R39, R42, R80, R81	DEC 423 INTERFACE	DSRVB-BA DSRVB-BB
W7	R21	32Kx8 EPROM	ANY
R21	W7	16Kx8 EPROM	ANY
W18-W21, W23	E96, R82	NO LANCE HARDWARE WORKAROUNDS	ANY
R82, E96	W18-W21, W23	LANCE HARDWARE WORKAROUNDS	ANY
W24		FULL STEP	ANY
	W24	HALF STEP	ANY

INTERRUPT VECTORS

DEVICE	INTERRUPT VECTOR	68000 VECTOR
TXINT0	48	120
TXINT1	49	124
TXINT2	4A	128
TXINT3	4B	12C
TXINT4	4C	130
TXINT5	4D	134
TXINT6	4E	138
TXINT7	4F	13C
GENERAL_TIM	57	15C
LANCE_INT	5F	17C
DSR_PROC_INT	60	180
CD_PROC_INT	61	184
RI_PROC_INT	62	188
CTS_PROC_INT	63	18C
SMI_PROC_INT	64	190
RXINT0	68	1A0
RXINT1	69	1A4
RXINT2	6A	1A8
RXINT3	6B	1AC
RXINT4	6C	1B0
RXINT5	6D	1B4
RXINT6	6E	1B8
RXINT7	6F	1BC
REFRESH_TIM	77	1DC
PARITY_ERR	7F	1FC

CNK	CHANGE NO	REV

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DATE 12-MAY-06  
CHK BY A PARIKH  
ENG DUC/NOH  
TCP ECC K-ED-541C045-0-0

DATE 12-MAY-06  
DATE 12-MAY-06

TITLE  
DEC SERVER 200

NUMBER  
541C034-0-INT4

REV  
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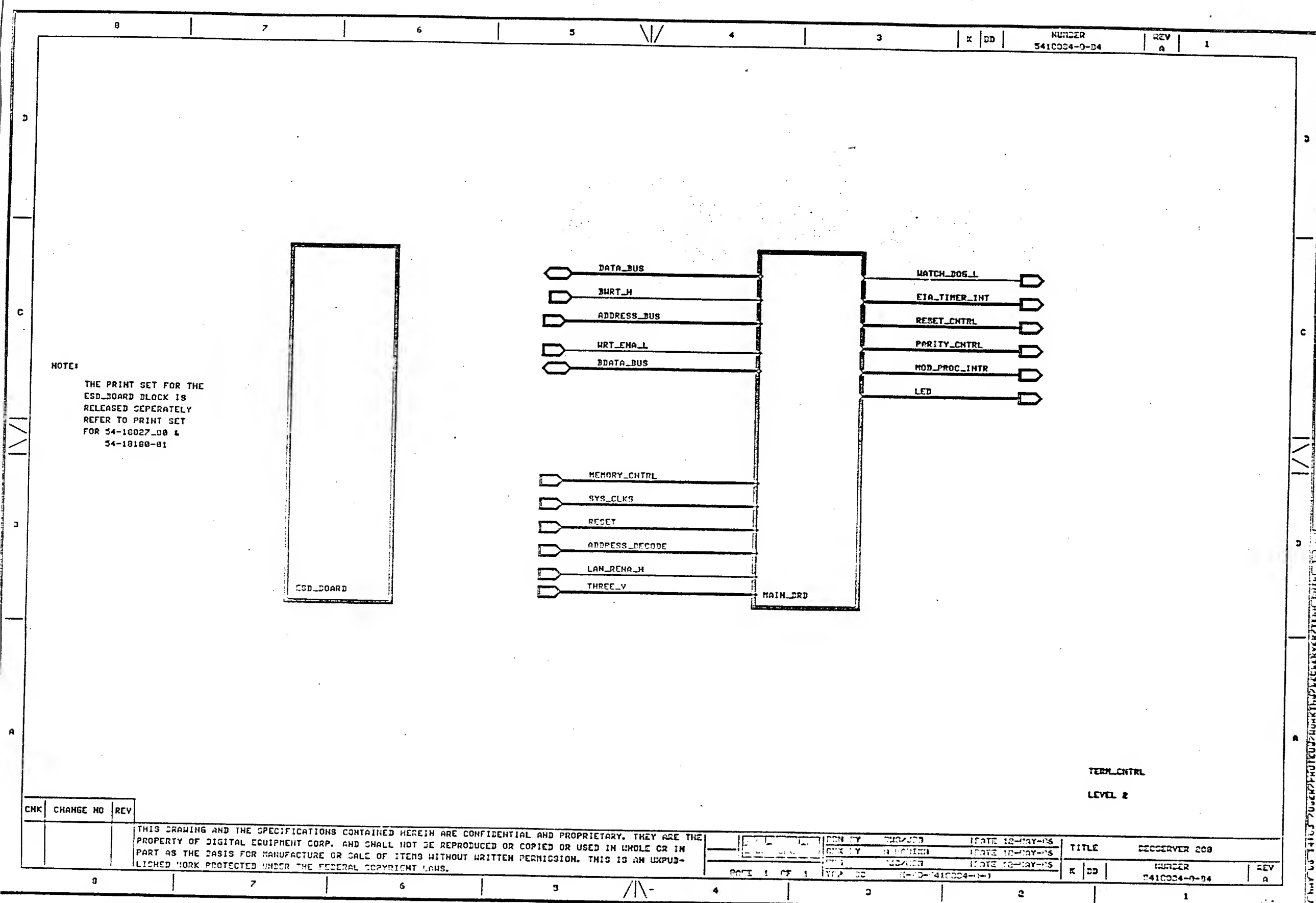
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33 MAY 06 14:17 USER:PROTEUS/HOOKING/DEC SERVER TABLE.LAM







NOTE:  
THE PRINT SET FOR THE  
ESD\_BOARD BLOCK IS  
RELEASED SEPARATELY  
REFER TO PRINT SET  
FOR 54-18027-00 &  
54-18180-01

ESD\_BOARD

MAIN\_BD

TERM\_CNTRL  
LEVEL 2

CHK	CHANGE NO	REV
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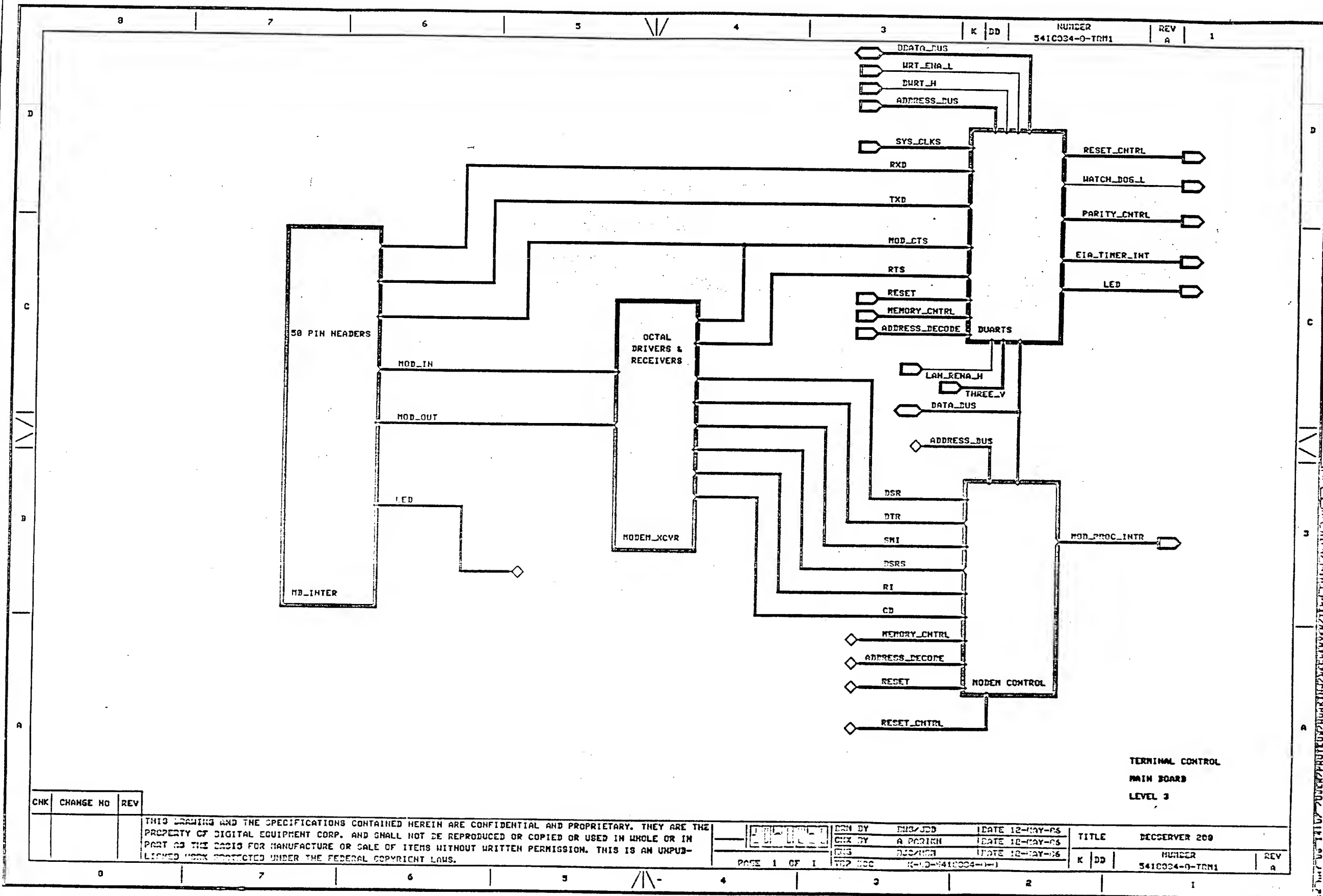
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PAGE 1 OF 1

DESIGNED BY: [blank] DATE 10-MAY-76  
CHECKED BY: [blank] DATE 10-MAY-76  
DATE 10-MAY-76  
VFP: [blank] (REV: 5410004-0-04)

TITLE DECCSERVER 200

NUMBER 5410004-0-04 REV A



CHK	CHANGE NO	REV

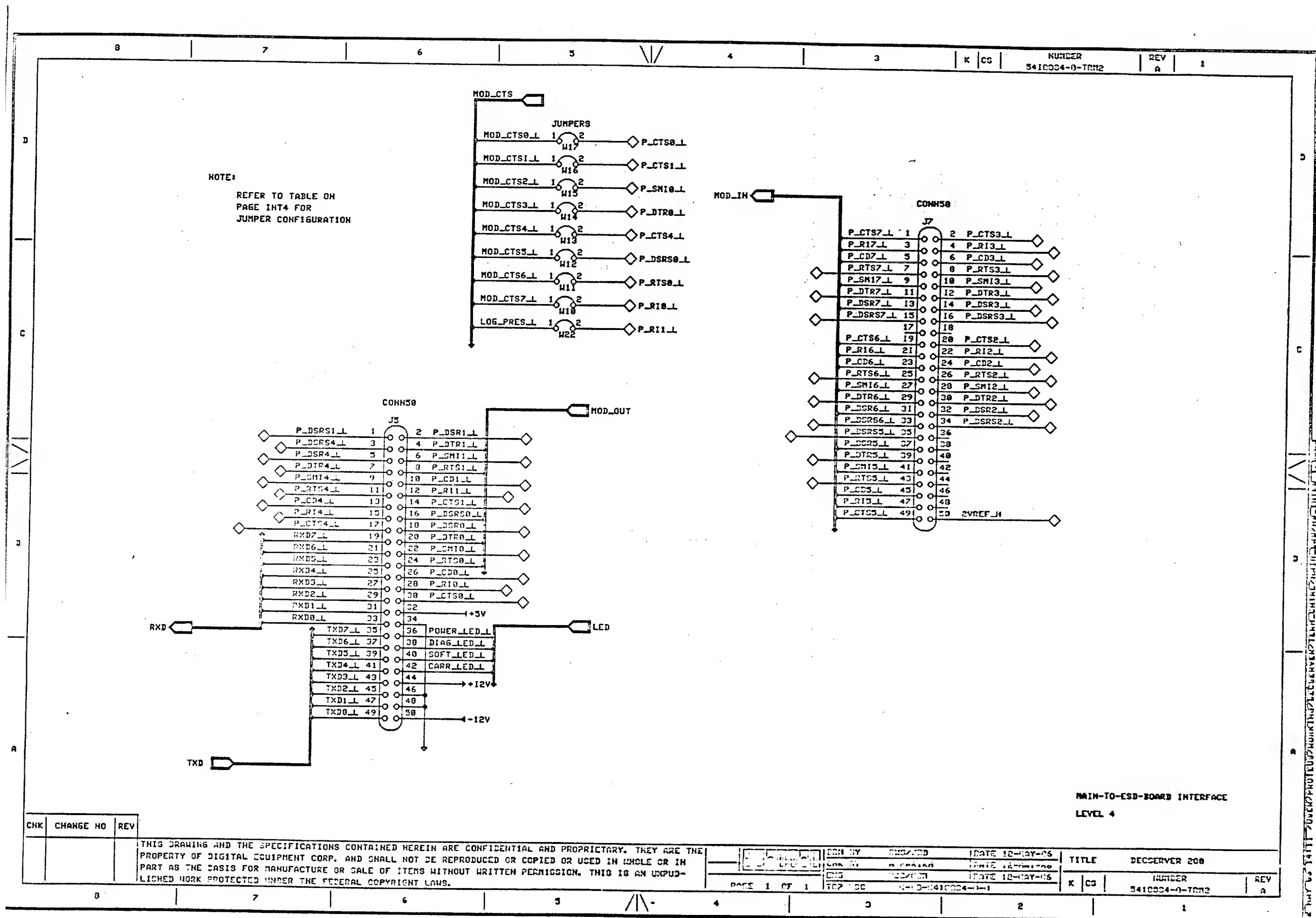
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PAGE 1 OF 1

DESIGNED BY: EMB/JDB  
CHECKED BY: A. PERLIN  
DATE: 12-MAY-85  
REV: 000000  
TOP MOD: K-10-9410004-1-1

TITLE: DECserver 200  
NUMBER: 5410004-0-TRM1  
REV: A

TERMINAL CONTROL  
MAIN BOARD  
LEVEL 3



NOTE:  
REFER TO TABLE ON  
PAGE INT4 FOR  
JUMPER CONFIGURATION

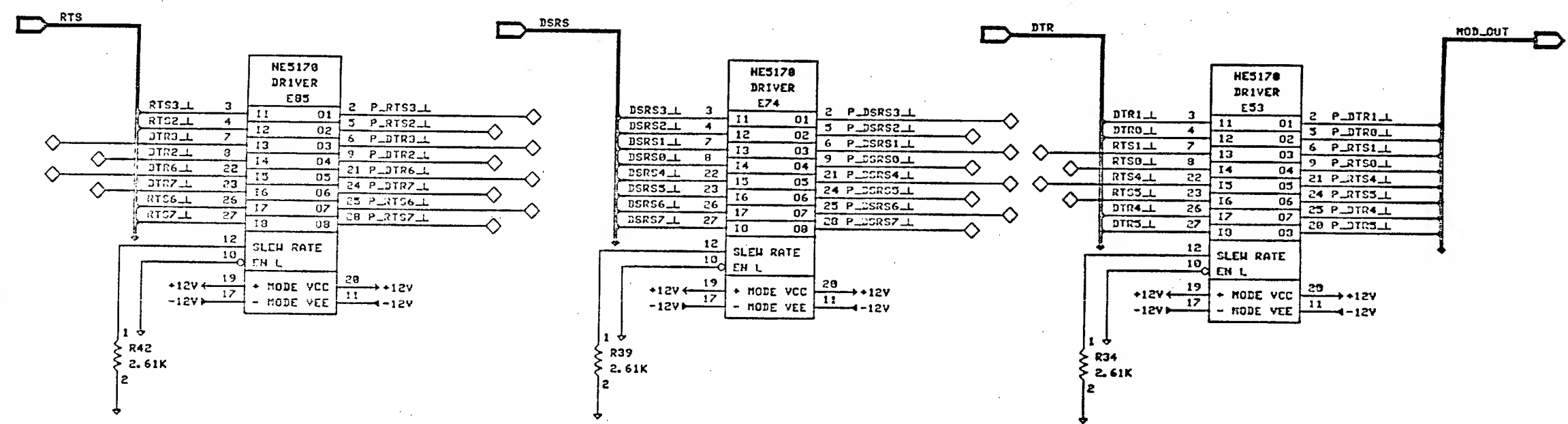
MAIN-TO-ESD-BOARD INTERFACE  
LEVEL 4

CHK	CHANGE NO	REV

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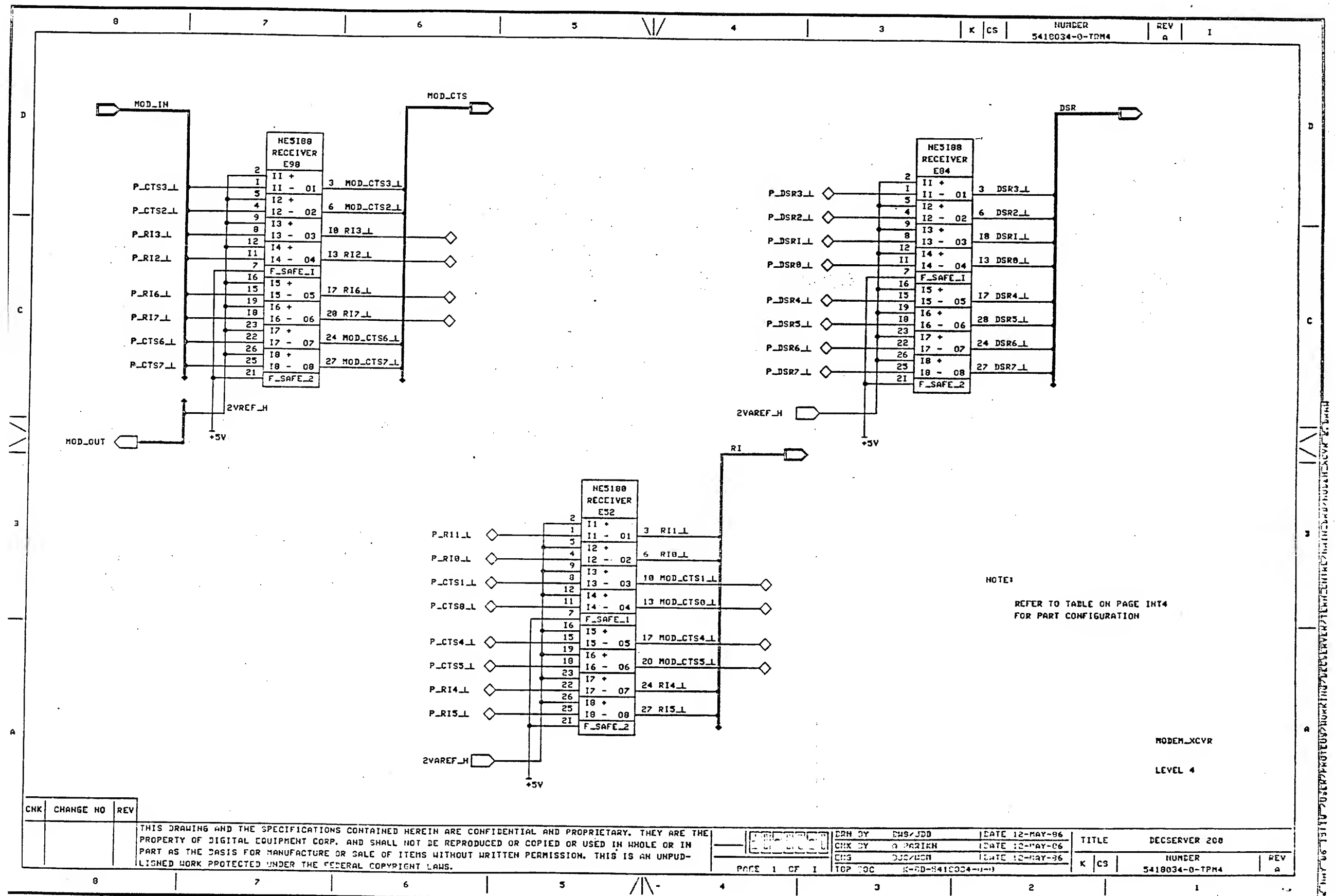
DATE 12-MAY-85	DATE 12-MAY-85	TITLE	DECSERVER 208
DESIGNED BY	DESIGNED BY	NUMBER	3410004-0-TRM2
DATE 12-MAY-85	DATE 12-MAY-85	REV	A

NOTE:  
REFER TO TABLE ON PAGE INT4  
FOR PART CONFIGURATION



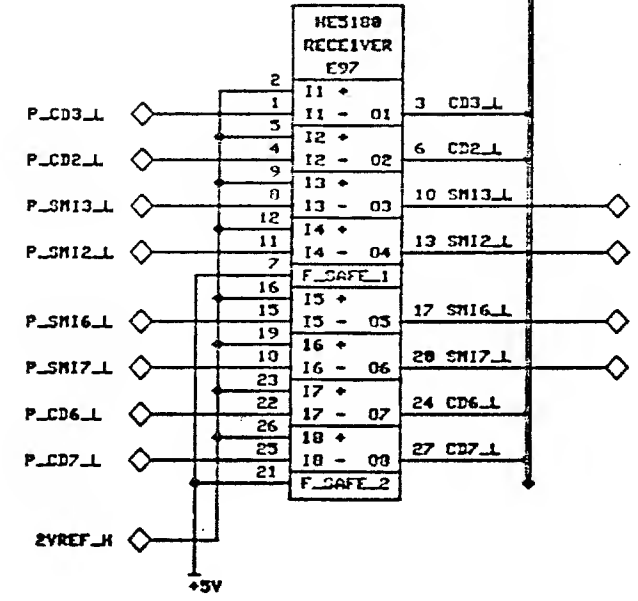
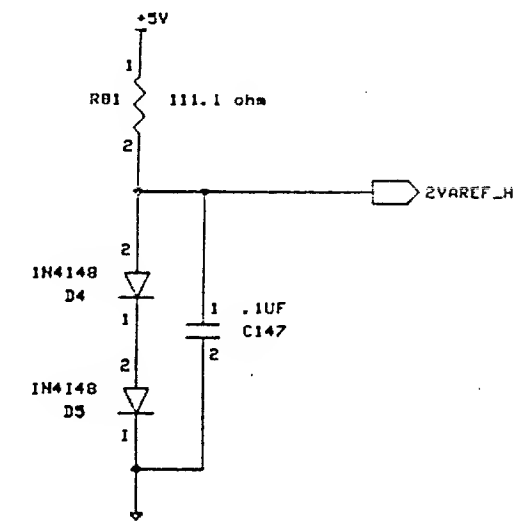
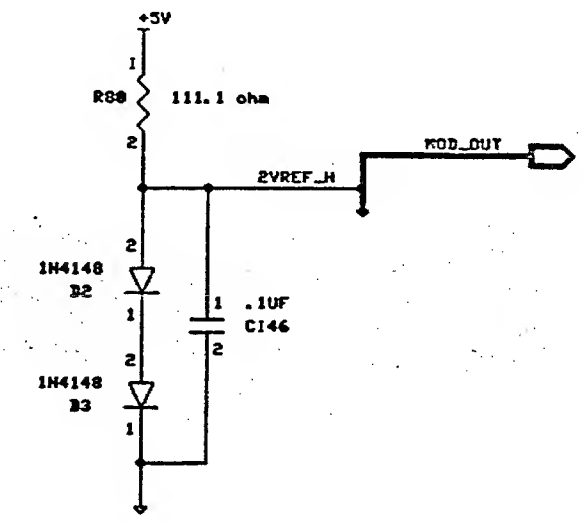
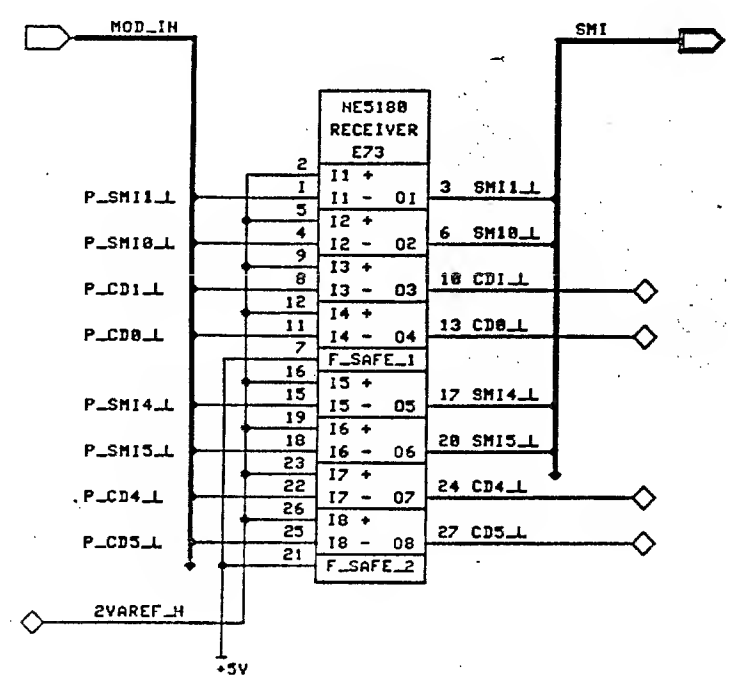
MODER\_XCVR  
LEVEL 4

CHK	CHANGE NO	REV	THIS DRAWING AND THE SPECIFICATIONS CONTAINED HEREIN ARE CONFIDENTIAL AND PROPRIETARY. THEY ARE THE PROPERTY OF DIGITAL EQUIPMENT CORP. AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. THIS IS AN UNPUBLISHED WORK PROTECTED UNDER THE FEDERAL COPYRIGHT LAWS.				DATE 12-MAY-86	DATE 12-MAY-86	TITLE	NUMBER	REV
							DATE 12-MAY-86	DATE 12-MAY-86	DECCSERVER 200	541C334-0-TRM3	A
						PAGE 1 OF 1	TOP DOC	K-00-541C334-0-1-1			



NOTE:

REFER TO TABLE ON PAGE INT4  
FOR PART CONFIGURATION

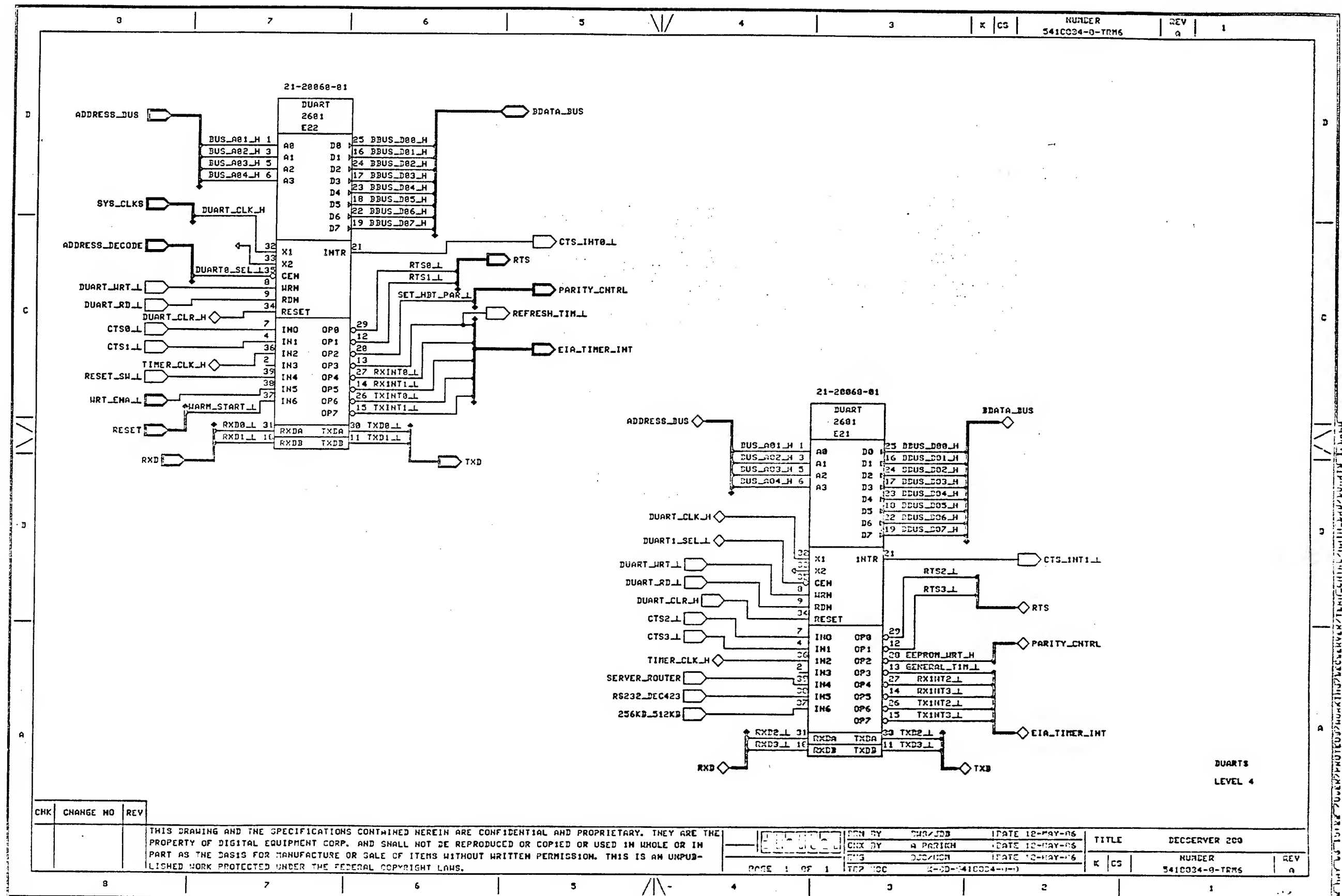


MODEN\_XCVR  
LEVEL 4

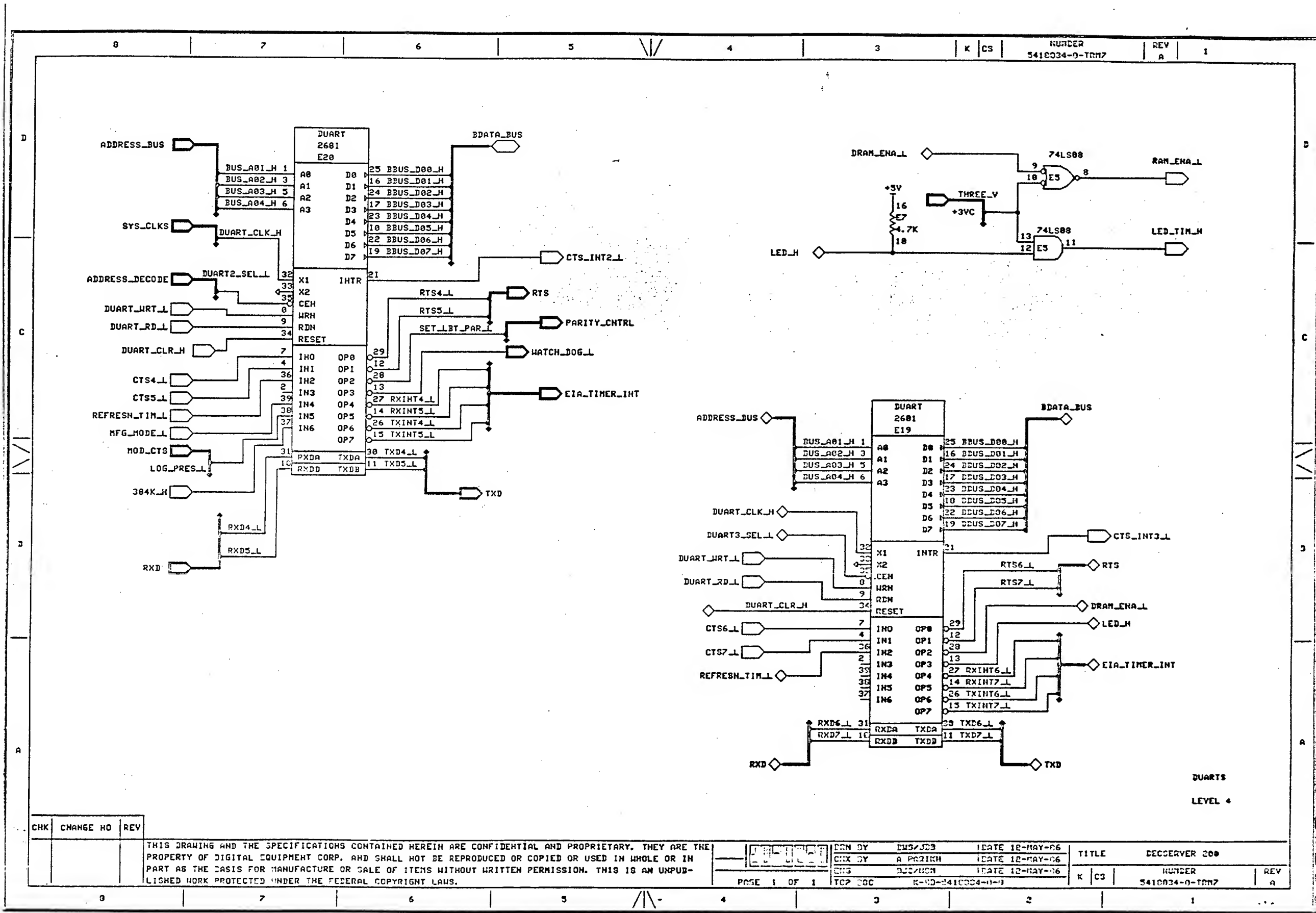
CHK	CHANGE NO	REV
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DESIGNED BY	CHK'D BY	DATE	TITLE
DESIGNED BY	CHK'D BY	DATE	DECSERVER 200
DESIGNED BY	CHK'D BY	DATE	NUMBER
DESIGNED BY	CHK'D BY	DATE	REV
DESIGNED BY	CHK'D BY	DATE	A



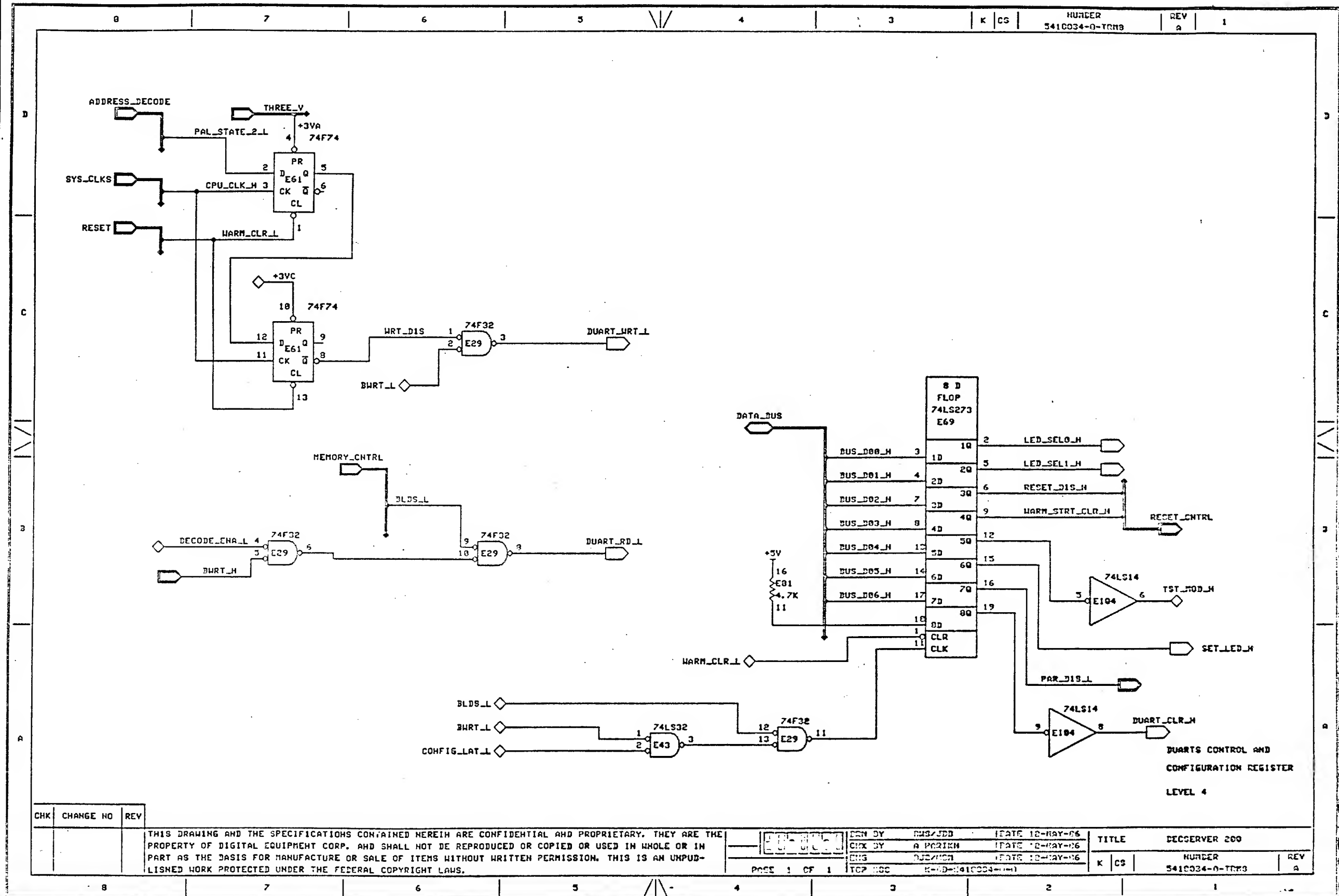




CHK	CHANGE NO	REV

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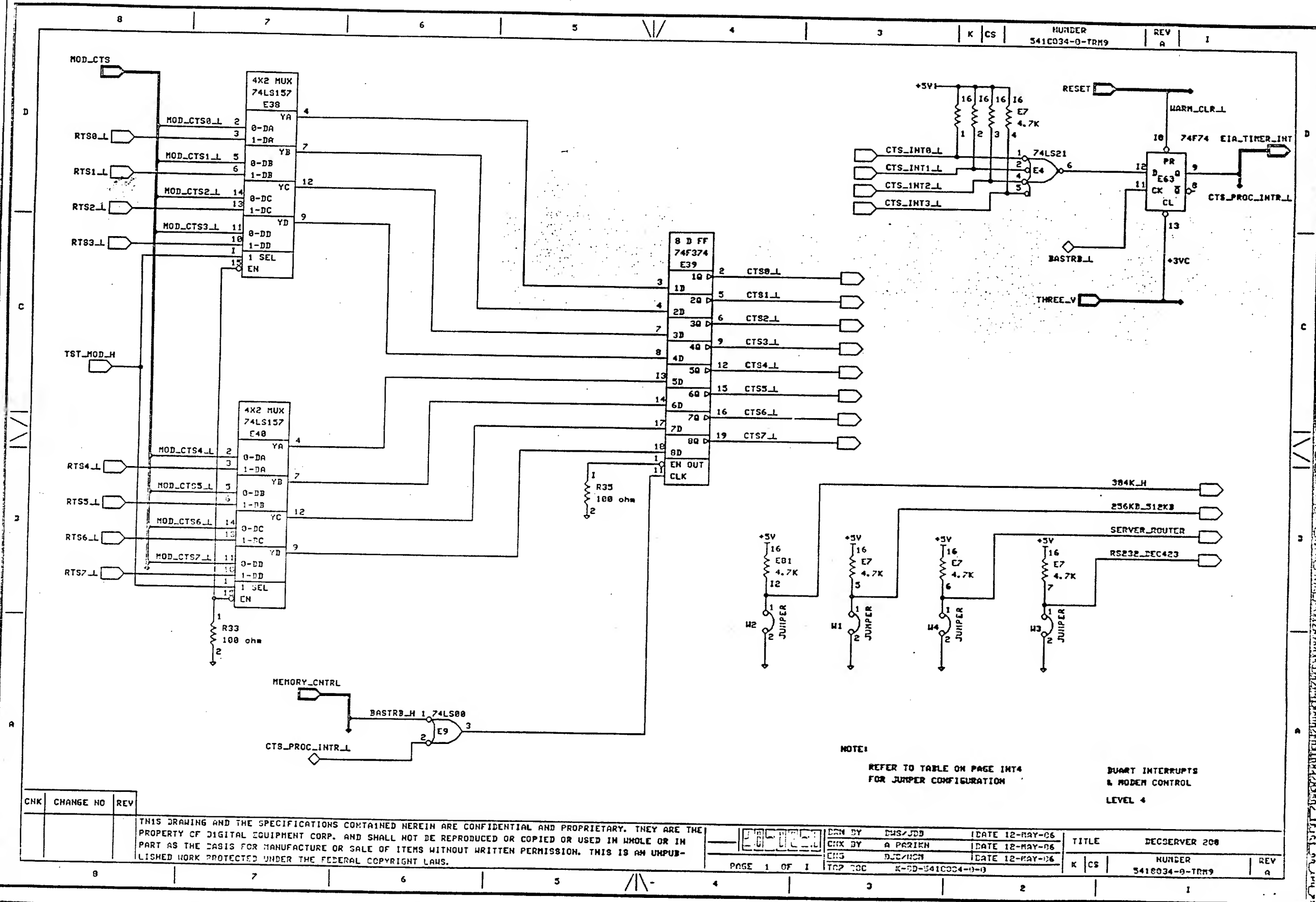
DESIGNED BY	DESIGNED BY	DATE	12-MAY-86	TITLE	DECSERVER 200
CHK'D BY	CHK'D BY	DATE	12-MAY-86	NUMBER	5410034-0-T007
ENG	ENG	DATE	12-MAY-86	REV	A
PAGE 1 OF 1	TCP 200	K-03-5410034-0-0			



CHK	CHANGE NO	REV
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DESIGNED BY	WMS/JDB	DATE	12-MAY-86	TITLE	DECSERVER 200
CHECKED BY	A. PARSON	DATE	12-MAY-86		
ENG	DJW/MCH	DATE	12-MAY-86		
TOP	800	REV	0410034-0-1003		



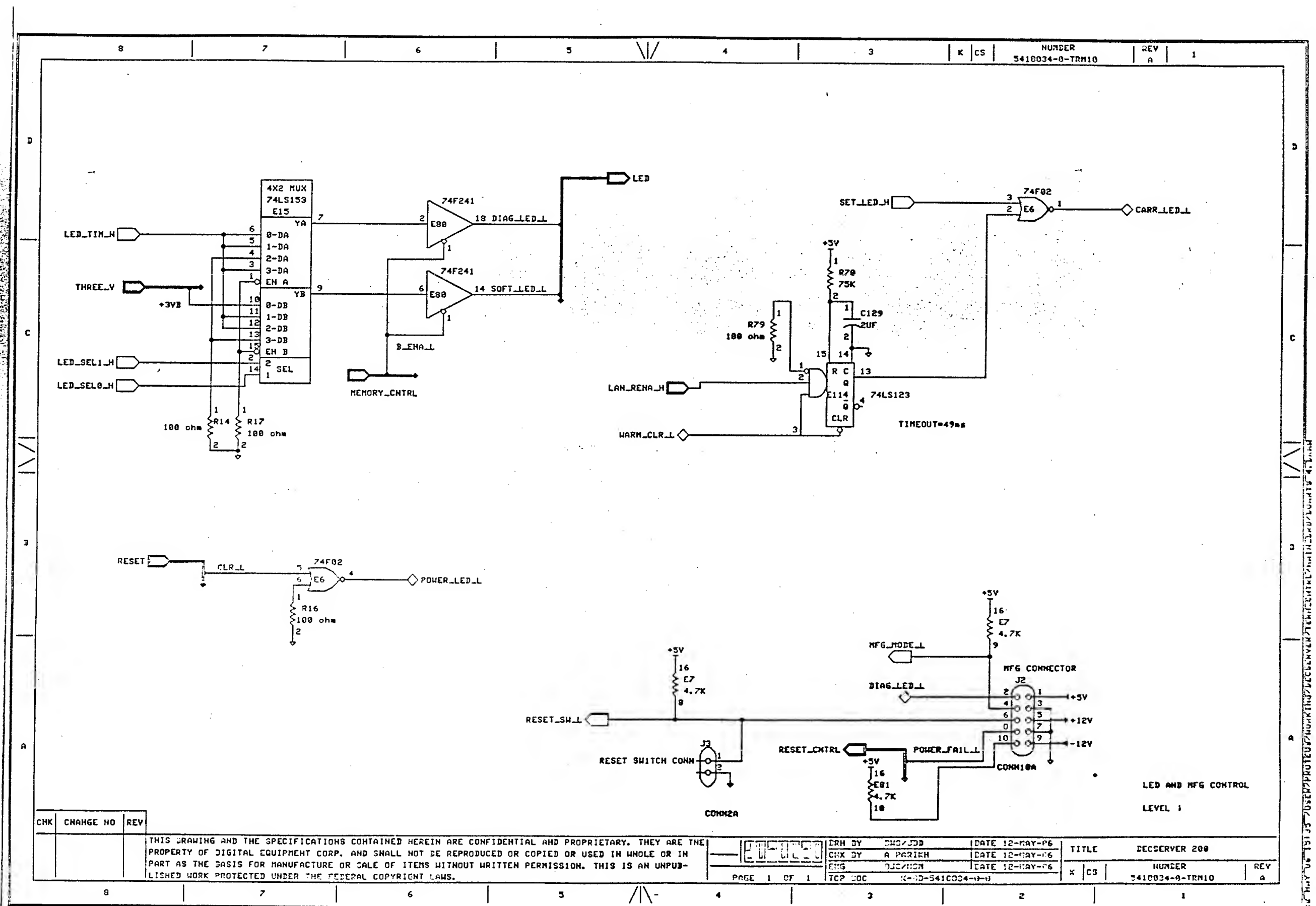
CNK	CHANGE NO	REV
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DESIGNED BY: DWS/JDD  
CHECKED BY: A. PARISH  
DATE: 12-MAY-06  
TCD: TCC  
K-5D-5410034-0-0

TITLE: DECserver 200  
NUMBER: 5410034-0-TRM9  
REV: A



CHK	CHANGE NO	REV
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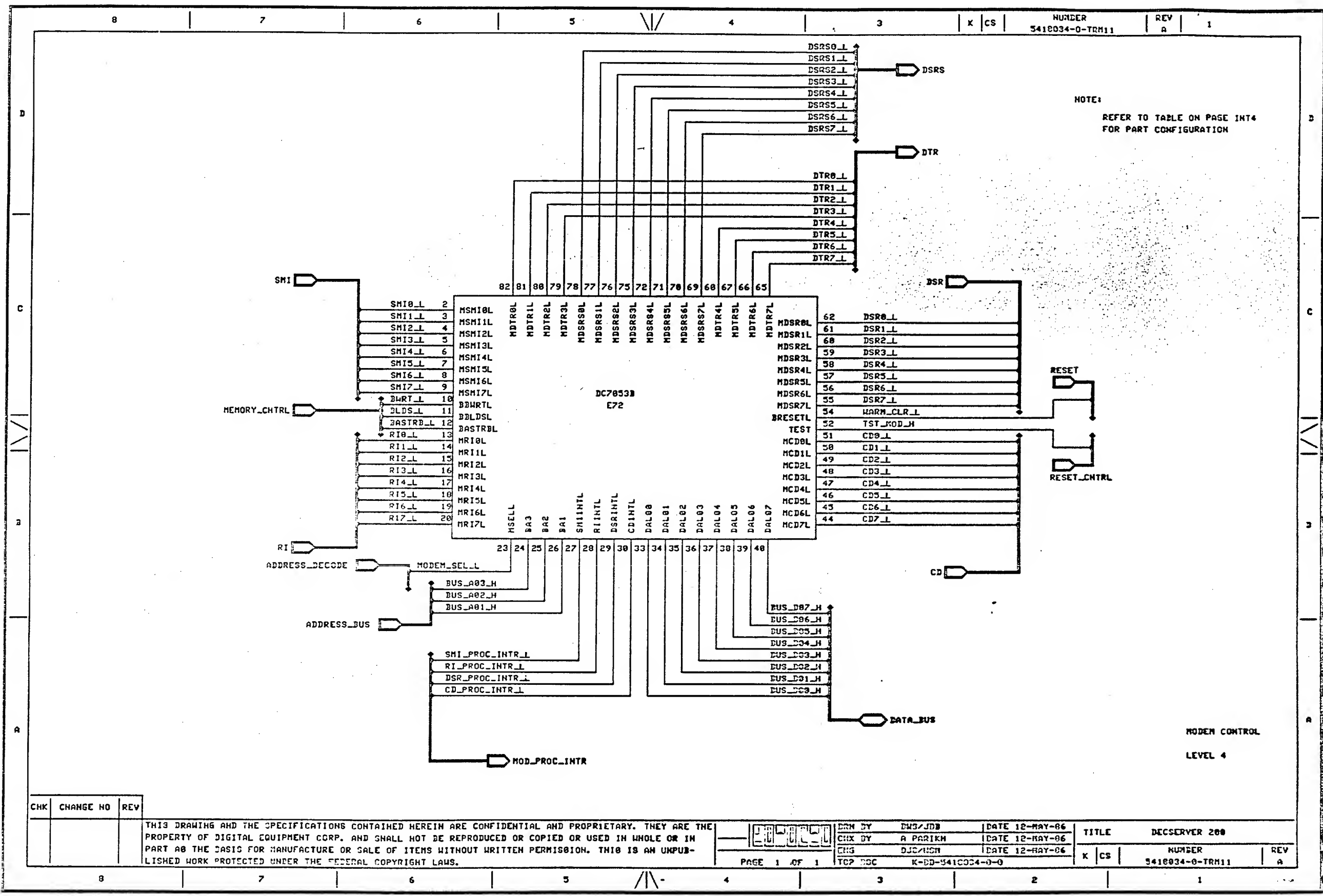
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PAGE 1 OF 1

DRN BY: DMS/JDD  
CHK BY: A PARIKH  
ENG: DJC/HCH  
TCP DOC: K-45-5410034-0-0

DATE 12-MAY-86  
DATE 12-MAY-86

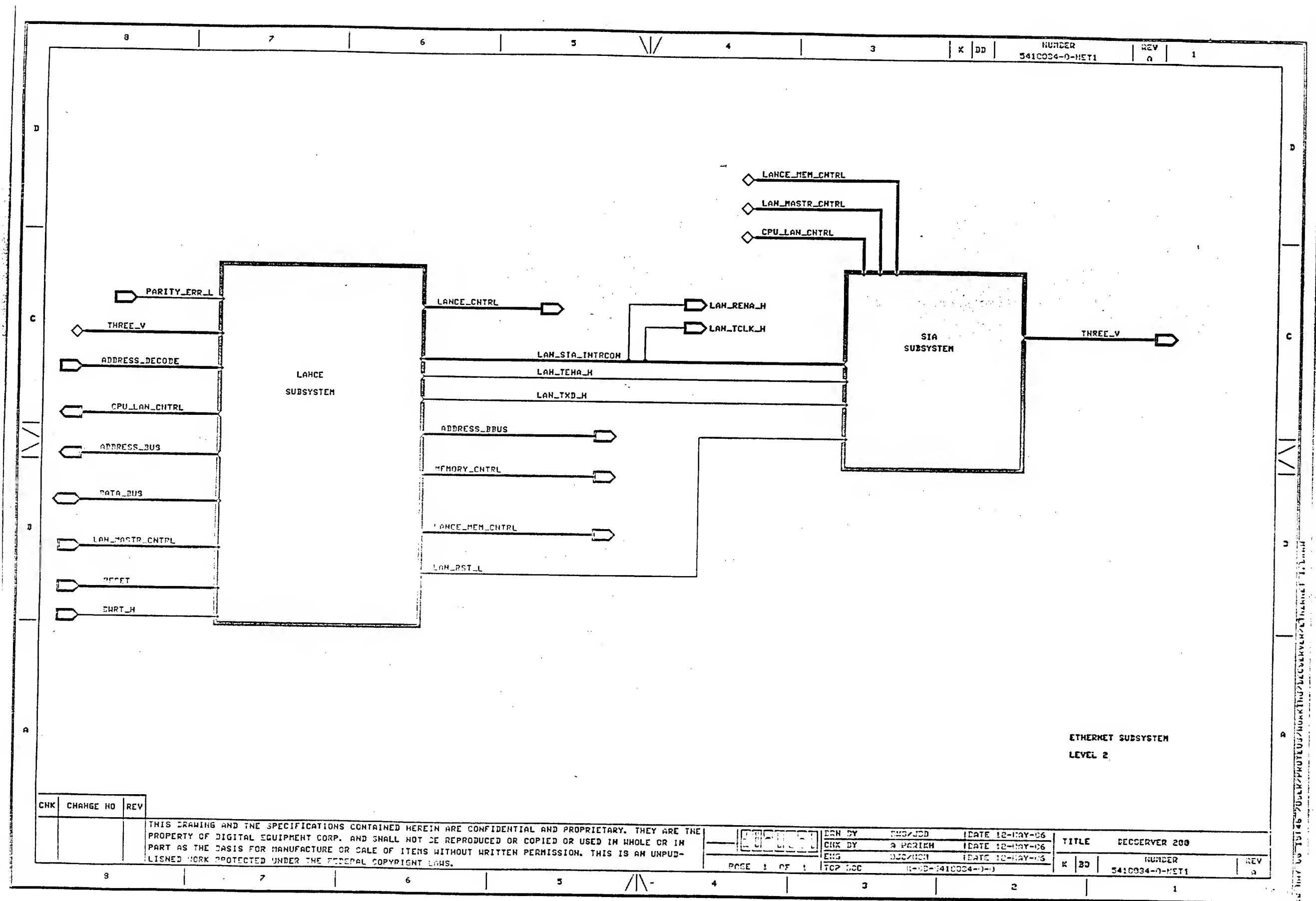
TITLE: DECserver 200  
NUMBER: 5410034-0-TRN10  
REV: a



CHK	CHANGE NO	REV

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DATE 12-MAY-86	DATE 12-MAY-86	TITLE	DECSERVER 200
CHK BY A PARIKH	CHK BY A PARIKH	NUMBER	5418034-0-TRM11
ENG DSC/USN	ENG DSC/USN	REV	A
PAGE 1 OF 1	TCP EDC	K-BD-5418034-0-0	



CNK	CHANGE NO	REV

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DATE 12-MAY-06  
 DATE 12-MAY-06  
 DATE 12-MAY-06

DESIGNED BY: ENG/JDD  
 CHECKED BY: A PERIEN  
 ENGINEER: DCC/UCH

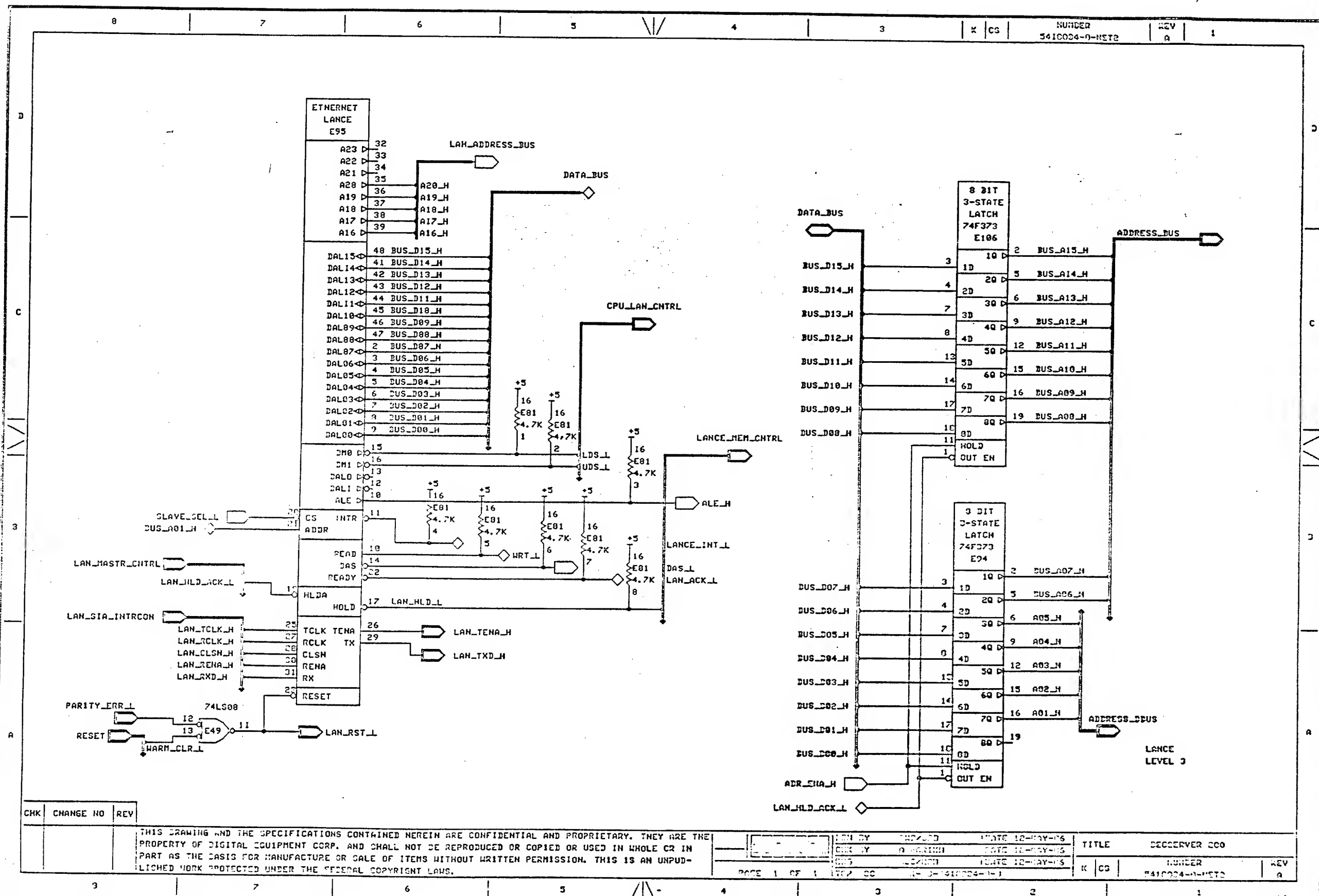
TITLE: DECSERVER 200

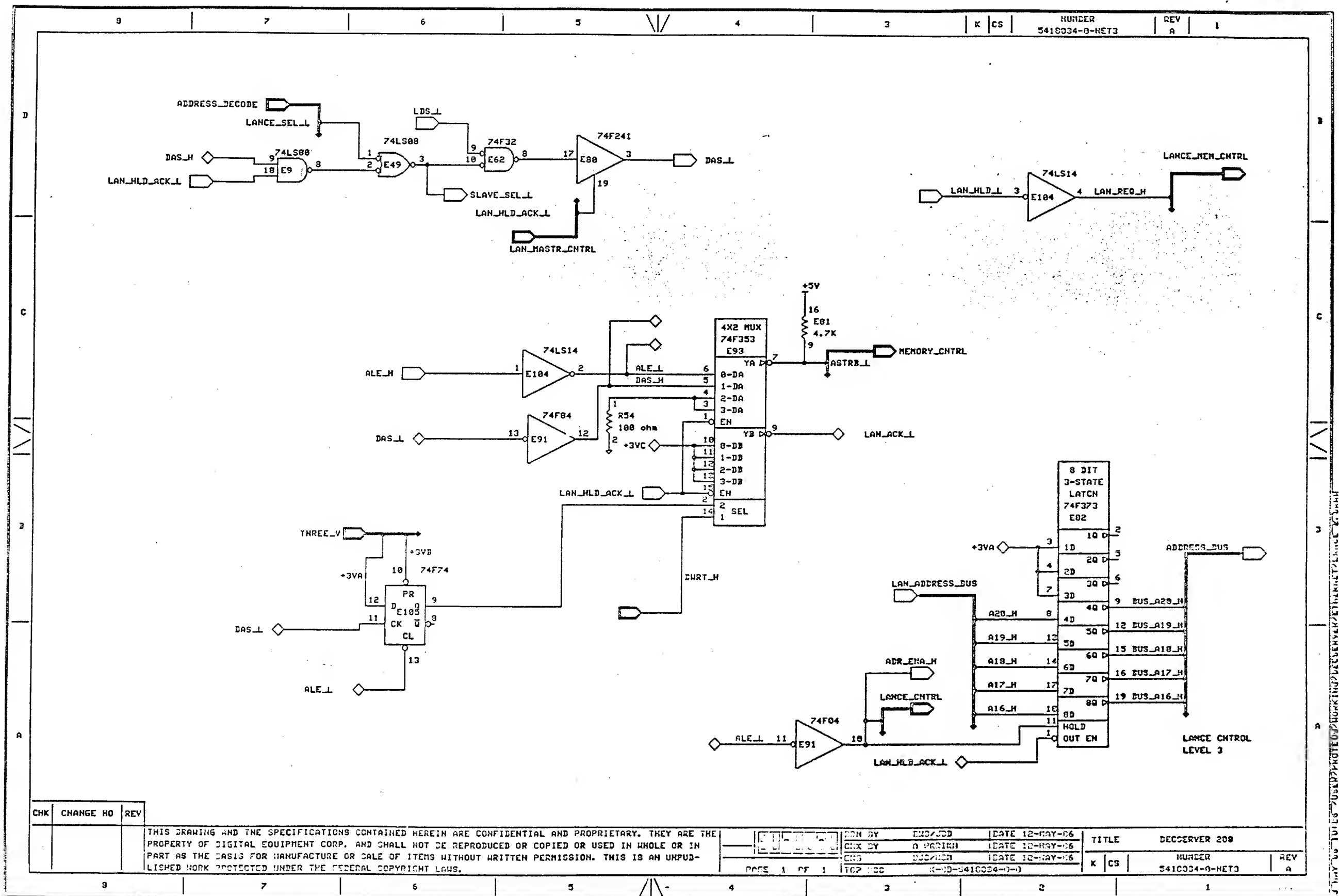
NUMBER: 5410034-0-NET1

REV: 0

PAGE 1 OF 1

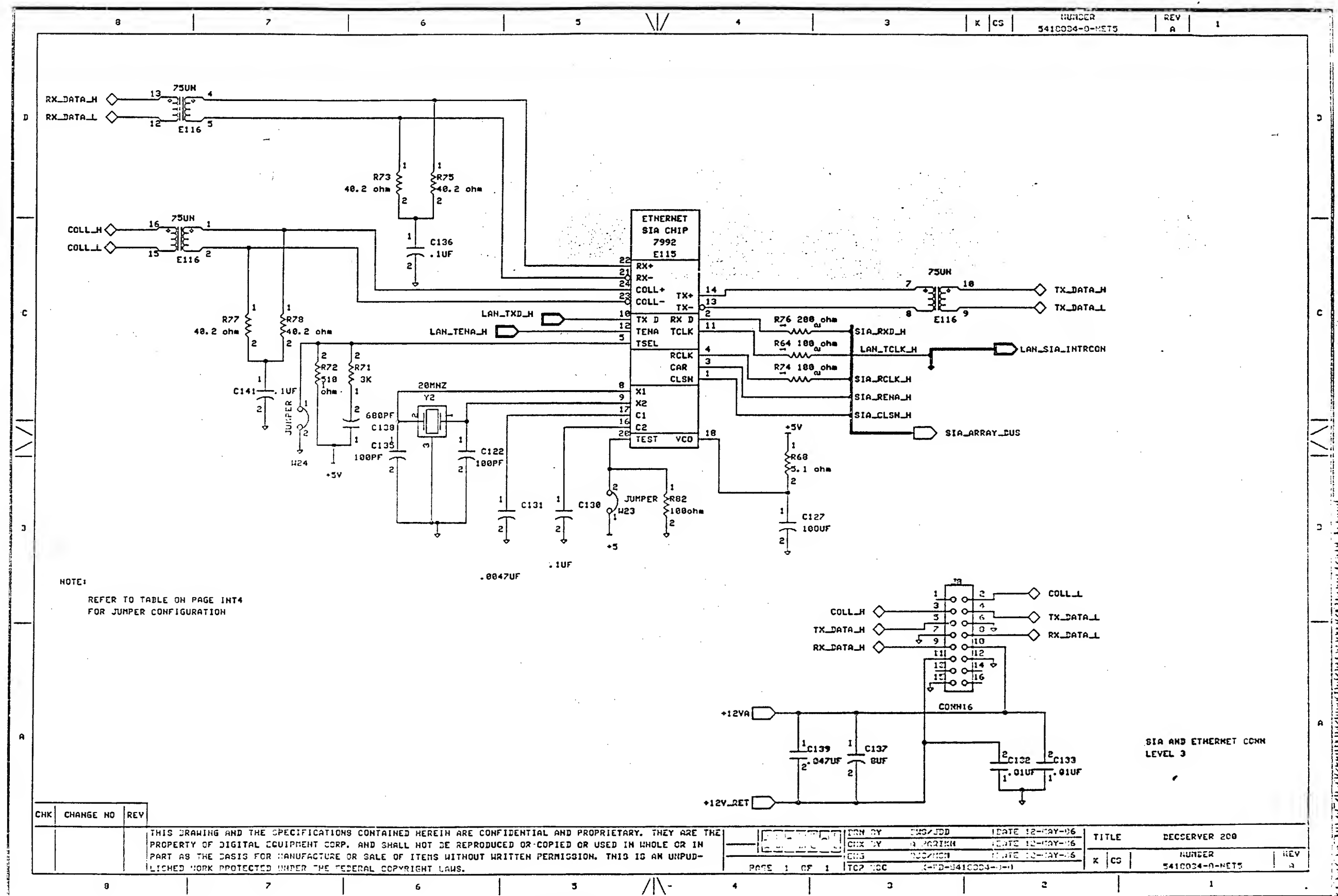
TOP LOC: K-ND-5410034-0-0











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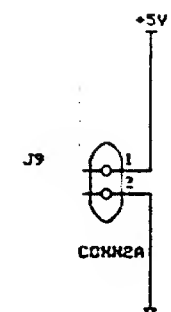
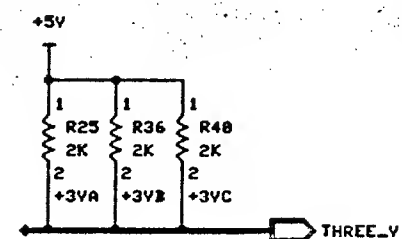
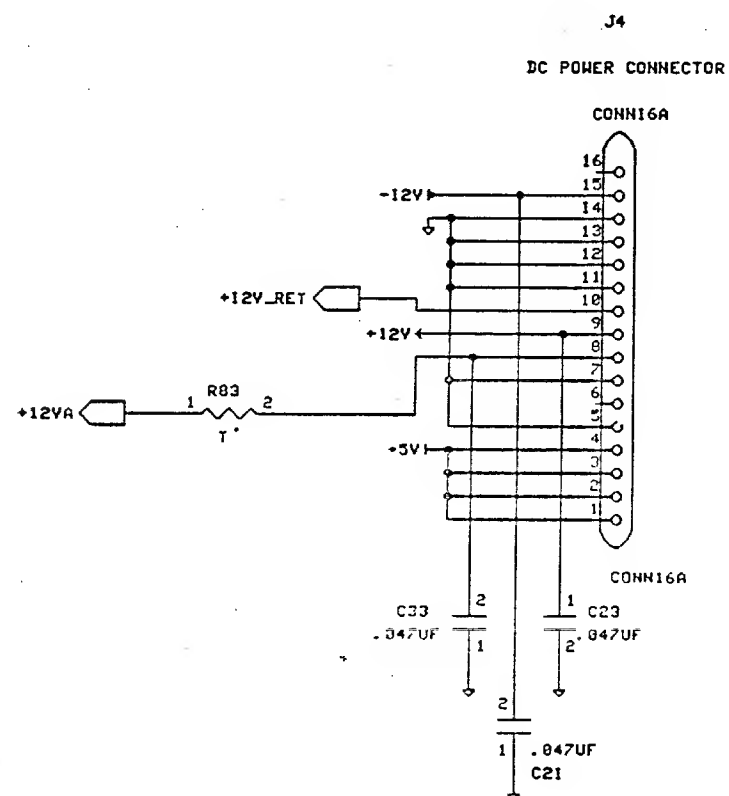
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DC POWER CONNECTOR

CHK	CHANGE NO	REV

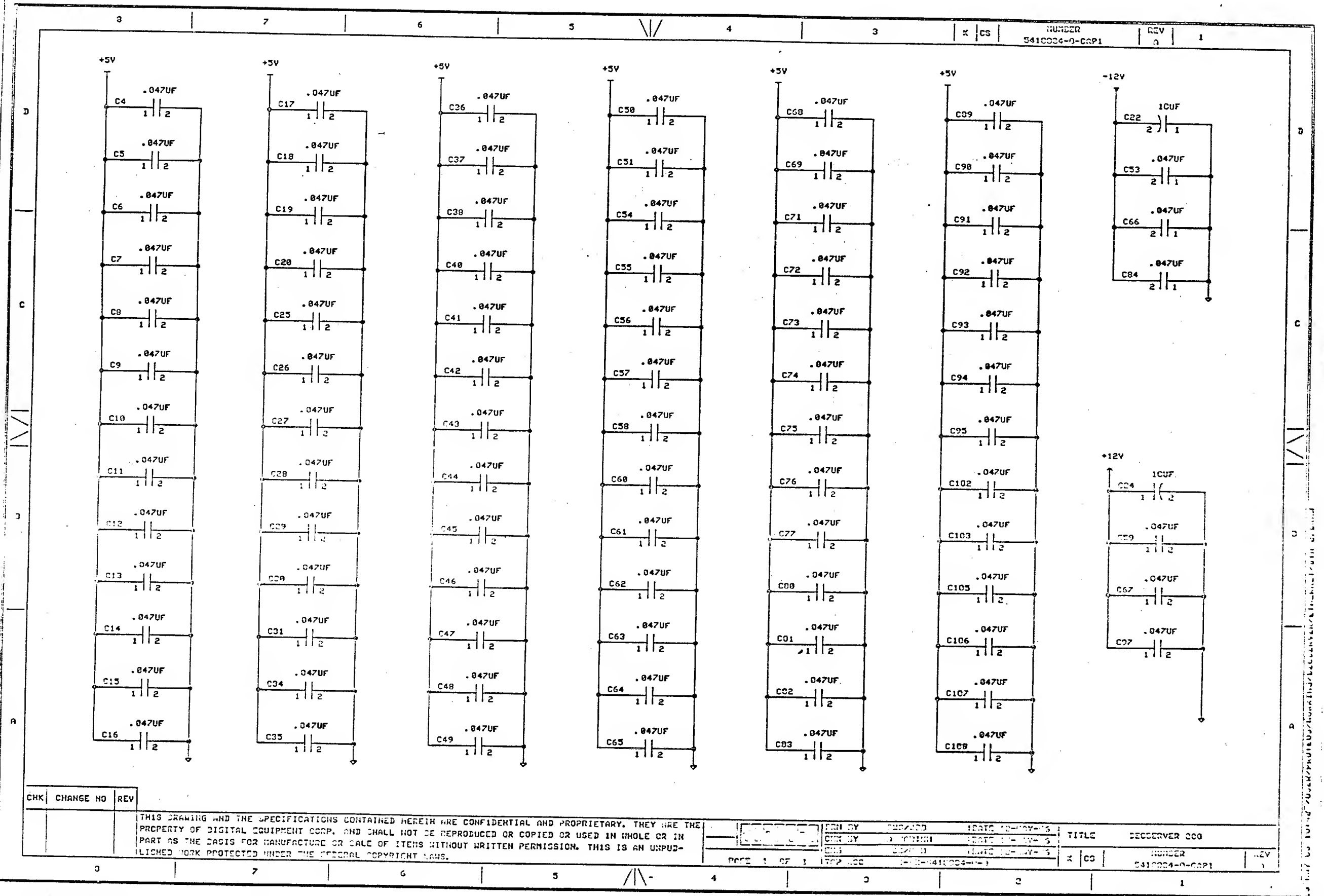
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DATE 12-MAY-86  
DATE 12-MAY-86  
DATE 12-MAY-86

DESIGNED BY  
CHECKED BY  
ENG

TITLE  
DEC SERVER 200NUMBER  
5410034-0-PWRREV  
A

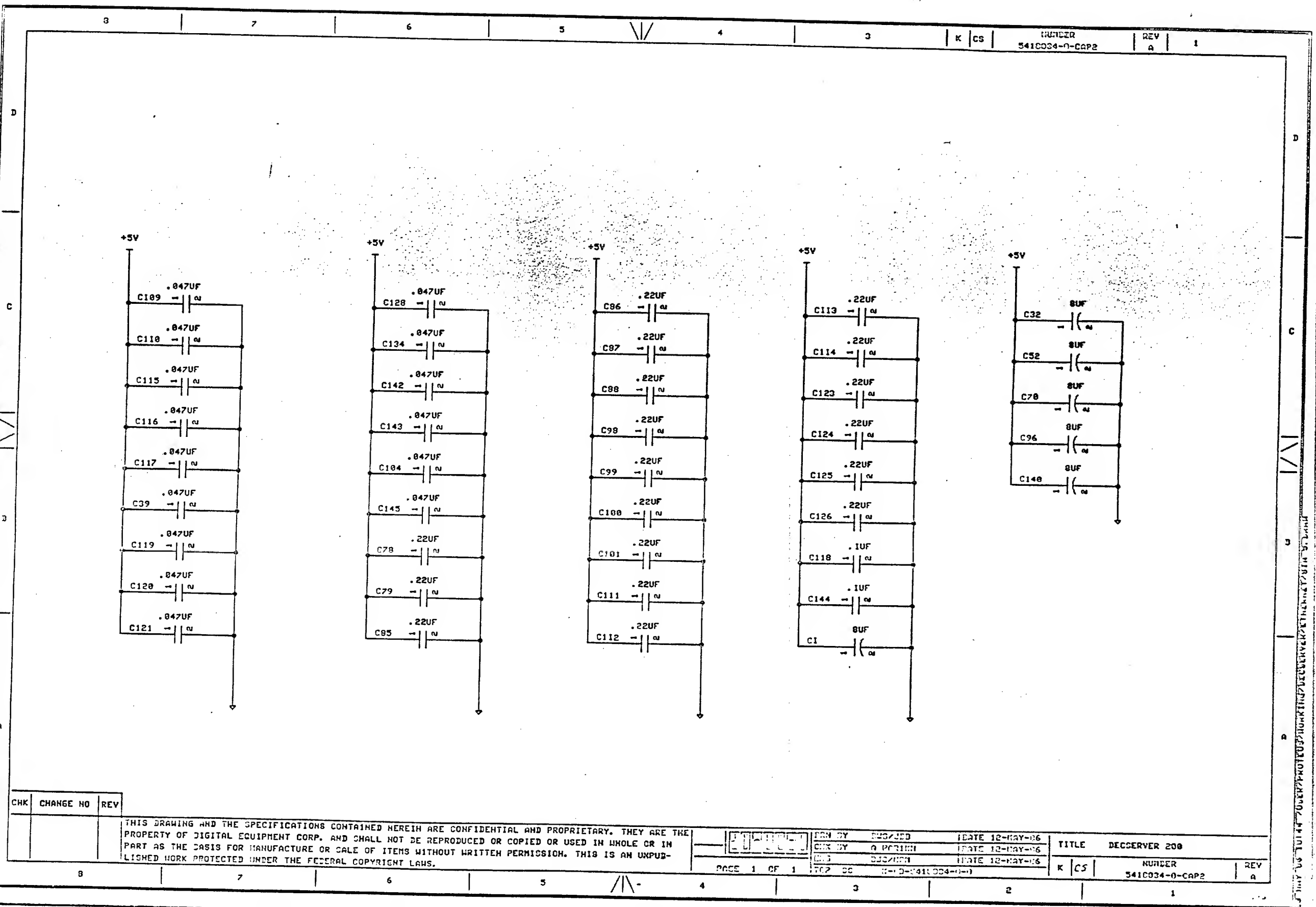
PWR



CHK	CHANGE NO	REV
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DESIGNED BY	DATE	12-10-76	TITLE	DECserver 200
CHECKED BY	DATE	12-10-76	NUMBER	5410004-0-C0P1
DATE	12-10-76	REV	1	



CHK	CHANGE NO	REV
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ED-00000  
PAGE 1 OF 1

DESIGNED BY	DESIGNED	DATE 12-MAY-86
CHECKED BY	A. PORTER	DATE 12-MAY-86
DATE 12-MAY-86	DATE 12-MAY-86	DATE 12-MAY-86

TITLE	DECserver 200
NUMBER	541C034-0-CAP2
REV	A

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KCS

NUMBER

5410034-0-01

REV

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MODULE 0

5410034-0-01

VARIABLE IDENTIFIER

PAGE 0 WITHIN SUBSYSTEM

FUNCTIONAL ABBREVIATION OF SUBSYSTEM

CONNECTOR TYPE

DISCRIPTION

1. USED WHEN A SIGNAL NEEDS TO BE SENT FROM ONE PART OF A PAGE TO ANOTHER WITHOUT THE USE OF A WIRE

2. IF THE SIGNAL HAS ALREADY BEEN BROUGHT ON TO THE PAGE BY ANOTHER CONNECTOR.

1. FINGER PIN CONNECTOR.

INPUT

OUTPUT

BI\_DIRECTIONAL

1. BRINGS A SINGLE SIGNAL FROM OR TO A HIGHER LEVEL DRAWING.

INPUT

OUTPUT

BI\_DIRECTIONAL

1. BRINGS A GROUP OF SIGNALS THROUGH ONE BUS FROM OR TO A HIGHER LEVEL DRAWING.

INPUT

OUTPUT

BI\_DIRECTIONAL

1. BRINGS A SINGLE SIGNAL FROM PAGE TO PAGE WITHIN THE SAME MODULE.

INPUT

OUTPUT

BI\_DIRECTIONAL

1. BRINGS A GROUP OF SIGNALS THROUGH A BUS FROM PAGE TO PAGE WITHIN THE SAME MODULE.

CNK

CHANGE NO

REV

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DATE 12-MAY-06

DATE 12-MAY-06

DATE 12-MAY-06

DRN BY

CHK BY

DES

WHS/JDB

A PORTER

WHS/JDB

TOP REC

K-0-5410034-0-01

TITLE

DECCSERVER 200

NUMBER

5410034-0-01

REV

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NOTE: THIS IS ONE OF TWO PAGES WHICH  
DEFINES THE CONTENTS OF EACH  
BUNDLE USED IN THE PRINT SET  
FOR THE 5418034 MODULE

BUNDLE NAME                      CONTENTS OF BUNDLE

ADDRESS_BBUS	A01_H, A02_H, A03_H, A04_H & A05_H
ADDRESS_BUS	BUS_A01_H ..... BUS_A20_H
ADDRESS_DECODE	DATA_ACK_L, PAL_STATE_2_L, DUART0_SEL_L, DUART1_SEL_L, DUART2_SEL_L, DUART3_SEL_L, MODEM_SEL_L, LANCE_SEL_L, ROM_SEL_L, ADR_PROM_SEL_L, BBUS_ENA_L, EEPROM_SEL_L, CONFIG_LAT_L & DECODE_ENA_L
BDATA_BUS	BBUS_D00_H ..... BBUS_D15_H
CD	CD0_L, CD1_L, CD2_L, CD3_L, CD4_L, CD5_L, CD6_L & CD7_L
CPU_LAN_CNTRL	UDS_L, LDS_L & WRT_L
CPU_MASTR_CNTRL	BR_L & BBACK_L
DATA_BUS	BUS_D00_H ..... BUS_D15_H
DIAG_BUS	SYNC_H & BIT1_H ..... BIT7_H
DSR	DSR0_L ..... DSR7_L
DSRS	DSRS0_L ..... DRS7_L
DTR	DTR0_L ..... DTR7_L
EIA_TIMER_INT	RXINT0_L ..... RXINT7_L, TXINT0_L ..... TXINT7_L, REFRESH_TIM_L, GENERAL_TIM_L & CTS_PROC_INTR_L
INTR_CNTRL	IPL0_L, IPL1_L & IPL2_L
LAN_ADDRESS_BUS	A16_H ..... A20_H
LAN_MASTR_CNTRL	LAN_HLD_ACK_L & LAN_HLD_ACK_H
LAN_SIA_INTRCON	LAN_TCLK_H, LAN_RENA_H, LAN_CLSN_H, LAN_RXD_H & LAN_RCLK_H
LANCE_CNTRL	LAN_ACK_L & ADR_ENA_H
LANCE_MEM_CNTRL	LANCE_INT_L, DAS_L, ALE_H, LAN_REQ_H & LAN_HLD_L
LED	POWER_LED_L, DIAG_LED_L, SOFT_LED_L & CARR_LED_L
MEM_ADDRESS_BUS	MA1_L ..... MA0_L
MEM_BANK_SEL	BANK0_SEL_L, BANK1_SEL_L, BANK0_WRT_L & BANK1_WRT_L
MEM_BANK_SEL1	BANK2_SEL_L, BANK3_SEL_L, BANK2_WRT_L & BANK3_WRT_L
MEMORY_CNTRL	BUDS_L, BLDS_L, BURT_L, ASTRB_L, BASTRB_L, BASTRB_H, BUDS_H, BLDS_H, INTR_ACK_L & B_ENA_L
MOD_IN	P_CTS0_L ..... P_CTS7_L, P_R10_L ..... P_R17_L, P_DSR_L ..... P_DSR7_L, P_SMI0_L ..... P_SMI7_L & P_CD0_L ..... P_CD7_L
MOD_OUT	P_RTS0_L ..... P_RTS7_L, P_DTR0_L ..... P_DTR7_L, P_DSRS0_L ..... P_DSRS7_L & 2VREF_H
MOD_PROC_INTR	DSR_PROC_INTR_L, CD_PROC_INTR_L, RI_PROC_INTR_L & SMI_PROC_INTR_L
MODEM_IN	P_R10_L ..... P_R17_L, P_SMI0_L ..... P_SMI7_L, P_CD0_L ..... P_CD7_L & P_CTS0_L ..... P_CTS7_L
MODEM_OUT	P_DSRS0_L ..... P_DSRS7_L, P_DTR0_L ..... P_DTR7_L & P_RTS0_L ..... P_RTS7_L

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PAGE 1 OF 1

DRN BY	CMS/JDD	DATE 12-MAY-86
CHK BY	A PARIKH	DATE 12-MAY-86
ENG	DJG/UCH	DATE 12-MAY-86
TC2 DEC	R-00-5418034-0-0	

TITLE    DECSERVER 200

NUMBER	5418034-0-02
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NOTE: CONTINUATION OF FIRST PAGE WHICH  
DEFINES THE CONTENTS OF EACH  
DUNDLE USED IN THE PRINT SET  
FOR THE 5418034 MODULE

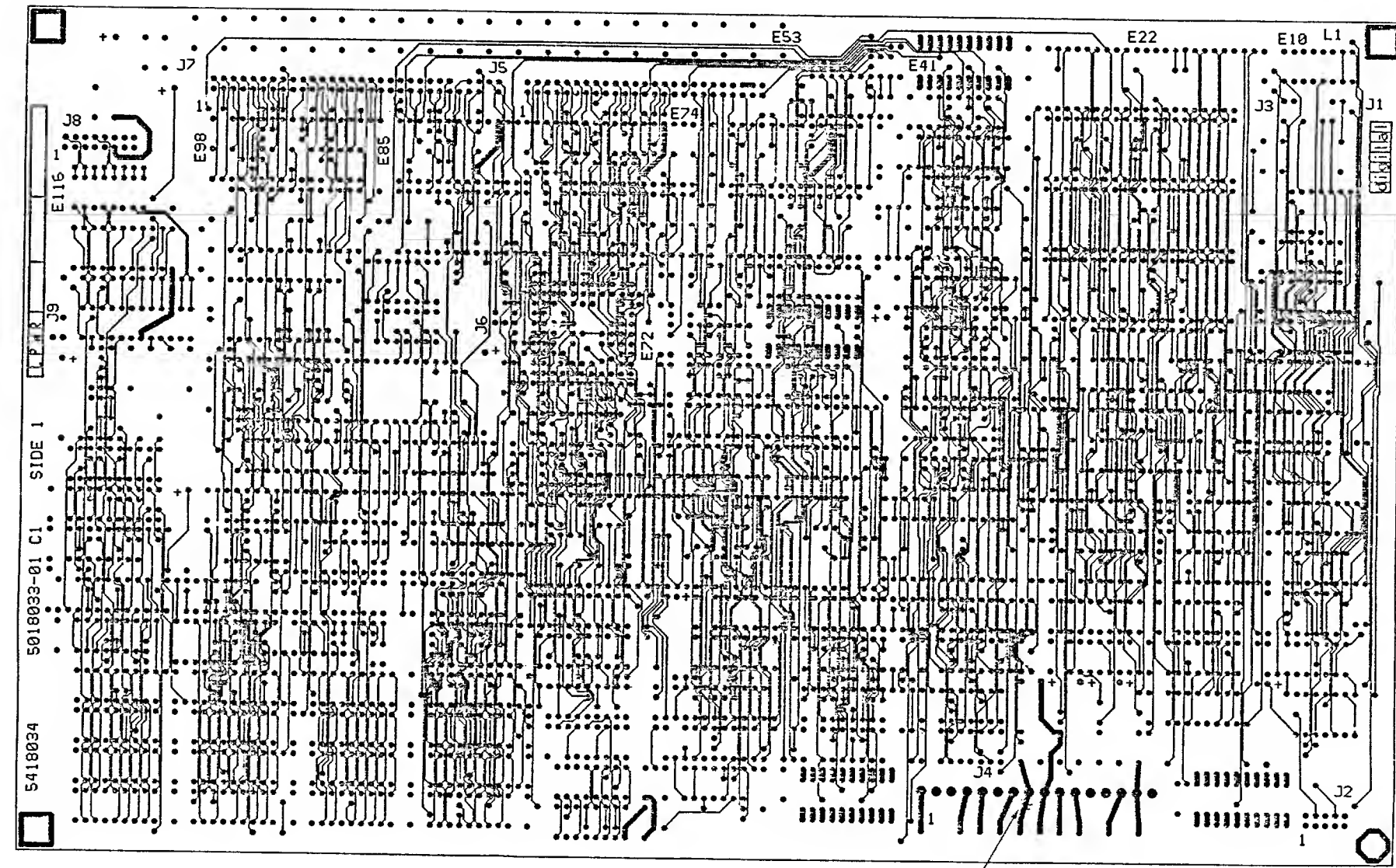
BUNDLE NAME	CONTENTS OF DUNDLE
PARITY_CNTRL	EEPROM_WRT_H,SET_LBT_PAR_L,SET_HBT_PAR_L & PAR_DIS_L
RAM_CNTRL	HBT_RAS_L,HBT_CAS_L,LBT_RAS_L,LBT_CAS_L & RAM_SEL_L
RCVDATA	RX0_L ..... RX7_L
RESET	CLR_L,WARM_CLR_L,WARM_START_L,WARM_CLR_H & EEPROM_CLR_L
RESET_CNTRL	RESET_DIS_H,WARM_STRT_CLR_H,RAM_ENA_L,TST_MOD_H & POWER_FAIL_L
RI	RI0_L ..... RI7_L
RTS	RTS0_L ..... RTS7_L
RXD	RXD0_L ..... RXD7_L
SIA_ARRAY_BUS	SIA_RXD_H,SIA_RCLK_H,SIA_RENA_H & SIA_CLSN_H
SMI	SMI0_L ..... SMI7_L
SYS_CLKS	DUART_CLK_H,CPU_CLK_H,CPU_CLK_L & TIMER_CLK_H
THREE_V	+3VA,+3VB & +3VC
TXD	TXD0_L ..... TXD7_L
XMTDATA	TX0_L ..... TX7_L



SCALE: 2.00 PEN: 0 BENSOW V3.0

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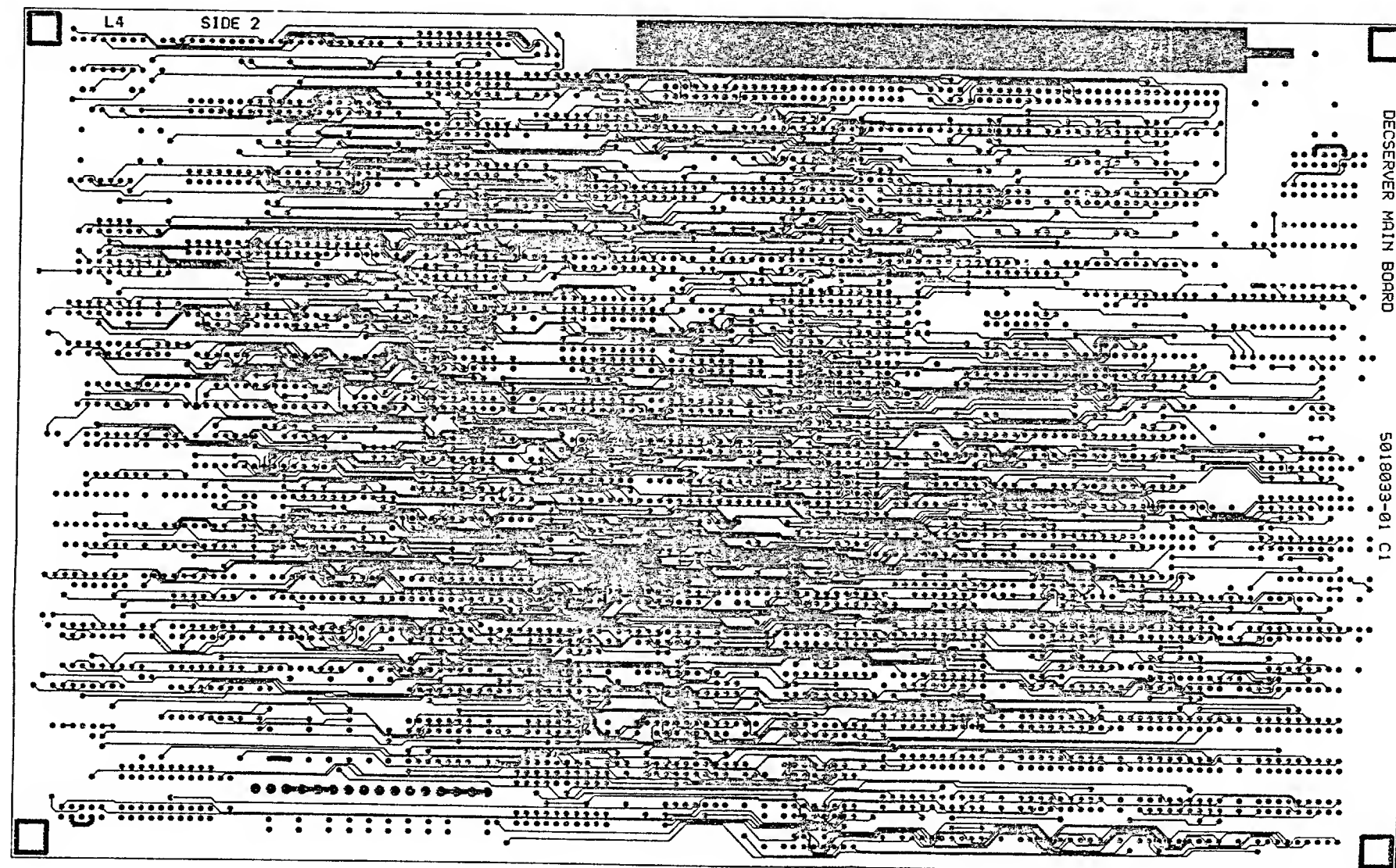


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CHK	DRAWN BY	ENGINEER	CHANGE NO	DATE	REV

DRAWN	DATE	TITLE
CHECKED	DATE	digital
DES. ENG.	DATE	ETCH CUT DRAWING
RESP. ENG.	DATE	
MFG. ENG.	DATE	

DOCUMENT NUMBER	133207
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TITLE  
ETCH CUT DRAWING

DOCUMENT NUMBER		REV
SIZE/COOP	NUMBER	
E	EC 5018033-0-0	A
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REWORK INSTRUCTIONS  
ETCH CUTS - SIDE 1  
0-1 AT J4-8

REVISION HISTORY		
REV	ECO NUMBER	DATE

DAQ 137C  
EN-4124-16 REV A (396)

TITLE  
ETCH CUT DRAWING

DOCUMENT NUMBER		
SIZE CODE	NUMBER	REV.
DEC	5018033-0-0	A
SCALE —		SHEET 3 OF 3